

GENERAL DESCRIPTION

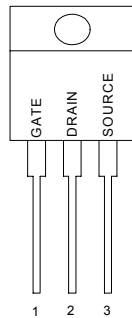
This Power MOSFET is designed to withstand high energy in avalanche and commutation modes. The new energy efficient design also offers a drain-to-source diode with a fast recovery time. Designed for high voltage, high speed switching applications in power supplies, converters and PWM motor controls, these devices are particularly well suited for bridge circuits where diode speed and commutating safe operating areas are critical and offer additional and safety margin against unexpected voltage transients.

FEATURES

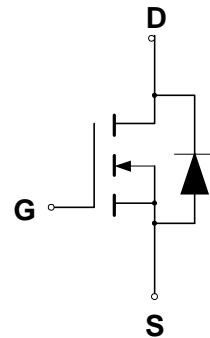
- ◆ Robust High Voltage Termination
- ◆ Avalanche Energy Specified
- ◆ Source-to-Drain Diode Recovery Time Comparable to a Discrete Fast Recovery Diode
- ◆ Diode is Characterized for Use in Bridge Circuits
- ◆ I_{BSS} and $V_{DS(on)}$ Specified at Elevated Temperature

PIN CONFIGURATION

TO-220
Front View



SYMBOL



N-Channel MOSFET

ORDERING INFORMATION

Part Number	Package
CMT20N15N220	TO-220

ABSOLUTE MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain to Current – Continuous	I_D	20	A
– Pulsed	I_{DM}	60	
Gate-to-Source Voltage – Continue	V_{GS}	± 20	V
– Non-repetitive	V_{GSM}	± 32	V
Total Power Dissipation	P_D	112	W
Derate above 25°C		0.9	W/°C
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to 150	°C
Single Pulse Drain-to-Source Avalanche Energy – $T_J = 25^\circ\text{C}$ ($V_{DD} = 100\text{V}, V_{GS} = 10\text{V}, I_L = 16\text{A}, L = 1.38\text{mH}, R_G = 25\Omega$)	E_{AS}	177	mJ
Thermal Resistance – Junction to Case	θ_{JC}	1.1	°C/W
– Junction to Ambient	θ_{JA}	62.5	
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds	T_L	260	°C

ELECTRICAL CHARACTERISTICS

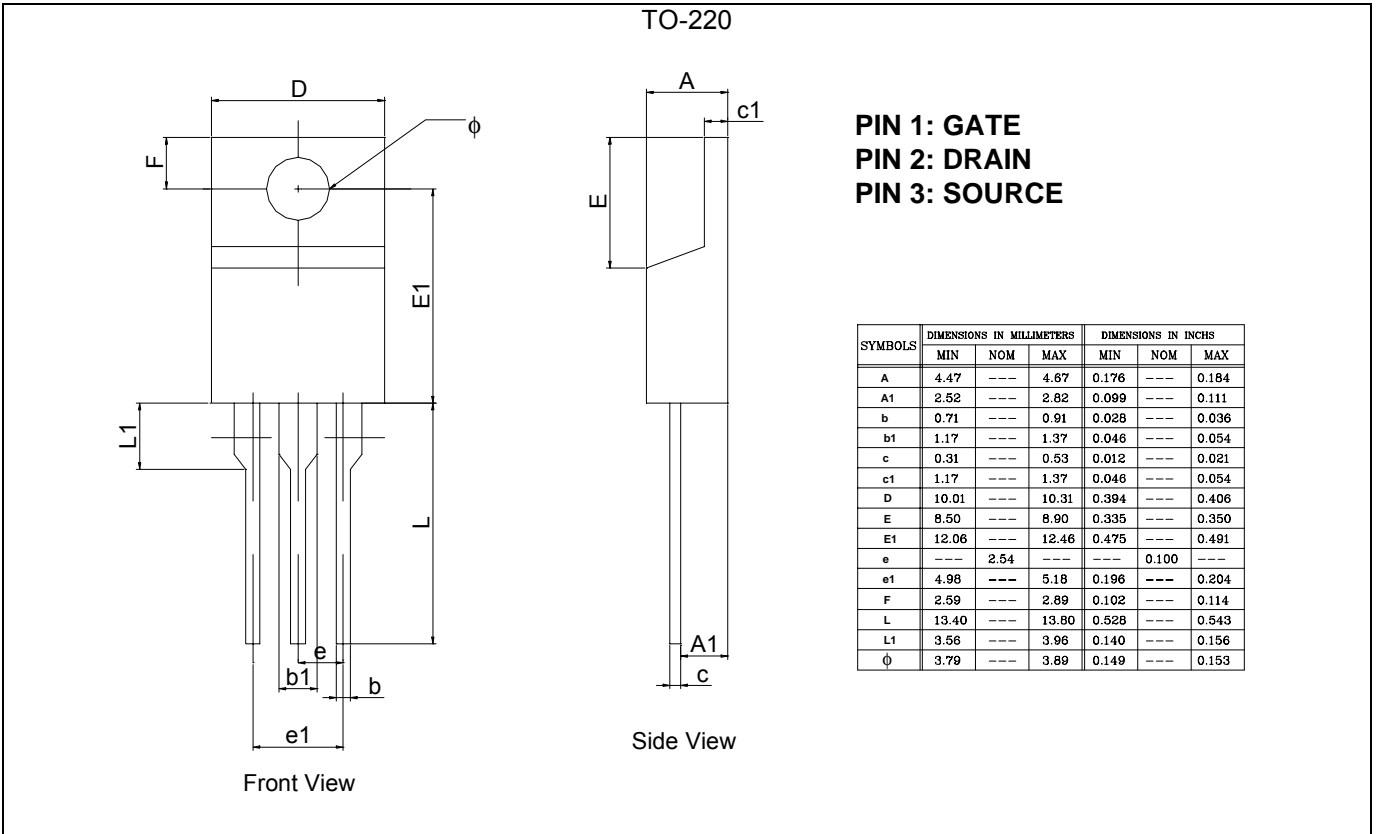
Unless otherwise specified, $T_J = 25^\circ\text{C}$.

Characteristic	Symbol	CMT20N15			Units	
		Min	Typ	Max		
Drain-Source Breakdown Voltage ($V_{GS} = 0\text{ V}$, $I_D = 250\ \mu\text{A}$)	$V_{(BR)DSS}$	150			V	
Drain-Source Leakage Current ($V_{DS} = 150\text{ V}$, $V_{GS} = 0\text{ V}$) ($V_{DS} = 150\text{ V}$, $V_{GS} = 0\text{ V}$, $T_J = 125^\circ\text{C}$)	I_{DSS}			25 100	μA	
Gate-Source Leakage Current-Forward ($V_{gsf} = 20\text{ V}$, $V_{DS} = 0\text{ V}$)	I_{GSSF}			100	nA	
Gate-Source Leakage Current-Reverse ($V_{gsr} = 20\text{ V}$, $V_{DS} = 0\text{ V}$)	I_{GSSR}			100	nA	
Gate Threshold Voltage ($V_{DS} = V_{GS}$, $I_D = 250\ \mu\text{A}$)	$V_{GS(th)}$	2.0		4.0	V	
Static Drain-Source On-Resistance ($V_{GS} = 10\text{ V}$, $I_D = 10\text{A}$) *	$R_{DS(on)}$			0.13	Ω	
Drain-Source On-Voltage ($V_{GS} = 10\text{ V}$) ($I_D = 10.0\text{ A}$)	$V_{DS(on)}$			2.8	V	
Forward Transconductance ($V_{DS} = 50\text{ V}$, $I_D = 10\text{A}$) *	g_{FS}	4.2			mhos	
Input Capacitance	$(V_{DS} = 25\text{ V}$, $V_{GS} = 0\text{ V}$, $f = 1.0\text{ MHz}$)	C_{ISS}		1133	1627	pF
Output Capacitance		C_{OSS}		332	474	pF
Reverse Transfer Capacitance		C_{RSS}		105	174	pF
Turn-On Delay Time	$(V_{DD} = 75\text{ V}$, $I_D = 20\text{ A}$, $V_{GS} = 10\text{ V}$, $R_G = 9.1\Omega$) *	$t_{d(on)}$		11	25	ns
Rise Time		t_r		77	153	ns
Turn-Off Delay Time		$t_{d(off)}$		33	67	ns
Fall Time		t_f		49	97	ns
Total Gate Charge	$(V_{DS} = 120\text{ V}$, $I_D = 20\text{ A}$, $V_{GS} = 10\text{ V}$) *	Q_g		39.1	55.9	nC
Gate-Source Charge		Q_{gs}		7.5		nC
Gate-Drain Charge		Q_{gd}		22		nC
Internal Drain Inductance (Measured from the drain lead 0.25" from package to center of die)	L_D			4.5		nH
Internal Drain Inductance (Measured from the source lead 0.25" from package to source bond pad)	L_S			7.5		nH
SOURCE-DRAIN DIODE CHARACTERISTICS						
Forward On-Voltage(1)	$(I_S = 20\text{ A}$, $V_{GS} = 0\text{ V}$, $d_I/d_t = 100\text{A}/\mu\text{s}$)	V_{SD}			1.5	V
Forward Turn-On Time		t_{on}		**		ns
Reverse Recovery Time		t_{rr}			160	

* Pulse Test: Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$

** Negligible, Dominated by circuit inductance

PACKAGE DIMENSION



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