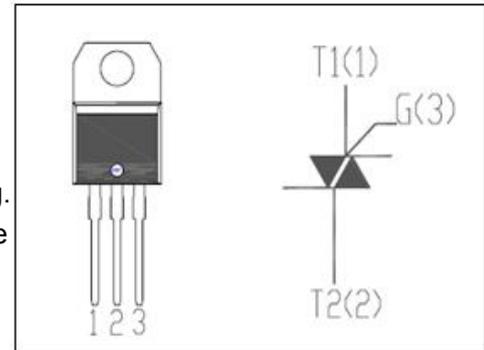


FEATURES

- With TO-220AB non insulated package
- Suitable for general purpose applications where gate high sensitivity is required. Application on 4Q such as phase control and static switching.
- Minimum Lot-to-Lot variations for robust device performance and reliable operation


ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

SYMBOL	PARAMETER	MIN	UNIT
V _{DRM}	Repetitive peak off-state voltage	800	V
V _{RRM}	Repetitive peak off-state voltage	800	V
I _{T(RMS)}	RMS on-state current (full sine wave) T _j =95°C	4	A
I _{TSM}	Non-repetitive peak on-state current t _p =10ms	40	A
T _j	Operating junction temperature	110	°C
T _{stg}	Storage temperature	-40~150	°C
R _{th(j-c)}	Thermal resistance, junction to case	2.4	°C/W
R _{th(j-a)}	Thermal resistance, junction to ambient	60	°C/W

ELECTRICAL CHARACTERISTICS (T_c=25°C unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS	MAX	UNIT	
I _{RRM}	Repetitive peak reverse current	V _R =V _{RRM} , V _R =V _{RRM} , T _j =110°C	0.01 0.75	mA	
I _{DRM}	Repetitive peak off-state current	V _D =V _{DRM} , V _D =V _{DRM} , T _j =110°C	0.01 0.75	mA	
I _{GT}	Gate trigger current	V _D =12V; R _L = 30 Ω	I	10	mA
			II	10	
			III	10	
			IV	25	
I _H	Holding current	I _{GT} = 0.1A, Gate Open	25	mA	
V _{GT}	Gate trigger voltage all quadrant	V _D =12V; R _L = 30 Ω	1.5	V	
V _{TM}	On-state voltage	I _T = 5.5A ; t _p = 380 μs	1.65	V	

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