



UT15P04

Power MOSFET

-40V, -15A P-CHANNEL POWER MOSFET

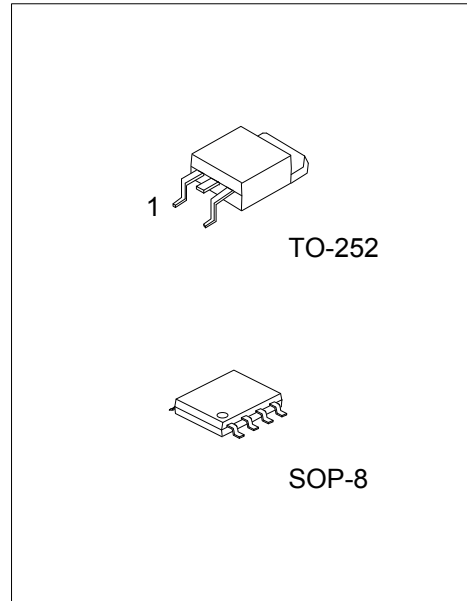
DESCRIPTION

The UTC **UT15P04** is a P-channel power MOSFET using UTC's advanced technology to provide the customers with high switching speed and a minimum on-state resistance, and it can also withstand high energy in the avalanche.

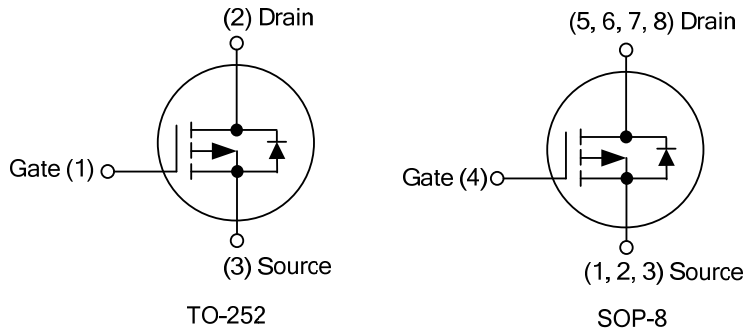
This UTC **UT15P04** is suitable for motor drivers, high-side switch and 12V board net, etc.

FEATURES

- * $R_{DS(ON)} \leq 64 \text{ m}\Omega @ V_{GS}=-10\text{V}, I_D=-7.5\text{A}$
- $R_{DS(ON)} \leq 100 \text{ m}\Omega @ V_{GS}=-10\text{V}, I_D=-7.5\text{A}$
- * High Switching Speed



SYMBOL



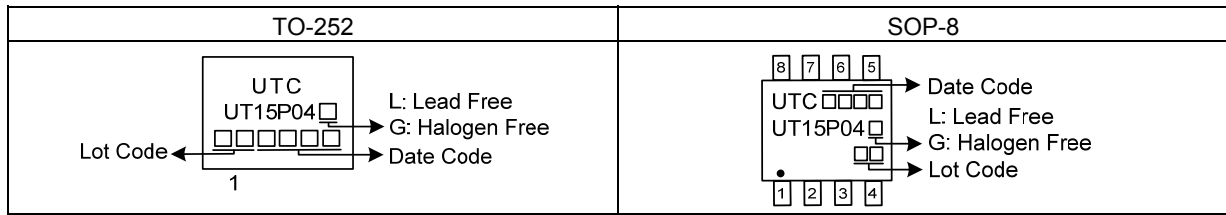
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment								Packing
Lead Free	Halogen Free		1	2	3	4	5	6	7	8	
UT15P04L-TN3-R	UT15P04G-TN3-R	TO-252	G	D	S	-	-	-	-	-	Tape Reel
UT15P04L-S08-R	UT15P04G-S08-R	SOP-8	S	S	S	G	D	D	D	D	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>UT15P04G-TN3-R</p> <ul style="list-style-type: none"> (1) Packing Type (2) Package Type (3) Green Package 	<ul style="list-style-type: none"> (1) R: Tape Reel (2) TN3: TO-252, S08: SOP-8 (3) G: Halogen Free and Lead Free, L: Lead Free
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■ MARKING



■ ABSOLUTE MAXIMUM RATINGS ($T_C=25^{\circ}\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	-40	V
Gate-Source Voltage		V_{GSS}	± 20	V
Drain Current	Continuous	I_D	-15	A
	Pulsed	I_{DM}	-30	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	18.2	mJ
Power Dissipation	TO-252	P_D	32	W
	SOP-8		1.2	W
Junction Temperature		T_J	-55 ~ +150	$^{\circ}\text{C}$
Storage Temperature		T_{STG}	-55 ~ +150	$^{\circ}\text{C}$

- Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
 2. Repetitive Rating: Pulse width limited by maximum junction temperature.
 3. $L = 0.1\text{mH}$, $I_{AS} = 19.1\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 25\ \Omega$, Starting $T_J = 25^{\circ}\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-252	θ_{JA}	50	$^{\circ}\text{C}/\text{W}$
	SOP-8		125	$^{\circ}\text{C}/\text{W}$
Junction to Case	TO-252	θ_{JC}	3.9	$^{\circ}\text{C}/\text{W}$
	SOP-8		104	$^{\circ}\text{C}/\text{W}$

Note: Device mounted on FR-4 substrate P_C board, 2oz copper, with 1inch square copper plate.

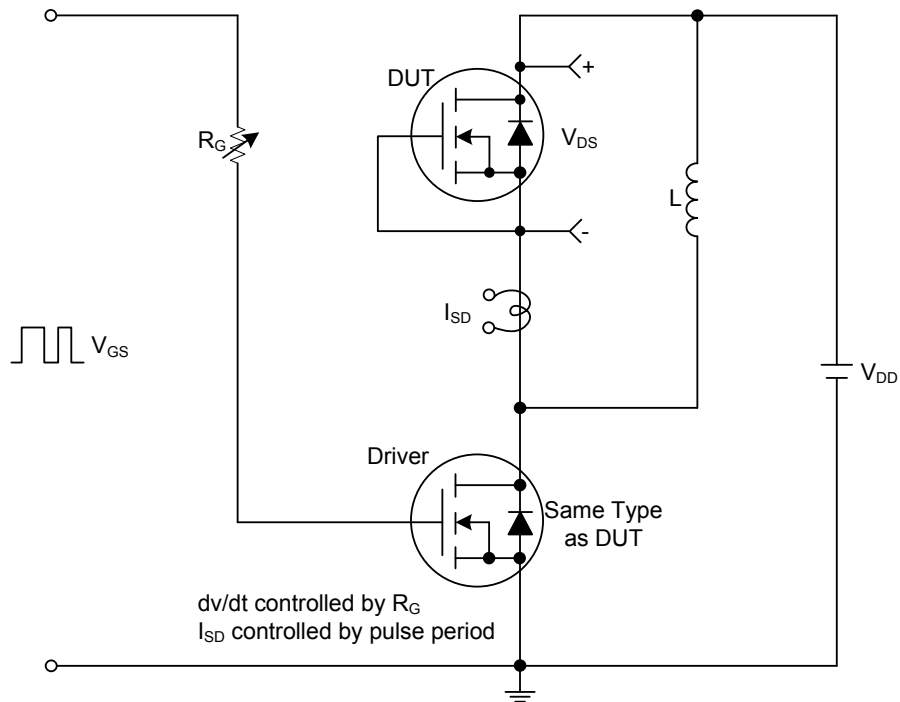
■ ELECTRICAL CHARACTERISTICS (T_J=25°C, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	I _D =-250μA, V _{GS} =0V	-40			V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =-40V, V _{GS} =0V			-1	μA
Gate- Source Leakage Current	Forward	V _{GS} =+20V, V _{DS} =0V			+100	nA
	Reverse	V _{GS} =-20V, V _{DS} =0V			-100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _D =-250μA	-1.0		-3.0	V
Static Drain-Source On-State Resistance (Note 1)	R _{DS(ON)}	V _{GS} =-10V, I _D =-7.5A			64	mΩ
		V _{GS} =-4.5V, I _D =-7.5A			100	mΩ
DYNAMIC PARAMETERS						
Input Capacitance	C _{ISS}	V _{DS} =-25V, V _{GS} =0V, f=1MHz		679		pF
Output Capacitance	C _{OSS}			77.2		pF
Reverse Transfer Capacitance	C _{RSS}			67.5		pF
SWITCHING PARAMETERS						
Total Gate Charge	Q _G	V _{DS} =-32V, V _{GS} =-10V, I _D =-15A (Note 1, 2)		18		nC
Gate to Source Charge	Q _{GS}			2.5		nC
Gate to Drain Charge	Q _{GD}			3.7		nC
Turn-ON Delay Time	t _{D(ON)}	V _{DS} =-20V, V _{GS} =-10V, I _D =-15A, R _G =3Ω (Note 1, 2)		5.6		ns
Rise Time	t _R			16.8		ns
Turn-OFF Delay Time	t _{D(OFF)}			22.8		ns
Fall-Time	t _F			18.8		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Continuous Drain-Source Diode Forward Current	I _S				-15	A
Maximum Pulsed Drain-Source Diode Forward Current	I _{SM}				-30	A
Drain-Source Diode Forward Voltage (Note 1)	V _{SD}	I _S =-15A, V _{GS} =0V			1.4	V

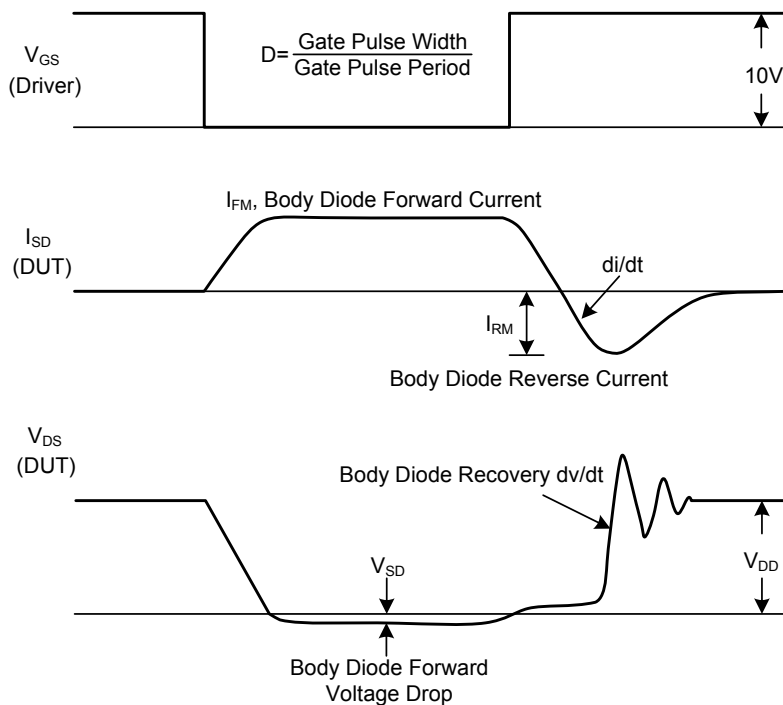
Notes: 1. Pulse Test : Pulse width ≤ 300μs, Duty cycle ≤ 2%.

2. Essentially independent of operating temperature.

TEST CIRCUITS AND WAVEFORMS



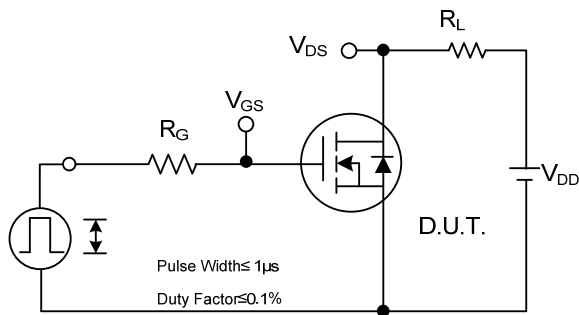
Peak Diode Recovery dv/dt Test Circuit



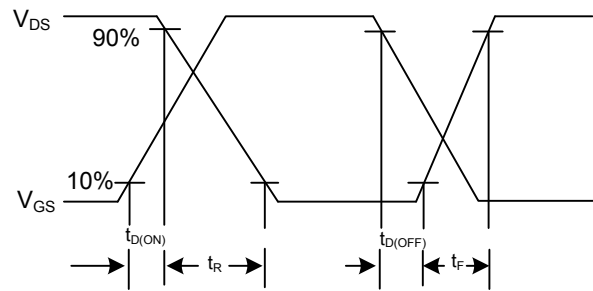
Peak Diode Recovery dv/dt Test Circuit and Waveforms

Peak Diode Recovery dv/dt Waveforms

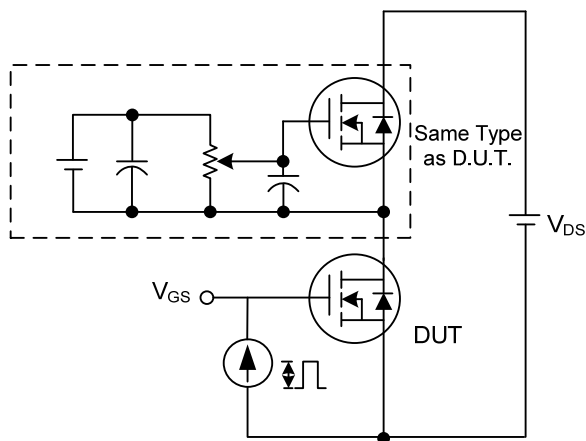
TEST CIRCUITS AND WAVEFORMS



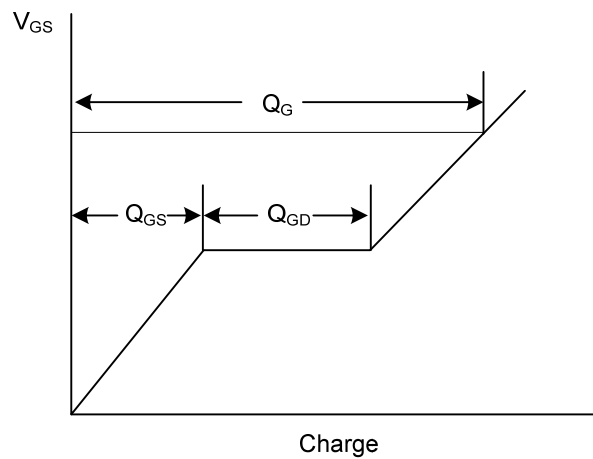
Switching Test Circuit



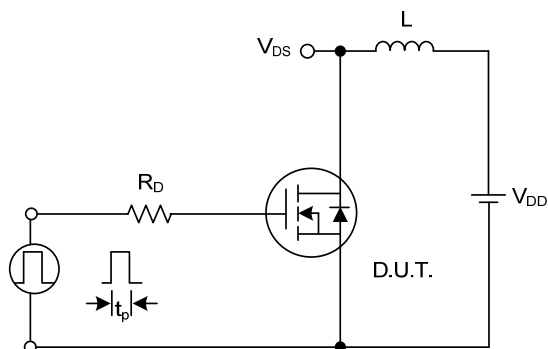
Switching Waveforms



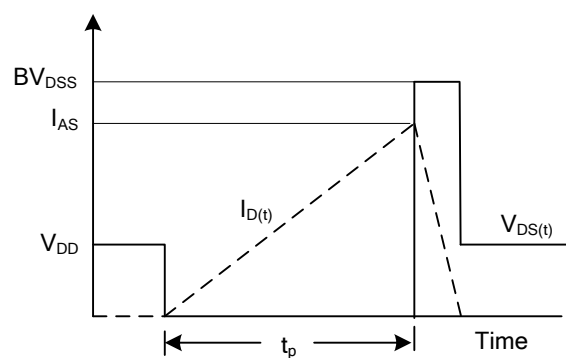
Gate Charge Test Circuit



Gate Charge Waveform

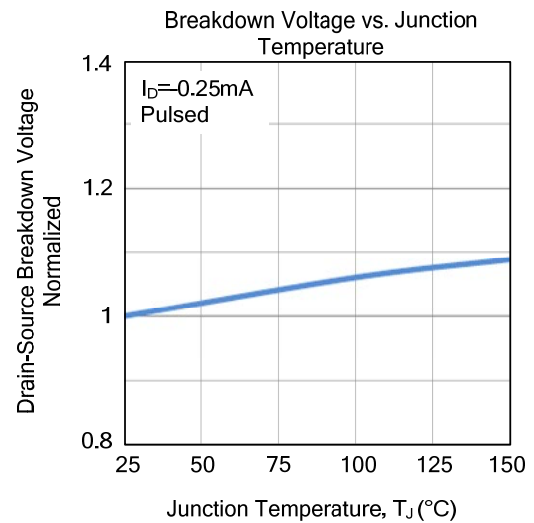
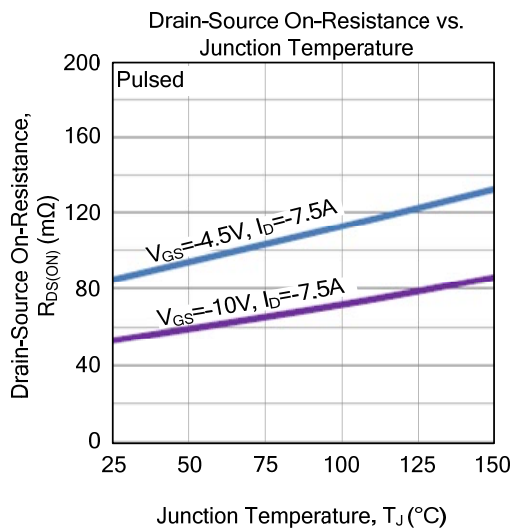
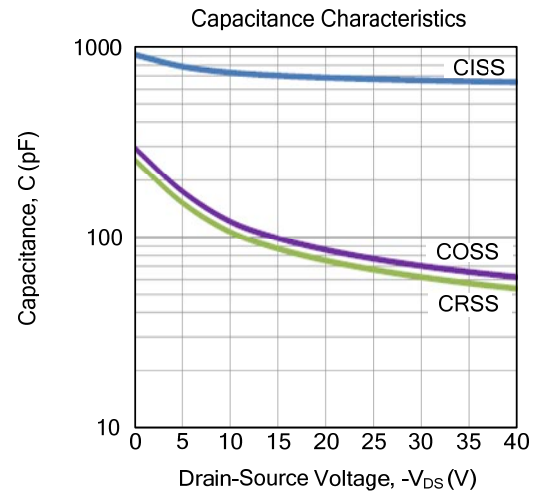
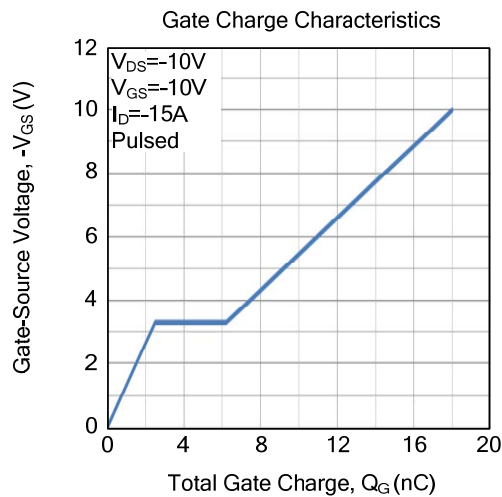
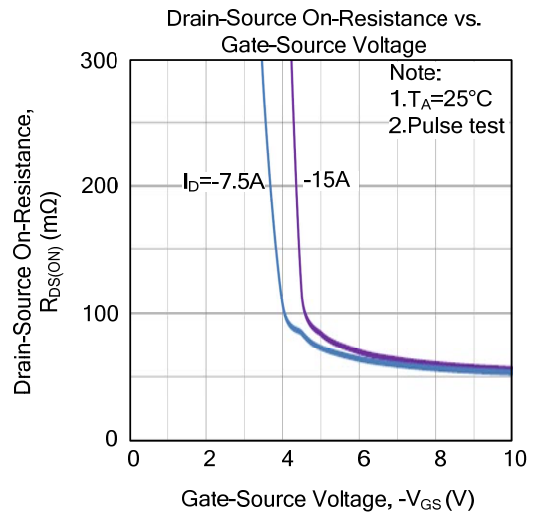
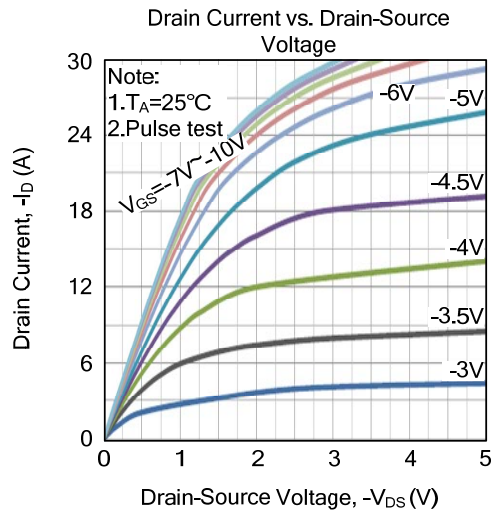


Unclamped Inductive Switching Test Circuit

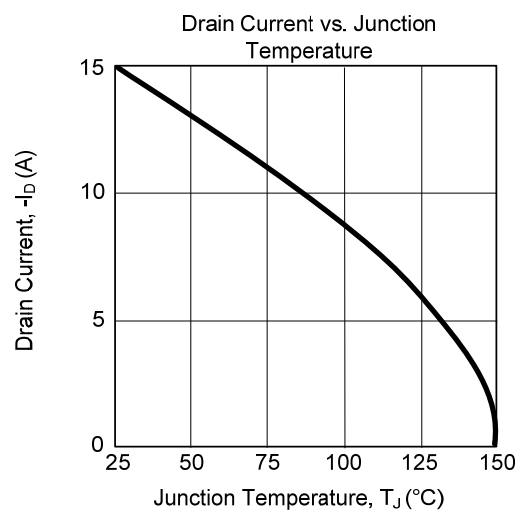
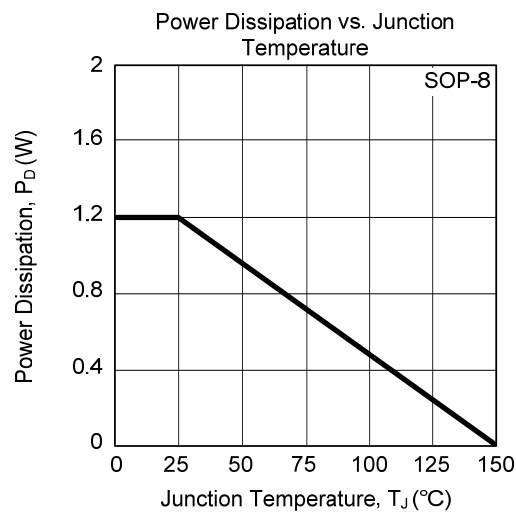
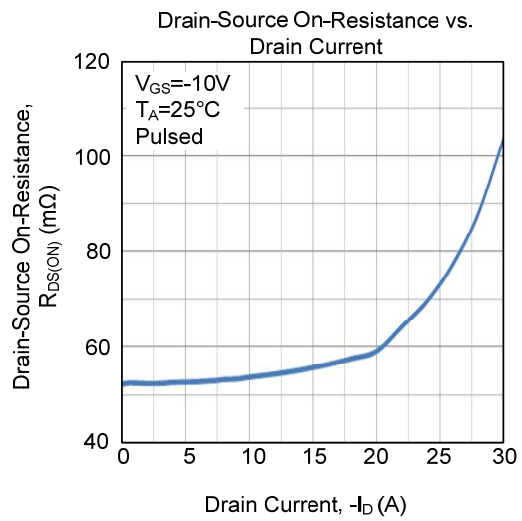
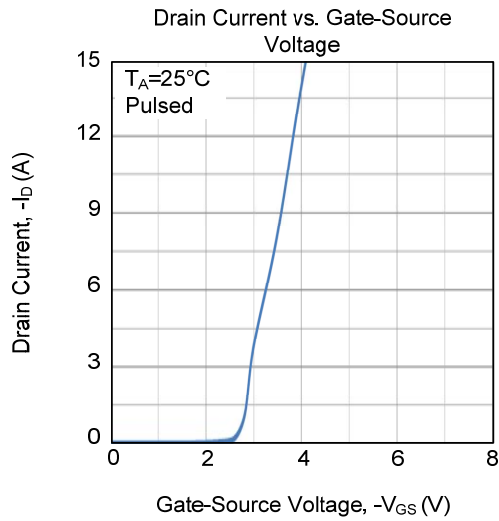
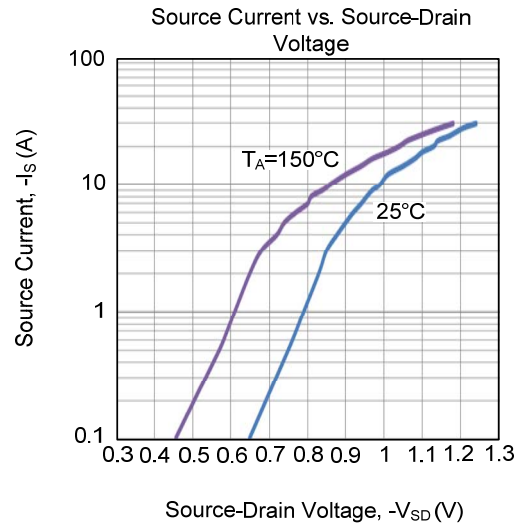
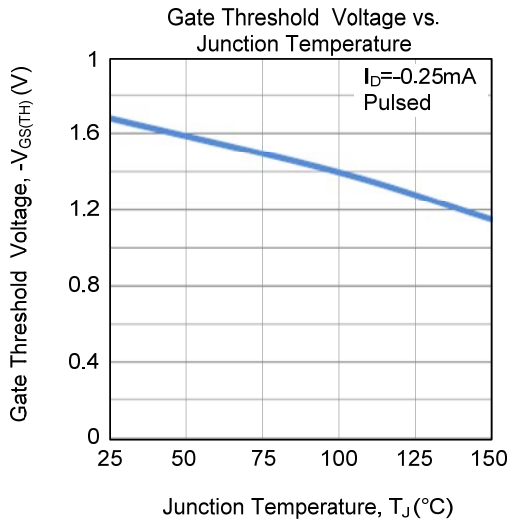


Unclamped Inductive Switching Waveforms

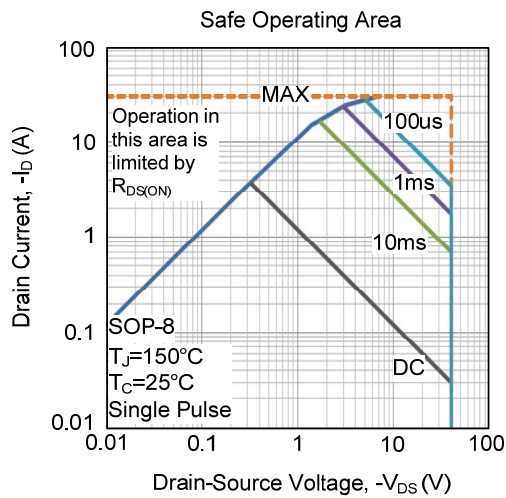
TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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