

RoHS Compliant Product
A suffix of "-C" specifies and halogen free

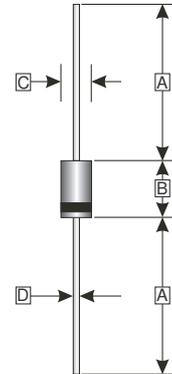
FEATURES

- Fast switching for high efficiency
- Low forward voltage drop
- High current capability
- Low reverse leakage current
- High surge current capability

MECHANICAL DATA

- Case : Molded plastic DO-27
- Epoxy : UL 94V-0 rate flame retardant
- Terminals : Solderable per MIL-STD-202 method 208
- Polarity : Color band denotes cathode
- Mounting position : Any
- Weight : 1.1 grams

DO-27



REF.	Millimeter	
	Min.	Max.
A	25.4 (TYP)	
B	7.20	9.50
C	4.80	5.60
D	1.10	1.30

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

(Rating at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load.
For capacitive load, de-rate current by 20%.)

Parameter	Symbol	Rating	Unit
Maximum Recurrent Peak Reverse Voltage	V_{RRM}	400	V
Maximum RMS Voltage	V_{RMS}	280	V
Maximum DC Blocking Voltage	V_{DC}	400	V
Maximum Average Forward Rectified Current $T_L=55^\circ\text{C}$	$I_{F(AV)}$	5	A
Peak Forward Surge Current, 8.3ms single Half sine-wave superimposed on rated load (JEDEC method)	I_{FSM}	125	A
Maximum Instantaneous Forward Voltage @ 5A	V_F	1.25	V
Maximum DC Reverse Current At Rated DC Blocking Voltage	I_R	$T_J=25^\circ\text{C}$	5
		$T_J=125^\circ\text{C}$	150
Maximum Reverse Recovery Time ¹	T_{RR}	25	nS
Typical Junction Capacitance ³	C_J	80	pF
Typical Thermal Resistance ²	$R_{\theta JL}$	40	$^\circ\text{C/W}$
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55~150	$^\circ\text{C}$

Notes :

1. Reverse recovery test conditions $I_F=0.5\text{A}$, $I_R=1.0\text{A}$, $I_{RR}=0.25\text{A}$.
2. Thermal Resistance junction to lead.
3. Measured at 1.0 MHz and applied reverse voltage of 4.0 Volts DC.

RATINGS AND CHARACTERISTICS CURVE

FIG.1 - FORWARD CURRENT DERATING CURVE

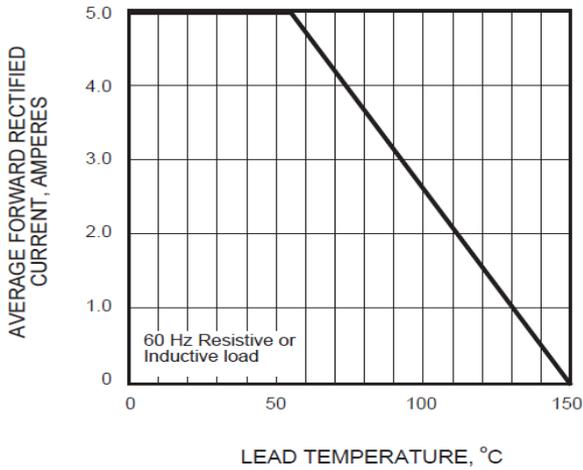


FIG.2 - MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT

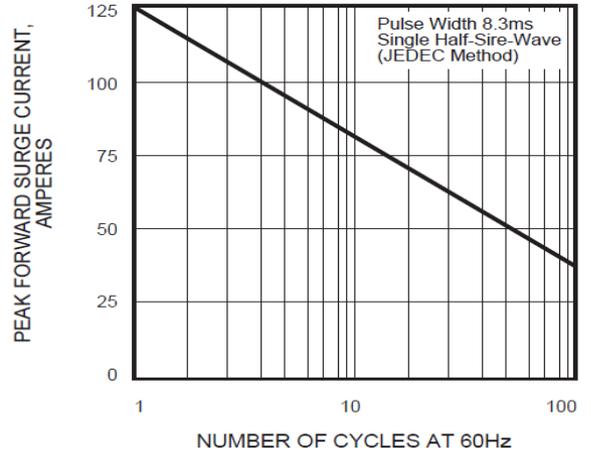


FIG.3 - TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

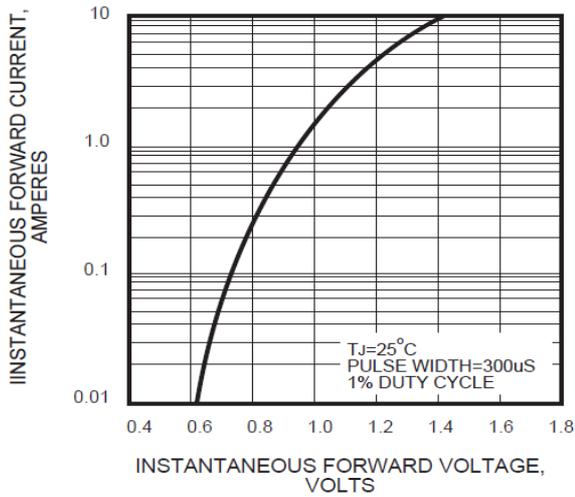


FIG.4 - TYPICAL REVERSE CHARACTERISTICS

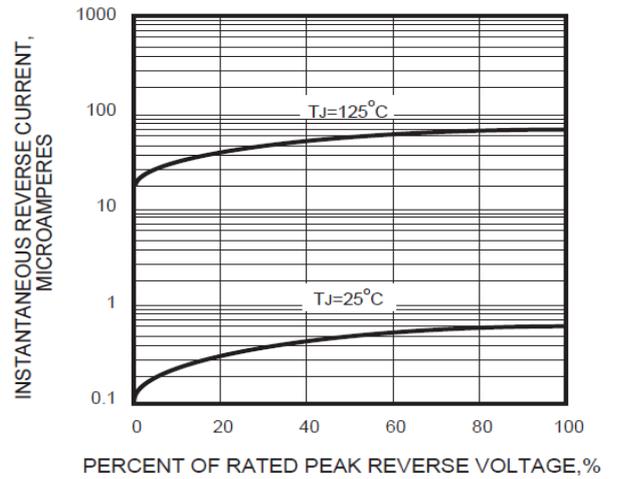


FIG.5 - TYPICAL JUNCTION CAPACITANCE

