

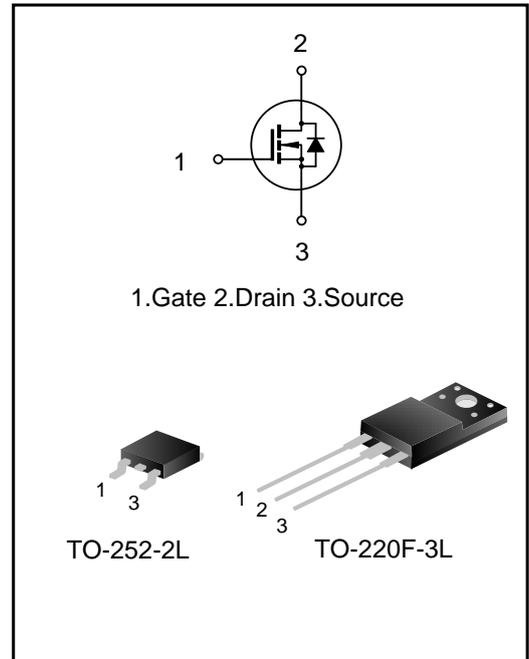
5A, 650V SUPER JUNCTION MOS POWER TRANSISTOR

DESCRIPTION

STS65R900D(F)S2 is an N-channel enhancement mode high voltage power MOSFETs produced using Silan's super junction MOS technology. It achieves low conduction loss and switching losses. It leads the design engineers to their power converters with high efficiency, high power density, and superior thermal behavior. Furthermore, it's universal applicable, i.e., suitable for hard and soft switching topologies.

FEATURES

- ◆ 5A, 650V, $R_{DS(on)(typ.)}=0.75\Omega@V_{GS}=10V$
- ◆ New revolutionary high voltage technology
- ◆ Ultra low gate charge
- ◆ Periodic avalanche rated
- ◆ Extreme dv/dt rated
- ◆ High peak current capability
- ◆ 100% avalanche tested
- ◆ Pb-free lead plating
- ◆ RoHS compliant



KEY PERFORMANCE PARAMETERS

Characteristics	Ratings	Unit
V_{DS}	650	V
$V_{GS(th)}$	2.5~4.5	V
$R_{DS(on),max.}$	0.9	Ω
$I_{D,pulse}$	20	A
$Q_{g,typ.}$	10	nC

ORDERING INFORMATION

Part No.	Package	Marking	Hazardous Substance Control	Packing Type
STS65R900DS2TR	TO-252-2L	65R90DS2	Halogen free	Tape & Reel
STS65R900FS2	TO-220F-3L	65R900FS2	Halogen free	Tube

ABSOLUTE MAXIMUM RATINGS (UNLESS OTHERWISE NOTED, T_A=25°C)

Characteristics	Symbol	Test conditions	Ratings			Unit
			Min.	Typ.	Max.	
Drain-source Voltage	V _{DS}	--	650	--	--	V
Gate-source Voltage (Static)	V _{GS}	--	-20	--	20	V
Gate-source Voltage (Dynamic)	V _{GS}	AC(f>1Hz)	-30	--	30	V
Drain Current	I _D	T _C =25°C	--	--	5.0	A
		T _C =100°C	--	--	3.2	A
Drain Current Pulsed (Note 1)	I _{DM}	T _C =25°C	--	--	20	A
Power Dissipation(TO-252-2L) (Note 2)	P _D	T _C =25°C	--	--	37	W
Power Dissipation (TO-220F-3L) (Note 2)	P _D	T _C =25°C	--	--	24	W
Single Pulsed Avalanche Energy	E _{AS}	L=79mH, V _{DD} =100V, R _G =25Ω, starting temperature T _J =25°C	--	--	89	mJ
Single Pulsed Current	I _{AS}	--	--	--	1.4	A
Reverse Diode dv/dt	dv/dt	V _{DS} =0~400V, I _{SD} ≤I _S , T _J =25°C	--	--	50	V/ns
MOSFET dv/dt Ruggedness	dv/dt	V _{DS} =0~480V	--	--	100	V/ns
Operation Junction Temperature Range	T _J	--	-55	--	150	°C
Storage Temperature Range	T _{stg}	--	-55	--	150	°C
Continuous Diode Forward Current	I _S	T _C =25°C, integral reverse P-N junction diode in the MOSFET	--	--	5.0	A
Diode Pulse Current	I _{S,pulse}		--	--	20	A
Maximum Diode Commutation Speed	di/dt	V _{DS} =0~400V, I _{SD} ≤I _S , T _J =25°C	--	--	250	A/μs

THERMAL CHARACTERISTICS

Table 1. TO-252-2L(STS65R900DS2) Thermal characteristics

Characteristics	Symbol	Test conditions	Ratings			Unit
			Min.	Typ.	Max.	
Thermal Resistance, Junction-case, Bottom	$R_{\theta JC}$	--	--	--	3.4	°C/W
Thermal Resistance, Junction-ambient	$R_{\theta JA}$	--	--	--	62.0	°C/W
Soldering Temperature (SMD)	T_{sold}	Reflow soldering: 10 ± 1 sec, 3times Wave soldering: 10_{-0}^{+2} sec, 1time	--	--	260	°C

Table 2. TO-220F-3L(STS65R900FS2) Thermal characteristics

Characteristics	Symbol	Test conditions	Ratings			Unit
			Min.	Typ.	Max.	
Thermal Resistance, Junction-case, Bottom	$R_{\theta JC}$	--	--	--	5.2	°C/W
Thermal Resistance, Junction-ambient	$R_{\theta JA}$	--	--	--	62.5	°C/W
Soldering Temperature (in line)	T_{sold}	15_{-0}^{+2} sec, 1time	--	--	260	°C

ELECTRICAL CHARACTERISTICS (UNLESS OTHERWISE NOTED, $T_J=25^\circ\text{C}$)

Static characteristics

Characteristics	Symbol	Test conditions	Ratings			Unit
			Min.	Typ.	Max.	
Drain-source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	650	--	--	V
Drain-source Leakage Current	I_{DSS}	$V_{DS}=650V, V_{GS}=0V, T_J=25^\circ\text{C}$	--	--	1.0	μA
		$V_{DS}=650V, V_{GS}=0V, T_J=125^\circ\text{C}$	--	1.0	--	
Gate-source Leakage Current	I_{GSS}	$V_{GS}=\pm 30V, V_{DS}=0V$	--	--	± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{GS}=V_{DS}, I_D=250\mu A$	2.5	--	4.5	V
Static Drain-source On State Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=2.5A$	--	0.75	0.9	Ω
Gate Resistance	R_G	$f=1\text{MHz}$	--	3.6	--	Ω

Dynamic characteristics

Characteristics	Symbol	Test conditions	Ratings			Unit
			Min.	Typ.	Max.	
Input Capacitance	C_{iss}	$f=1\text{MHz}, V_{GS}=0V, V_{DS}=100V$	--	371	--	pF
Output Capacitance	C_{oss}		--	15	--	
Reverse Transfer Capacitance	C_{rss}		--	1.1	--	
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=380V, V_{GS}=10V, R_G=25\Omega, I_D=5.0A$ (Notes 3, 4)	--	9.7	--	ns
Turn-on Rise Time	t_r		--	25	--	
Turn-off Delay Time	$t_{d(off)}$		--	21	--	
Turn-off Fall Time	t_f		--	23	--	
Total Gate Charge	Q_g	$V_{DD}=480V, V_{GS}=10V, I_D=5.0A$ (Notes 3, 4)	--	10	--	nC
Gate-source Charge	Q_{gs}		--	3.6	--	
Gate-drain Charge	Q_{gd}		--	4.1	--	
Gate-plateau Voltage	$V_{plateau}$		--	6.9	--	V

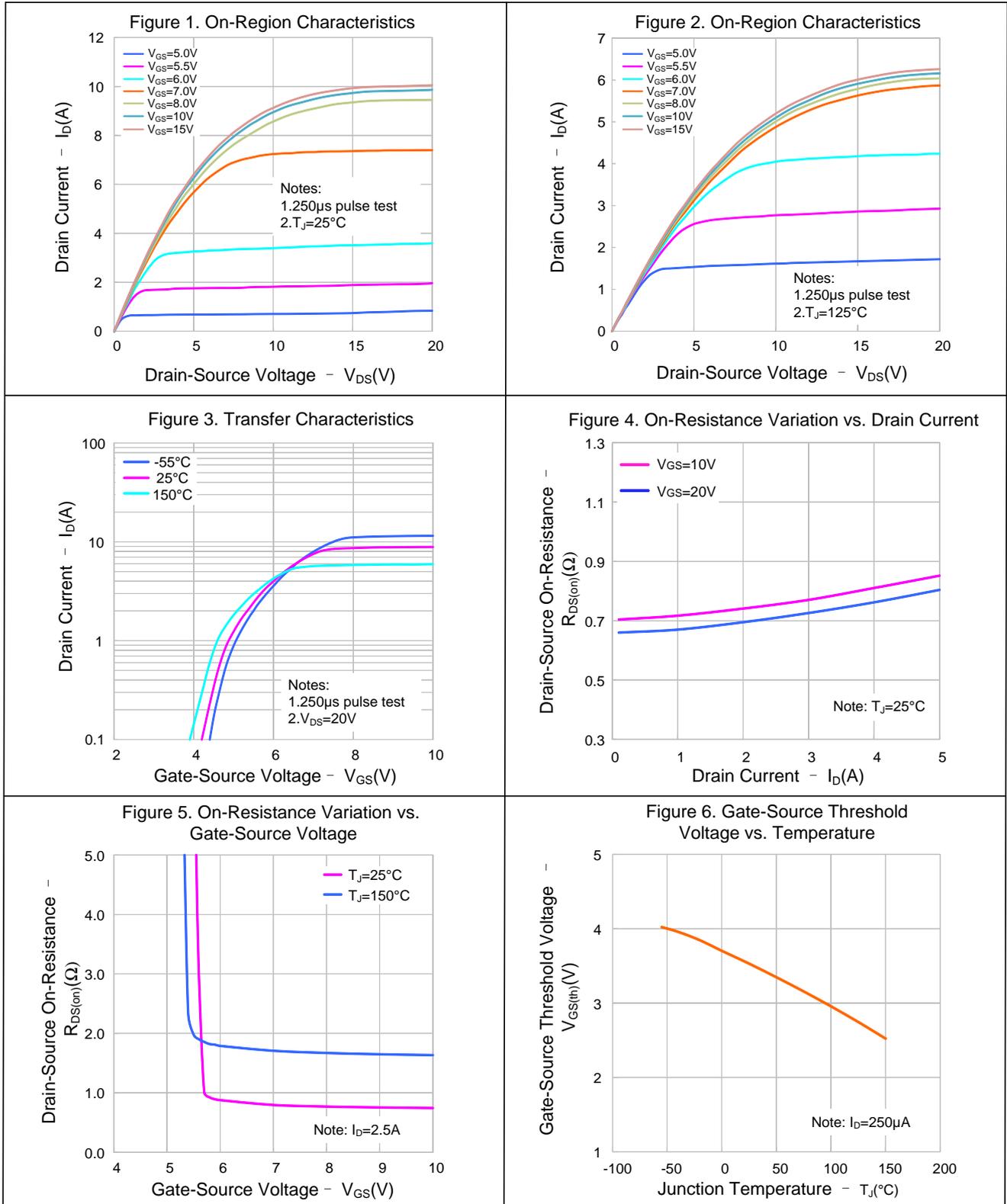
Reverse diode characteristics

Characteristics	Symbol	Test conditions	Ratings			Unit
			Min.	Typ.	Max.	
Diode Forward Voltage	V_{SD}	$I_S=5.0A, V_{GS}=0V$	--	--	1.4	V
Reverse Recovery Time	T_{rr}	$I_S=5.0A, V_{GS}=0V, dI_F/dt=100A/\mu s$ (Note 3)	--	184	--	ns
Reverse Recovery Charge	Q_{rr}		--	1.3	--	μC
Reverse Recovery Peak Current	I_{rrm}		--	13	--	A

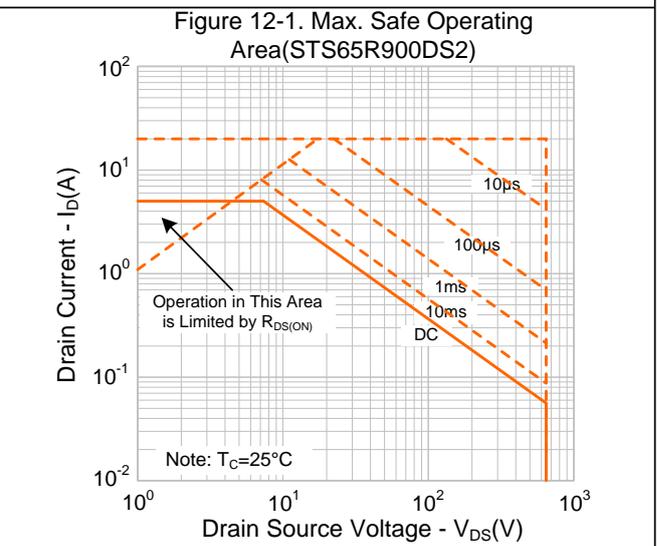
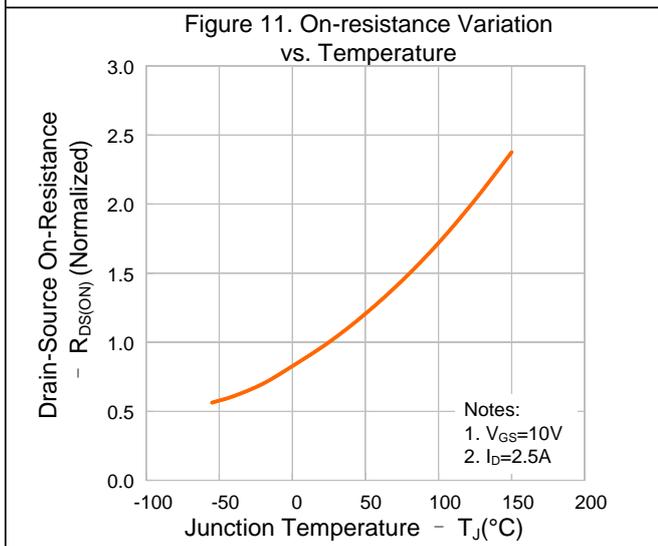
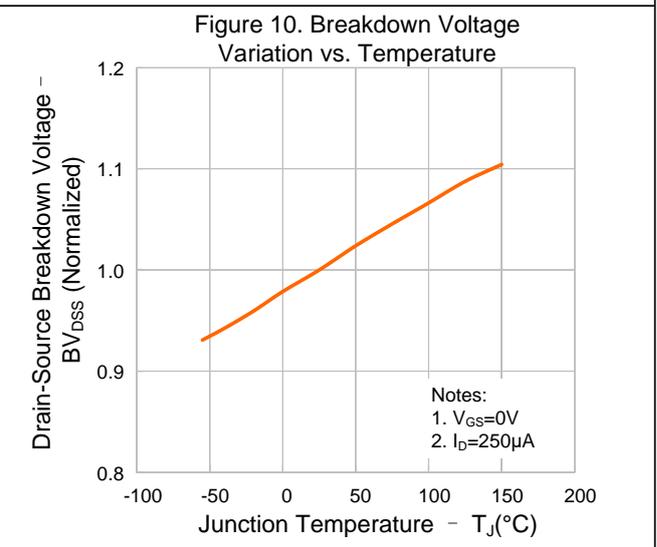
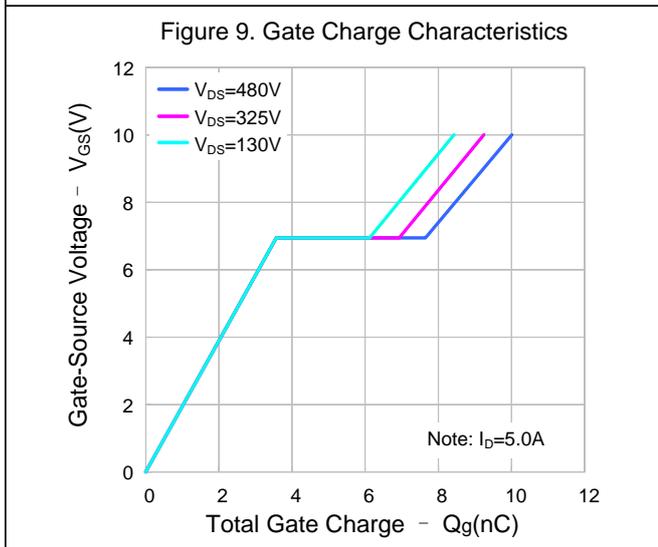
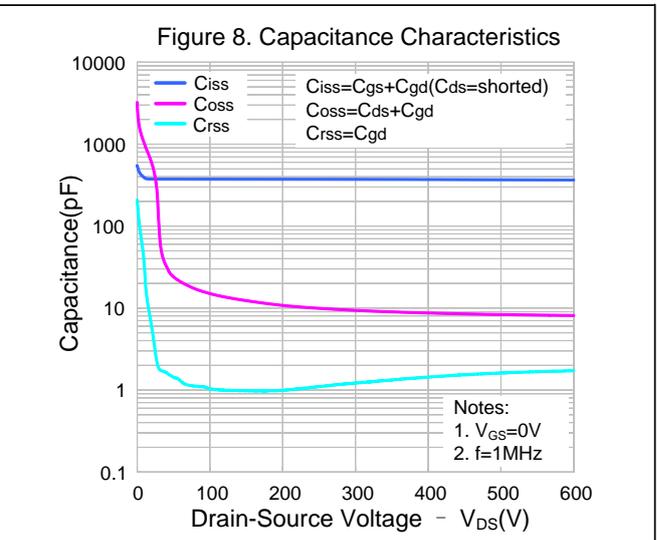
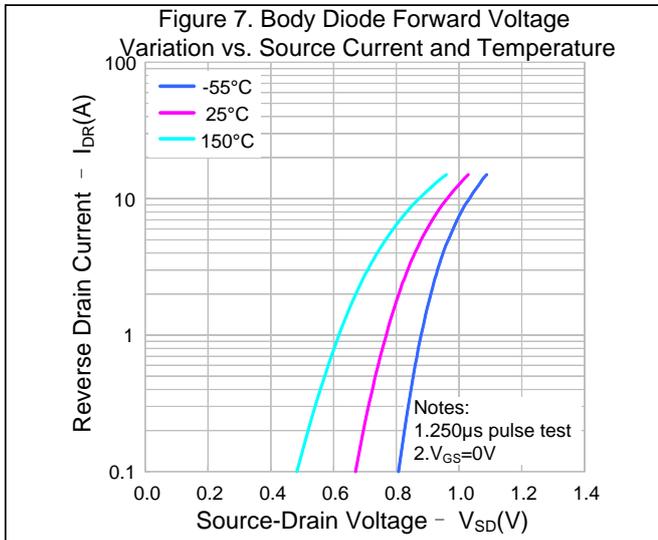
Notes:

- Pulse time $5\mu s$;
- The dissipation power will change with temperature, derating above 25°C :
 $0.29W/^\circ\text{C}$ (TO-252-2L)/ $0.19W/^\circ\text{C}$ (TO-220F-3L);
- Pulse Test: Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$;
- Essentially independent of operating temperature.

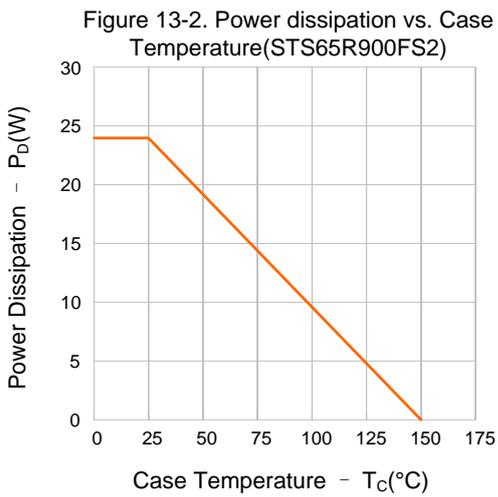
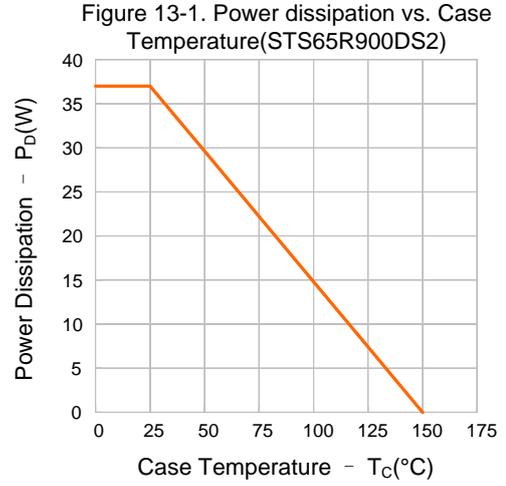
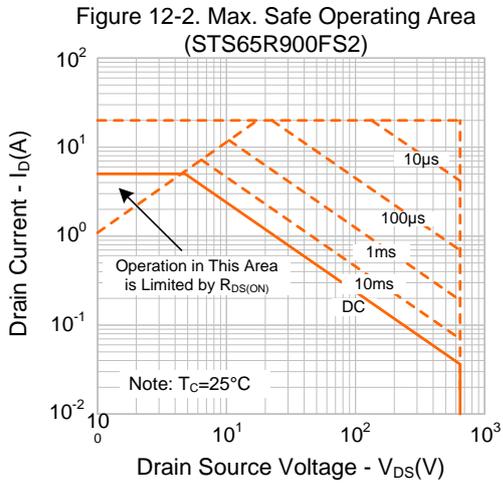
TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS (CONTINUED)

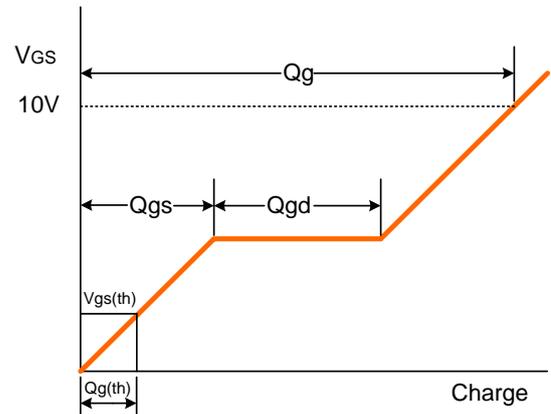
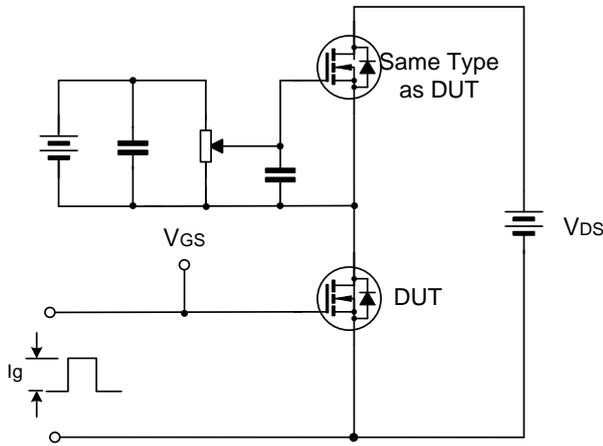


TYPICAL CHARACTERISTICS (CONTINUED)

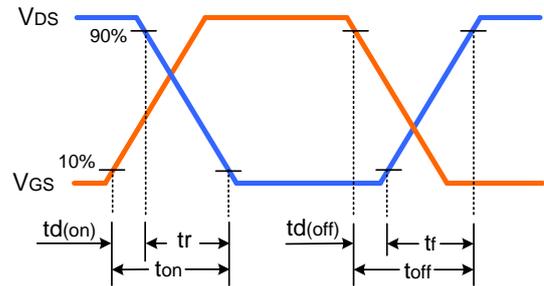
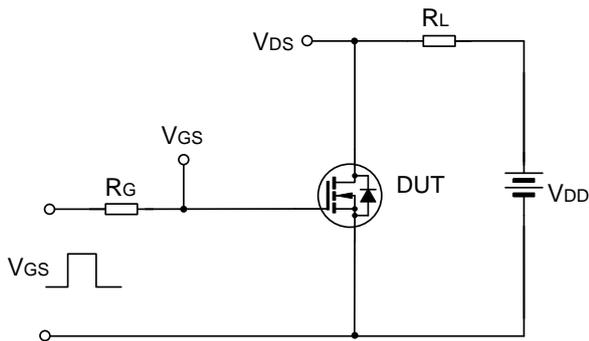


TYPICAL TEST CIRCUIT

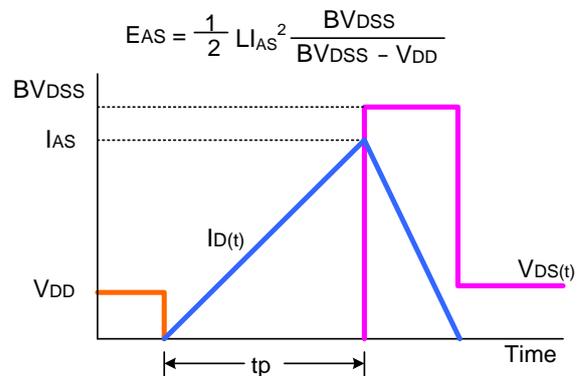
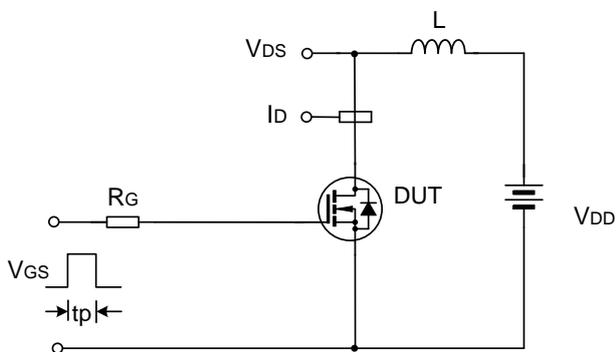
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveform



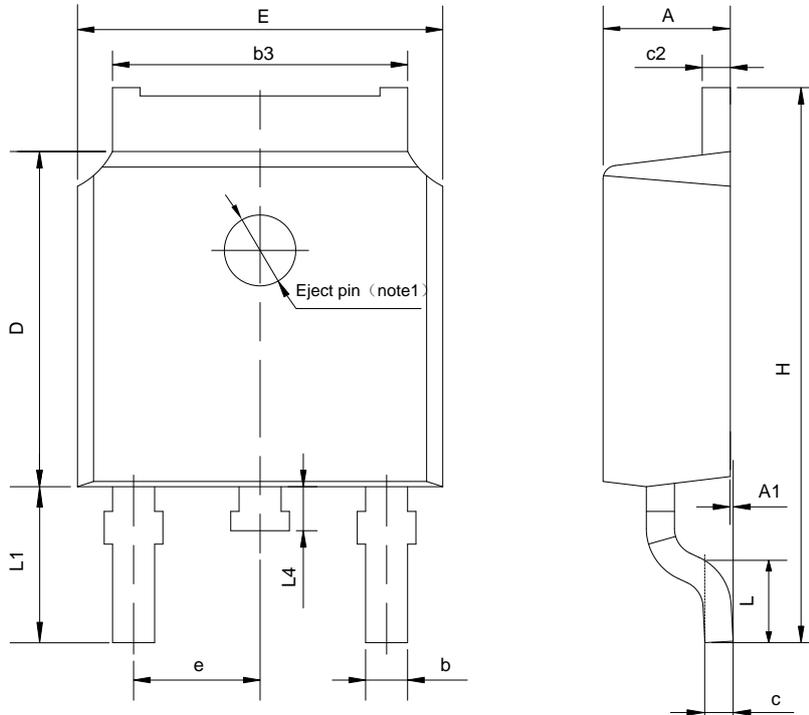
Unclamped Inductive Switching Test Circuit & Waveform



PACKAGE OUTLINE

TO-252-2L

UNIT: mm

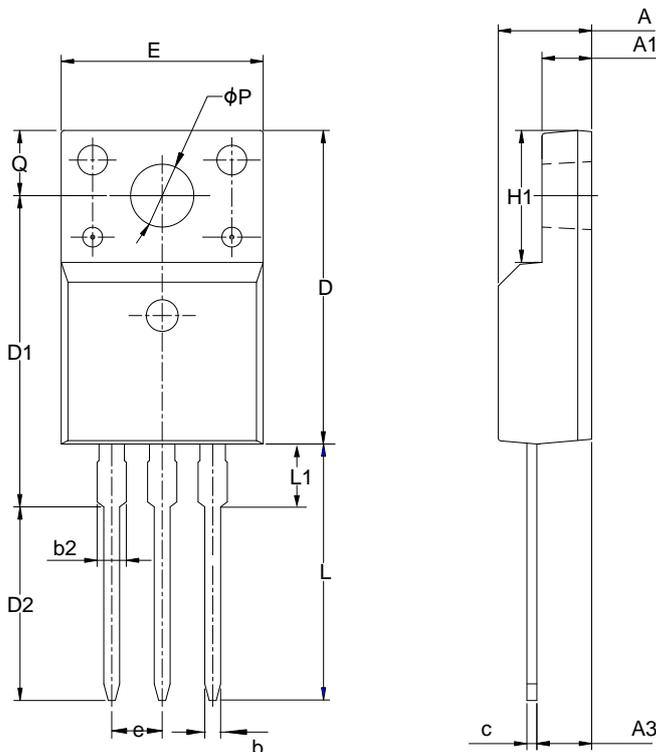


SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	2.10	2.30	2.50
A1	0	—	0.127
b	0.66	0.76	0.89
b3	5.10	5.33	5.46
c	0.45	—	0.65
c2	0.45	—	0.65
D	5.80	6.10	6.40
E	6.30	6.60	6.90
e	2.30TYP		
H	9.60	10.10	10.60
L	1.40	1.50	1.70
L1	2.90REF		
L4	0.60	0.80	1.00

NOTE1 : There are two conditions for this position:has an eject pin or has no eject pin.

TO-220F-3L

UNIT: mm



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	4.42	4.70	5.02
A1	2.30	2.54	2.80
A3	2.50	2.76	3.10
b	0.70	0.80	0.90
b2	—	—	1.47
c	0.35	0.50	0.65
D	15.25	15.87	16.25
D1	15.30	15.75	16.30
D2	9.30	9.80	10.30
E	9.73	10.16	10.36
e	2.54BSC		
H1	6.40	6.68	7.00
L	12.48	12.98	13.48
L1	—	—	3.50
ϕP	3.00	3.18	3.40
Q	3.05	3.30	3.55

Important notice :

1. The instructions are subject to change without notice!
2. Customers should obtain the latest relevant information before placing orders and should verify that such information is complete and current. Please read the instructions carefully before using our products, including the circuit operation precautions.
3. Our products are consumer electronic products or the other civil electronic products.
4. When using our products, please do not exceed the maximum rating of the products, otherwise the reliability of the whole machine will be affected. There is a certain possibility of failure or malfunction of any semiconductor product under specific conditions. The buyer is responsible for complying with safety standards and taking safety measures when using our products for system design, sample and whole machine manufacturing, so as to avoid potential failure risk that may cause personal injury or property loss.
5. It is strongly recommended to identify the trademark when buying our products. Please contact us if there is any question.
6. Product promotion is endless, our company will wholeheartedly provide customers with better products!
7. Website: <http://www.silan.com.cn>

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Rev.: 1.1

Revision History:

1. Add package of STS65R900FS2(TO-220F-3L)

Rev.: 1.0

Revision History:

1. First release
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