

**ECL 16,384-BIT BIPOLAR
RANDOM ACCESS MEMORY**

**ADVANCE
INFORMATION**

DESCRIPTION

The Fujitsu MBM10480 is a fully decoded 16,384-bit ECL read/write random access memory designed for high speed scratch pad, microprocessor and buffer storage applications.

The MBM10480 offers extremely small cell and chip sizes, realized

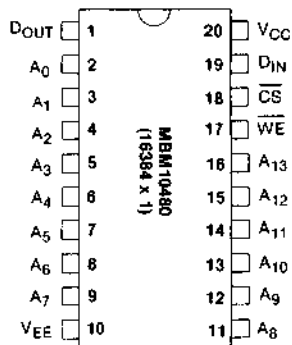
through the use of Fujitsu's patented DOPOS (Doped Polysilicon), as well as IOP (Isolation by Oxide and Polysilicon) processing. As a result, very fast access time with high yields and outstanding device reliability are achieved in volume production.

FEATURES

- Organized as 16,384 x 1
- Address Access Time: 20ns Max.
- Fully compatible with industry standard 10K series ECL families
- Open emitter for easy memory expansion

- DOPOS and IOP processing
- Pin compatible with F10480
- Low power dissipation: 700mW
- -5.2 V power supply
- Will be available in 100K series ECL

PIN ASSIGNMENT



Small geometry bipolar integrated circuits are occasionally susceptible to damage from static voltages or electric field. It is therefore advised that normal precautions be taken to avoid application of any voltage higher than the maximum rated voltages to this device.

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FOR A NEW PRODUCT TO BE
INTRODUCED DURING 1982. THIS IS
NOT A FINAL SPECIFICATION.
PARAMETRIC LIMITS ARE SUBJECT
TO CHANGE.***