

FUJITSU MICROELECTRONICS

MBM100422A-7

ECL 1024-BIT BIPOLAR RANDOM ACCESS MEMORY

ADVANCE INFORMATION

DESCRIPTION

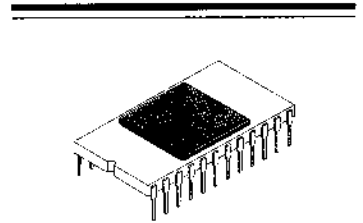
The Fujitsu MBM100422A-7 is a fully decoded 1024-bit ECL read/write random access memory designed for high speed scratch pad, microprocessor and buffer storage applications.

The MBM100422A-7 offers extremely small cell and chip sizes,

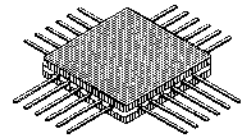
realized through the use of Fujitsu's patented DOPOS (Doped Polysilicon), as well as IOP (Isolation by Oxide and Polysilicon) processing. As a result, very fast access time with high yields and outstanding device reliability are achieved in volume production.

FEATURES

- Organized as 256 x 4
- Address Access Time: 7ns Max.
- Fully compatible with industry standard 100K series ECL families
- Open emitter for easy memory expansion
- DOPOS and IOP processing
- Pin compatible with F100422
- Low power dissipation: 900mW
- -4.5V power supply



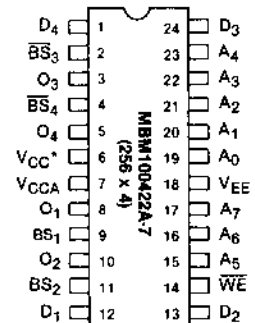
**CERAMIC PACKAGE
DIP-24C-A01**



**CERAMIC PACKAGE
FPT-24C-C02**

*THIS IS PRELIMINARY INFORMATION
FOR A NEW PRODUCT TO BE
INTRODUCED DURING 1982. THIS IS
NOT A FINAL SPECIFICATION.
PARAMETRIC LIMITS ARE SUBJECT
TO CHANGE.*

PIN ASSIGNMENT



*V_{CC} Grounded

Small geometry bipolar integrated circuits are occasionally susceptible to damage from static voltages or electric field. It is therefore advised that normal precautions be taken to avoid application of any voltage higher than the maximum rated voltages to this device.