

HSC276

Silicon Schottky Barrier Diode for Detector and Mixer

REJ03G0134-0200Z
(Previous: ADE-208-421A)
Rev.2.00
Nov.10.2003

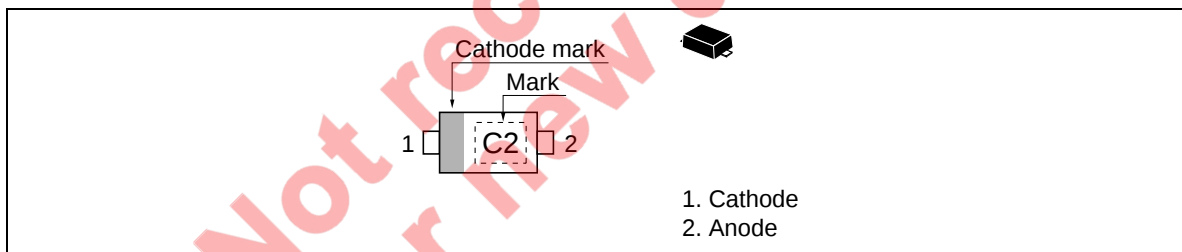
Features

- High forward current, Low capacitance.
- Ultra small Flat Package (UFP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Package Code
HSC276	C2	UFP

Pin Arrangement



Absolute Maximum Ratings

(T_a = 25°C)

Item	Symbol	Value	Unit
Reverse voltage	V _R	3	V
Average rectified current	I _O	30	mA
Junction temperature	T _j	125	°C
Storage temperature	T _{stg}	–55 to +125	°C

Electrical Characteristics

(T_a = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse voltage	V _R	3	—	—	V	I _R = 1 mA
Reverse current	I _R	—	—	50	μA	V _R = 0.5 V
Forward current	I _F	35	—	—	mA	V _F = 0.5 V
Capacitance	C	—	—	0.85	pF	V _R = 0.5 V, f = 1 MHz
ESD-Capability ^{*1}	—	30	—	—	V	C = 200 pF, R = 0 Ω, Both forward and reverse direction 1 pulse.

Note: 1. Failure criterion ; I_R ≥ 100 μA at V_R = 0.5 V

Main Characteristics

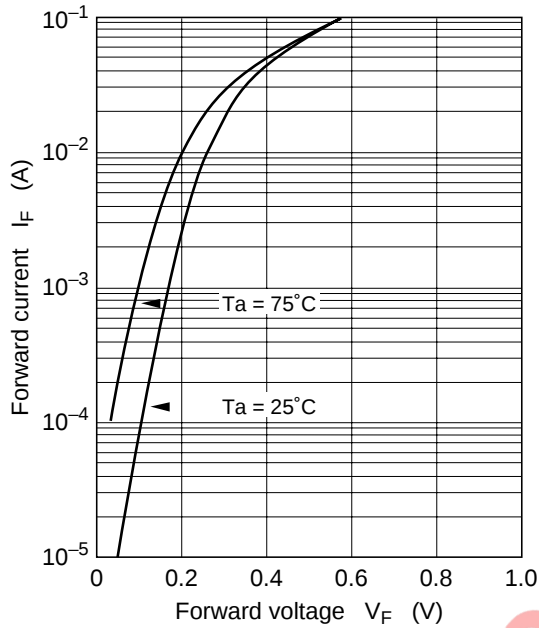


Fig.1 Forward current vs. Forward voltage

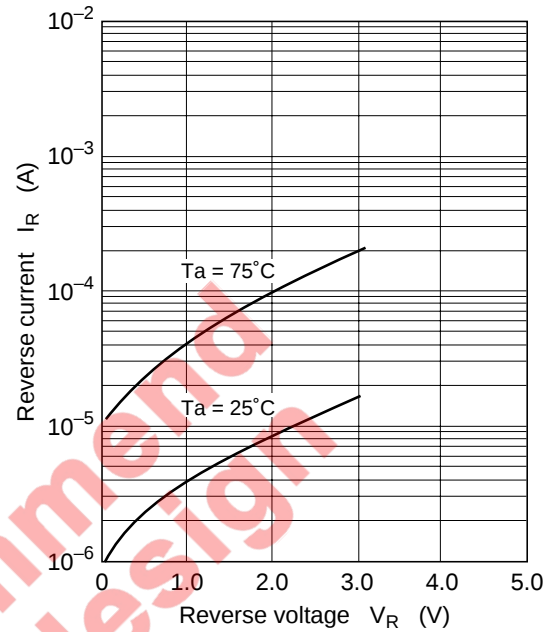


Fig.2 Reverse current vs. Reverse voltage

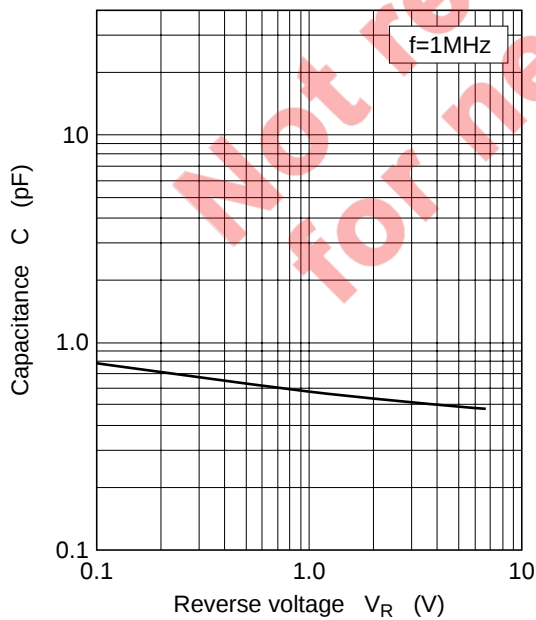
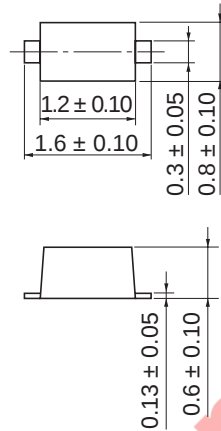


Fig.3 Capacitance vs. Reverse voltage

Package Dimensions

As of January, 2003

Unit: mm



Package Code	UFP
JEDEC	—
JEITA	Conforms
Mass (reference value)	0.0016 g

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April 1st, 2010
Renesas Electronics Corporation

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