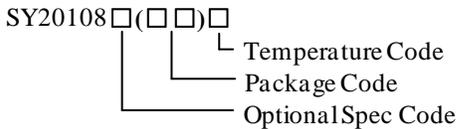


General Description

SY20108 is a high efficiency 3MHz synchronous step down DC/DC regulator IC capable of delivering up to 1A output current. It can operate over a wide input voltage range from 2.5V to 5.5V and integrate main switch and synchronous switch with very low $R_{DS(ON)}$ to minimize the conduction loss.

Ordering Information



Ordering Number	Package Type	Note
SY20108AAC	SOT23-5	1A

Features

- Low $R_{DS(ON)}$ for internal switches (top/bottom) 270mΩ/160mΩ
- 2.5~5.5V input voltage range
- High switching frequency 3.0MHz minimizes the external components
- Internal soft-start limits the inrush current
- 100% dropout operation
- RoHS Compliant and Halogen Free
- Compact package: SOT23-5

Applications

- Portable Navigation Device
- Set Top Box
- USB Dongle
- Media Player
- Smart phone

Typical Application

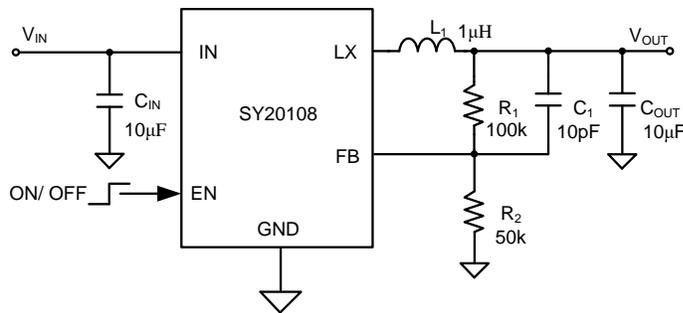


Figure1. Schematic Diagram

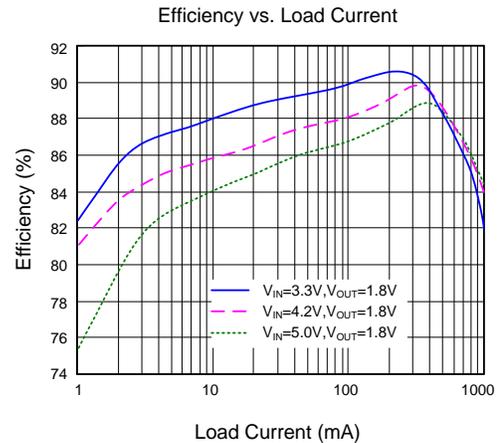
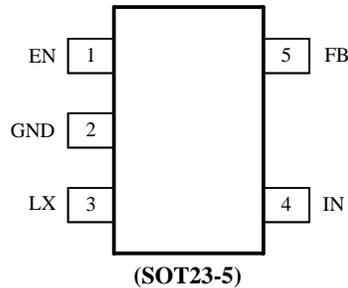


Figure2. Efficiency vs. Load Current

Pinout (Top View)



Top Mark: PVxyz (device code: PV, x=year code, y=week code, z= lot number code)

Pin Name	Pin Number	Pin Description
EN	1	Enable control. Pull high to turn on. Do not float.
GND	2	Ground pin.
LX	3	Inductor pin. Connect this pin to the switching node of the inductor.
IN	4	Input pin. Decouple this pin to the GND pin with at least 1uF ceramic capacitor.
FB	5	Output Feedback Pin. Connect this pin to the center point of the output resistor divider (as shown in Figure 1) to program the output voltage: $V_{OUT}=0.6*(1+R_1/R_2)$. Add optional C_1 (10pF~47pF) to speed up the transient response.

Absolute Maximum Ratings (Note 1)

Supply Input Voltage ----- 6.0V
 LX Voltage----- -0.3V^(*1) to 6V^(*2)
 Enable, FB Voltage----- $V_{IN} + 0.6V$
 Power Dissipation, P_D @ $T_A = 25^\circ C$, SOT23-5----- 0.6W
 Package Thermal Resistance (Note 2)
 θ_{JA} ----- 170°C/W
 θ_{JC} ----- 130°C/W
 Junction Temperature Range ----- 125°C
 Lead Temperature (Soldering, 10 sec.) ----- 260°C
 Storage Temperature Range ----- -65°C to 150°C

(*1) LX Voltage tested down to -5V<10ns

(*2) LX Voltage tested up to +7V<50ns

Recommended Operating Conditions (Note 3)

Supply Input Voltage ----- 2.5V to 5.5V
 Junction Temperature Range ----- -40°C to 125°C
 Ambient Temperature Range ----- -40°C to 85°C

Electrical Characteristics

($V_{IN} = 5V$, $V_{OUT} = 2.5V$, $L = 1.0\mu H$, $C_{OUT} = 10\mu F$, $T_A = 25^\circ C$, unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Input Voltage Range	V_{IN}		2.5		5.5	V
Quiescent Current	I_Q	$I_{OUT}=0$, $V_{FB}=V_{REF} \cdot 105\%$		40		μA
Shutdown Current	I_{SHDN}	EN=0		0.1	1	μA
Feedback Reference Voltage	V_{REF}		0.588	0.6	0.612	V
PFET RON	$R_{DS(ON),P}$			270		m Ω
NFET RON	$R_{DS(ON),N}$			160		m Ω
PFET Current Limit	I_{LIM}		1.3			A
EN rising threshold	V_{ENH}		1.5			V
EN falling threshold	V_{ENL}				0.4	V
Input UVLO threshold	V_{UVLO}				2.5	V
UVLO hysteresis	V_{HYS}			0.1		V
Oscillator Frequency	F_{OSC}			3.0		MHz
Min ON Time				70		ns
Max Duty Cycle			100			%
Soft Start Time	T_{SS}			1		ms
Thermal Shutdown Temperature	T_{SD}			160		$^\circ C$
Thermal Shutdown Hysteresis	T_{HYS}			15		$^\circ C$

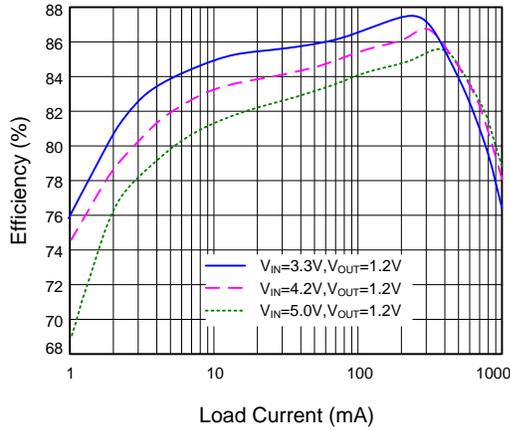
Note 1: Stresses beyond the “Absolute Maximum Ratings” may cause permanent damage to the device. These are stress ratings only. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Note 2: θ_{JA} is measured in the natural convection at $T_A = 25^\circ C$ on a low effective single layer thermal conductivity test board of JEDEC 51-3 thermal measurement standard. Pin 2 of SOT23-6 packages is the case position for θ_{JC} measurement.

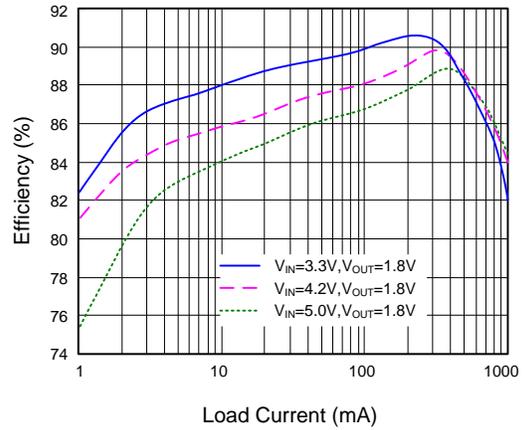
Note 3: The device is not guaranteed to function outside its operating conditions.

Typical Performance Characteristics

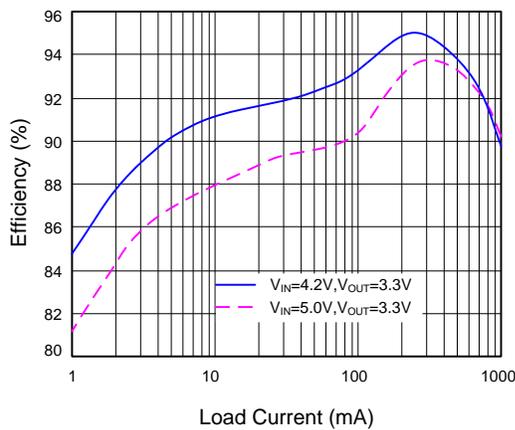
Efficiency vs. Load Current



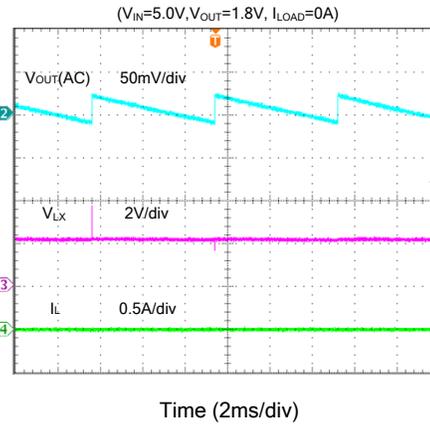
Efficiency vs. Load Current



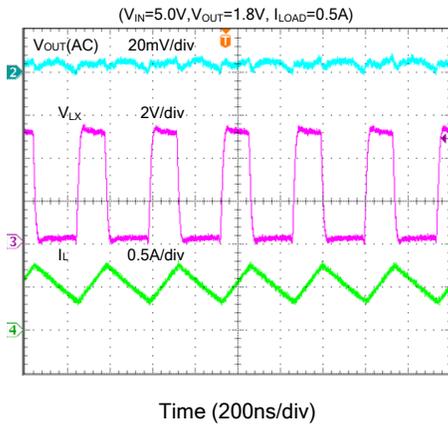
Efficiency vs. Load Current



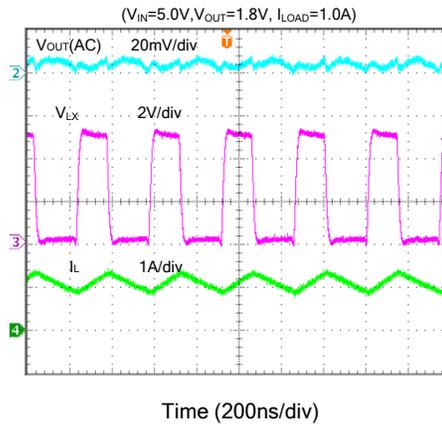
Output Ripple

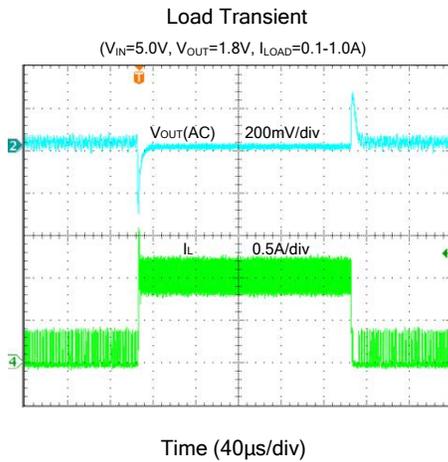
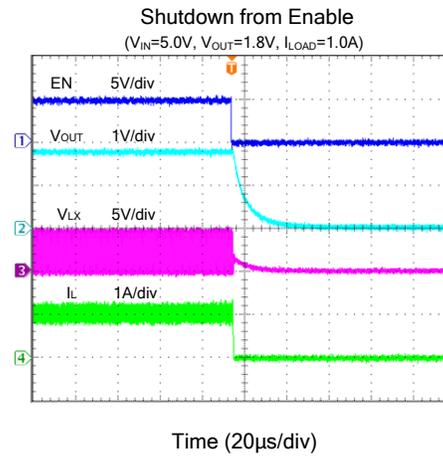
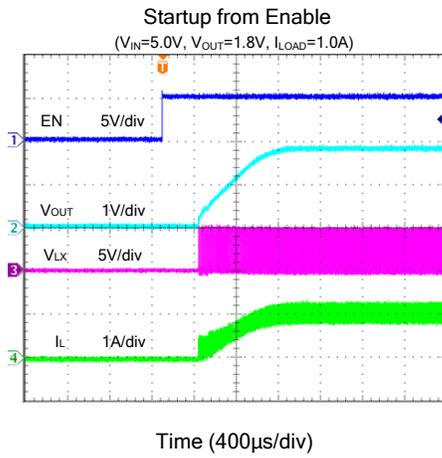


Output Ripple



Output Ripple





Operation

SY20108 is a high efficiency 3MHz synchronous step down DC/DC regulator IC capable of delivering up to 1A output current. It can operate over a wide input voltage range from 2.5V to 5.5V and integrate main switch and synchronous switch with very low $R_{DS(ON)}$ to minimize the conduction loss.

Applications Information

Because of the high integration in the SY20108 IC, the application circuit based on this regulator IC is rather simple. Only input capacitor C_{IN} , output capacitor C_{OUT} , output inductor L and feedback resistors (R1 and R2) need to be selected for the targeted applications specifications.

Feedback resistor dividers R1 and R2:

Choose R1 and R2 to program the proper output voltage. To minimize the power consumption under light loads, it is desirable to choose large resistance values for both R1 and R2. A value of between 100k Ω and 1M Ω is highly recommended for both resistors. If $R_2=120k\Omega$ is chosen, then R1 can be calculated to be:

$$R_1 = \frac{(V_{OUT} - 0.6V) \cdot R_2}{0.6V}$$

Input capacitor C_{IN} :

A typical X7R or better grade ceramic capacitor with 6V rating and greater than 4.7 μ F capacitance is recommended. To minimize the potential noise problem, place this ceramic capacitor really close to the IN and GND pins. Care should be taken to minimize the loop area formed by C_{IN} , and IN/GND pins.

Output capacitor C_{OUT} :

The output capacitor is selected to handle the output ripple noise requirements. Both steady state ripple and transient requirements must be taken into consideration when selecting this capacitor. For the best performance, it is recommended to use X7R or better grade ceramic capacitor with 6V rating and greater than 4.7 μ F capacitance.

Output inductor L:

There are several considerations in choosing this inductor.

- 1) Choose the inductance to provide the desired ripple current. It is suggested to choose the ripple current to be about 40% of the maximum output current. The inductance is calculated as:

$$L = \frac{V_{OUT}(1 - V_{OUT}/V_{IN,MAX})}{F_{SW} \times I_{OUT,MAX} \times 40\%}$$

where F_{sw} is the switching frequency and $I_{out,max}$ is the maximum load current.

The SY20108 regulator is quite tolerant of different ripple current amplitude. Consequently, the final choice of inductance can be slightly off the calculation value without significantly impacting the performance.

- 2) The saturation current rating of the inductor must be selected to be greater than the peak inductor current under full load conditions.

$$I_{SAT, MIN} > I_{OUT, MAX} + \frac{V_{OUT}(1 - V_{OUT}/V_{IN,MAX})}{2 \cdot F_{SW} \cdot L}$$

- 3) The DCR of the inductor and the core loss at the switching frequency must be low enough to achieve the desired efficiency requirement. It is desirable to choose an inductor with $DCR < 50m\Omega$ to achieve a good overall efficiency.

Short Circuit Protection:

SY20108 integrates hic-cup mode hard short protection function. If output voltage is below 40% of the regulation voltage, the internal soft-start node and the error amplifier output will be reset immediately. IC works in hic-cup protection mode. The hic-cup frequency is about 250Hz, and the hic-cup duty is 50%. If the hard short condition is removed, IC will go back to normal operation.

Load Transient Considerations:

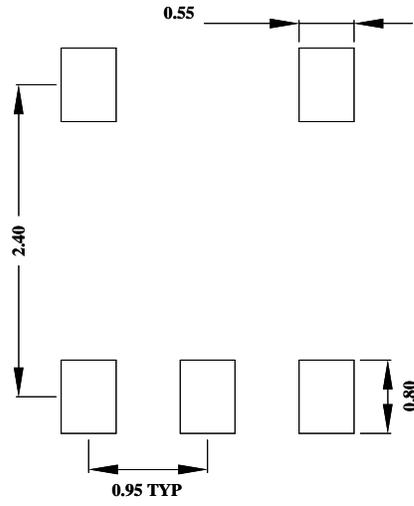
The SY20108 regulator IC integrates the compensation components to achieve good stability and fast transient responses. In some applications, adding a 10pF ceramic cap in parallel with R1 may further speed up the load transient responses and is thus recommended for applications with large load transient step requirements.

Layout Design:

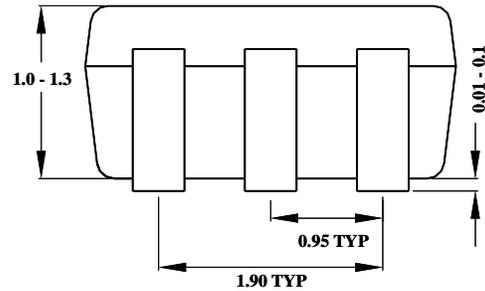
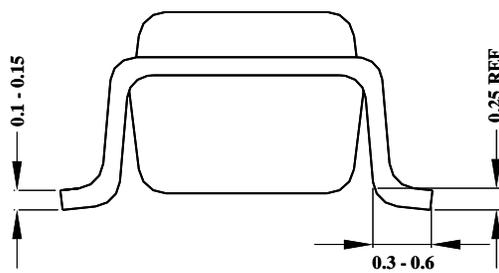
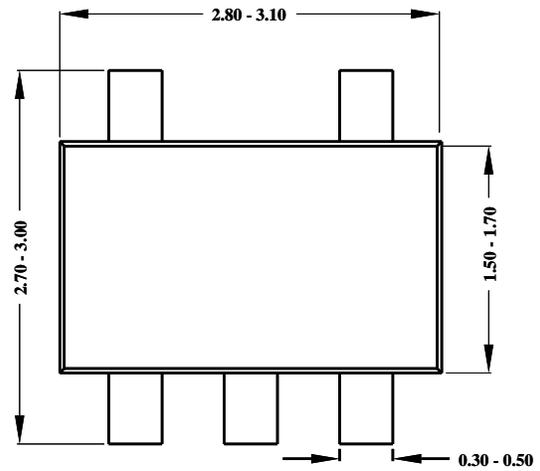
The layout design of SY20108 regulator is relatively simple. For the best efficiency and minimum noise problems, we should place the following components close to the IC: C_{IN} , L, R1 and R2.

- 1) It is desirable to maximize the PCB copper area connecting to GND pin to achieve the best thermal and noise performance. If the board space allowed, a ground plane is highly desirable.
- 2) C_{IN} must be close to Pins IN and GND. The loop area formed by C_{IN} and GND must be minimized.

SOT23-5 Package outline & PCB layout design



Recommended Pad Layout

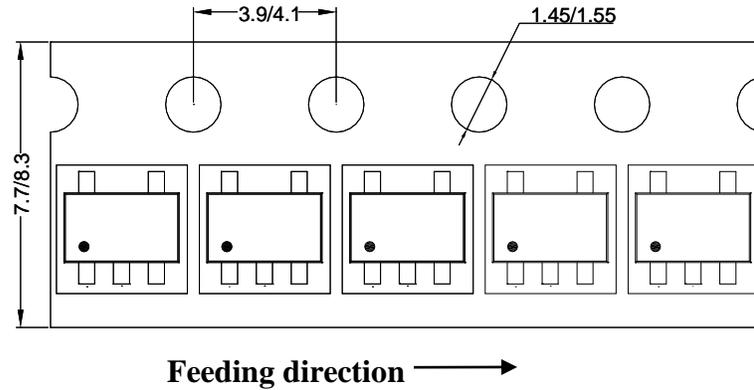


**Notes: All dimensions are in millimeters.
All dimensions don't include mold flash & metal burr.**

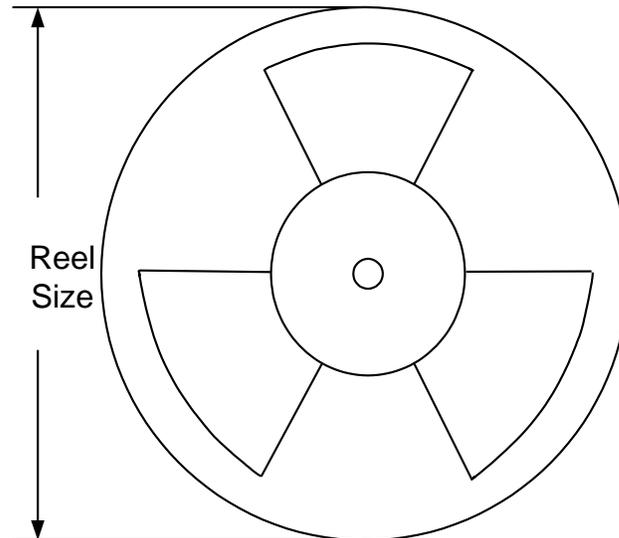
Taping & Reel Specification

1. Taping orientation

SOT23-5



2. Carrier Tape & Reel specification for packages



Package types	Tape width (mm)	Pocket pitch(mm)	Reel size (Inch)	Trailer length(mm)	Leader length (mm)	Qty per reel
SOT23-5	8	4	7"	280	160	3000

3. Others: NA



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