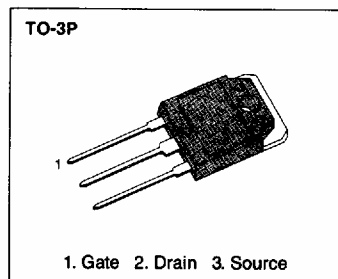


FEATURES

- Lower $R_{DS(on)}$
- Improved inductive ruggedness
- Fast switching times
- Rugged polysilicon gate cell structure
- Lower input capacitance
- Extended safe operating area
- Improved high temperature reliability



PRODUCT SUMMARY

Part Number	V _{DS}	R _{DS(on)}	I _D
SSH7N90	900V	1.8Ω	7A

ABSOLUTE MAXIMUM RATINGS

Characteristic	Symbol	SSH7N90	Unit
Drain-Source Voltage (1)	V _{DSS}	900	V _{dc}
Drain-Gate Voltage (R _{GS} =1.0MΩ)(1)	V _{DGR}	900	V _{dc}
Gate-Source Voltage	V _{GS}	±30	V _{dc}
Continuous Drain Current T _c =25 °C	I _D	7	A _{dc}
Continuous Drain Current T _c =100 °C	I _D	5	A _{dc}
Drain Current - Pulsed (3)	I _{DM}	28	A _{dc}
Single Pulsed Avalanche Energy (4)	E _{AS}	1300	mJ
Avalanche Current	I _{AS}	7	A
Total Power Dissipation at T _c =25 °C	P _D	190	Watts
Derate Above 25 °C		1.54	W/ °C
Operating and Storage Junction Temperature Range	T _J , T _{STG}	-55 to +150	°C
Maximum Lead Temp. for Soldering Purposes, 1/8" from case for 5 seconds	T _L	300	°C

Notes : (1) T_J=25°C to 150°C

(2) Pulse test : Pulse width ≤ 300μs, Duty Cycle ≤ 2%

(3) Repetitive rating : Pulse width limited by junction temperature

(4) L=51mH, V_{dd}=50V, R_G=25Ω, Starting T_J=25°C

ELECTRICAL CHARACTERISTICS (Tc=25°C unless otherwise specified)

Symbol	Characteristic	Min	Typ	Max	Units	Test Conditions
BV _{DSS}	Drain-Source Breakdown Voltage	900	-	-	V	V _{GS} =0V, I _D =250μA
V _{GS(th)}	Gate Threshold Voltage	2.0	-	4.5	V	V _{DS} =V _{GS} , I _D =250μA
I _{GSS}	Gate-Source Leakage Forward	-	-	100	nA	V _{GS} =20V
I _{GSS}	Gate-Source Leakage Reverse	-	-	-100	nA	V _{GS} =-20V
I _{DSS}	Zero Gate Voltage Drain Current	-	-	250	μA	V _{DS} =Max. Rating, V _{GS} =0V
		-	-	1000	μA	V _{DS} =0.8 Max. Rating, V _{GS} =0V, T _C =150°C
R _{DS(on)}	Static Drain-Source On-Resistance(2)	-	-	1.8	Ω	V _{GS} =10V, I _D =3.5A
g _{fs}	Forward Transconductance (2)	4.0	-	-	Ω	V _{DS} =15V, I _D =3.5A
C _{iss}	Input Capacitance	-	1500	-	pF	V _{GS} =0V, V _{DS} =25V, f=1MHz
C _{oss}	Output Capacitance	-	190	-	pF	
C _{rss}	Reverse Transfer Capacitance	-	70	-	pF	
t _{d(on)}	Turn-On Delay Time	-	40	-	ns	V _{DD} =0.5 BV _{DSS} , I _D =7.0A, Z _Θ =9.1Ω (MOSFET switching times are essentially independent of operating temperature)
t _r	Rise Time	-	110	-	ns	
t _{d(off)}	Turn-Off Delay Time	-	300	-	ns	
t _f	Fall Time	-	130	-	ns	
Q _g	Total Gate Charge (Gate-Source Plus Gate-Drain)	-	-	150	nC	V _{GS} =10V, I _D =7.0A, V _{DS} =0.8 Max. Rating (Gate charge is essentially independent of operating temperature)
Q _{gs}	Gate-Source Charge	-	15	-	nC	
Q _{gd}	Gate-Drain ("Miller") Charge	-	60	-	nC	

THERMAL RESISTANCE

Symbol	Characteristics		SSH7N90	Units	Remark
R _{thJC}	Junction-to-Case	MAX	0.65	K/W	
R _{thCS}	Case-to-Sink	TYP	0.24	K/W	Mounting surface flat
R _{thJA}	Junction-to-Ambient	MAX	40	K/W	Free Air Operation

Notes : (1) T_J=25°C to 150°C

(2) Pulse test : Pulse width ≤ 300μs, Duty Cycle ≤ 2%

(3) Repetitive rating : Pulse width limited by max. junction temperature

SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS

Symbol	Characteristic	Min	Typ	Max	Units	Test Conditions
I_S	Continuous Source Current (Body Diode)	-	-	7	A	Modified MOSFET symbol showing the integral reverse P-N junction rectifier
I_{SM}	Pulse Source Current (Body Diode) (3)	-	-	28	A	
V_{SD}	Diode Forward Voltage (2)	-	-	1.5	V	$T_J=25^\circ\text{C}$, $I_S=7\text{A}$, $V_{GS}=0\text{V}$
t_{rr}	Reverse Recovery Time	-	800	-	ns	$T_J=25^\circ\text{C}$, $I_F=7\text{A}$, $dI_F/dt=100\text{A}/\mu\text{S}$

Notes : (1) $T_J=25^\circ\text{C}$ to 150°C (2) Pulse test : Pulse width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$

(3) Repetitive rating : Pulse width limited by max. junction temperature

