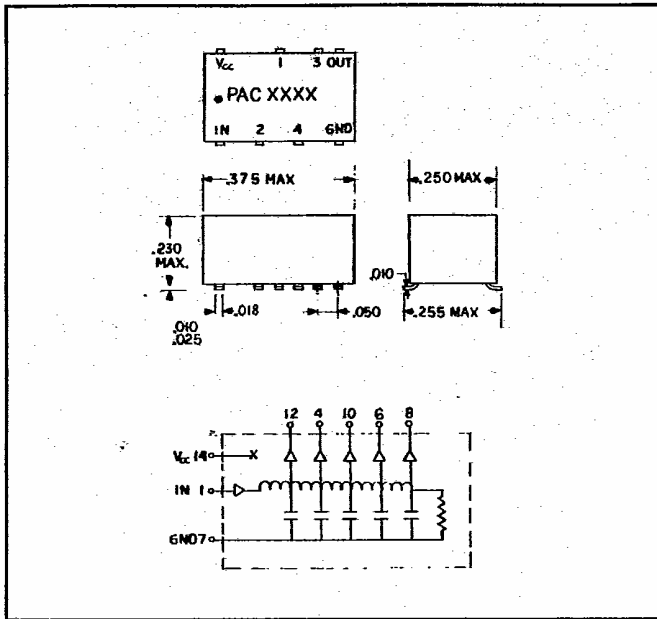


DELAY MODULES, TTL, SURFACE MOUNTED

14 PIN 5 TAPS



- TTL and DTL compatible
- No external components required
- Extremely Compact
- Surface Mounted

Part Number	Delay Time ^{1,3} ns	Number of Taps	Tap Delay ^{1,3} ns	Rise Time ² ns
4428	25	5	5	4
4429	30	5	6	4
4437	35	5	7	4
4438	40	5	8	4
4439	45	5	9	4
4431	50	5	10	4
4441	60	5	12	4
4440	75	5	15	4
4432	100	5	20	4
4443	125	5	25	4
4433	150	5	30	4
4434	200	5	40	4
4435	250	5	50	4

1. MEASURED AT 1.5V LEVEL ON THE LEADING EDGE
2. MEASURED FROM 0.75V TO 2.4V
3. TOLERANCE $\pm 5\%$ or $\pm 2\text{ns}$ which ever is greater

OPERATING SPECIFICATIONS

Vcc supply voltage:	4.75 to 5.25 VDC
Logic 1 input:	
Voltage	2V min; 5.5V max.
Current	2.4V = 50ua max. 5.5V = 1ma max.
Logic 0 input:	
Voltage	.8V max.
Current	-2ma max.
Logic 1 Voltage out:	2.4V min.
Logic 0 Voltage out:	.4V max.
Operating temperature range:	0° to 70°C
Storage temperature:	-55 to +125°C.

TEST CONDITIONS

Input pulse width:	Minimum of 50% of Delay
Input pulse rise time:	3ns
Input pulse voltage:	3.2V
Vcc supply voltage:	5.0 VDC
Icc supply current:	40 ma typ.
All measurements made at 25°C.	

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