

OKI semiconductor

MSM82C43RS/GS

INPUT/OUTPUT PORT EXPANDER

GENERAL DESCRIPTION

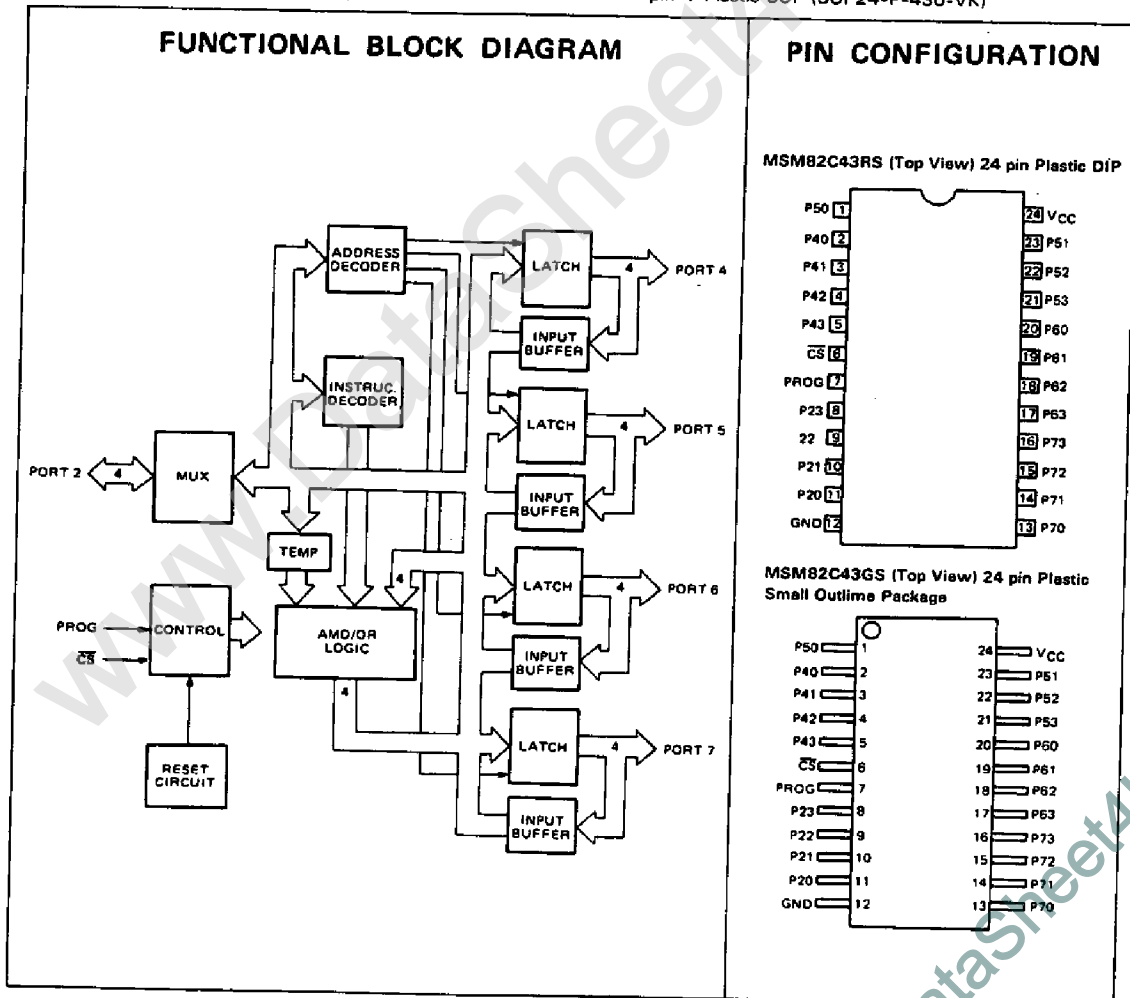
The MSM82C43 is an input/output port expander device based on 3μ silicon gate CMOS technology and designed to operate at low power consumption levels.

In systems employing the MSM80C48/49 8-bit 1-chip microcomputers, 4-bit data can be expanded by dividing between four I/O lines by executing the MOVDpp, A, MOVDA; Pp, ANLDPp, A and ORLDPp, A instructions.

FEATURES

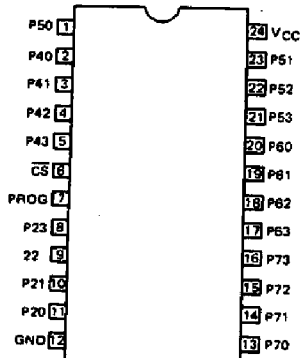
- 3μ silicon gate CMOS technology for low power consumption
- 2.5 to 6 V single power supply (dependent on MSM80C 48/49 operating frequency.)
- Fully static operation
- Bidirectional I/O ports
- TTL compatible (ports 4 thru 7)
- Functional compatibility with Intel i8243
- 24 pin Plastic DIP (DIP24-P-600)
- 24 pin Plastic SOP (SOP24-P-430-K)
- 24 pin-V Plastic SOP (SOP24-P-430-VK)

FUNCTIONAL BLOCK DIAGRAM

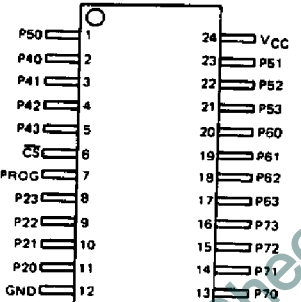


PIN CONFIGURATION

MSM82C43RS (Top View) 24 pin Plastic DIP



MSM82C43GS (Top View) 24 pin Plastic Small Outline Package



ELECTRIC CHARACTERISTICS

• Absolute Maximum Ratings

Parameter	Symbol	Conditions	Rating	Unit
Supply Voltage	V_{CC}	$T_a = 25^\circ\text{C}$	$-0.5 \sim 7$	V
Input Voltage	V_I	$T_a = 25^\circ\text{C}$	$-0.3 \sim V_{CC}$	V
Storage Temperature	T_{stg}	—	$-65 \sim +150$	$^\circ\text{C}$

• Operating Conditions

Parameter	Symbol	Conditions	Rating	Unit
Supply Voltage	V_{CC}	—	$2.5 \sim 6^{*1}$	V
Ambient Temperature	T_A	—	$-40 \sim +85$	$^\circ\text{C}$
Fan-out	N	MOS load	10	—
		TTL load	3^{*2}	—

• DC Characteristics

 $(V_{CC} = 4.0\text{V} \sim 6.0\text{V}, T_a = -40^\circ\text{C} \sim +85^\circ\text{C})$

Parameter	Symbol	Conditions	MIN	TYP	MAX	Unit
"L" Input Voltage	V_{IL}		-0.5	—	$0.13V_{CC}$	V
"H" Input Voltage	V_{IH}		$0.4V_{CC}$	—	V_{CC}	V
"L" Output Voltage Ports 4-7	V_{OL1}	$I_{OL} = 5\text{mA}$	—	—	0.45	V
"L" Output Voltage Port 7	V_{OL2}	$I_{OL} = 20\text{mA}$	—	—	1	V
"L" Output Voltage Port 2	V_{OL3}	$I_{OL} = 0.9\text{mA}$	—	—	0.45	V
"L" Total Output Current from Ports 4-7 ^{*3}	I_{OL}	5mA/1PIN	—	—	80	mA
"H" Output Voltage Ports 4-7	V_{OH1}	$I_{OH} = -240\mu\text{A}$	$0.75V_{CC}$	—	—	V
"H" Output Voltage Port 2	V_{OH2}	$I_{OH} = -100\mu\text{A}$	$0.75V_{CC}$	—	—	V
"H" Output Voltage Ports 4-7	V_{OH1}	$I_{OH} = -40\mu\text{A}$	$0.93V_{CC}$	—	—	V
"H" Output Voltage Port 2	V_{OH2}	$I_{OH} = -20\mu\text{A}$	$0.93V_{CC}$	—	—	V
Input Leak Current ^{*3}	I_{IL1}	$0 \leq V_{IN} \leq V_{CC}$	-10	—	20	μA
Input Leak Current ^{*6}	I_{IL2}	$0 \leq V_{IN} \leq V_{CC}$	-10	—	10	μA
Power Supply Current	I_{CC}	Standby stop No accessing	—	5	100	μA
		For continuous MSM80C49 access- ing at 11 MHz	—	1	2	mA

NOTE: *1 The supply voltage during operation is dependent on MSM80C49 operating frequency.

*2 Except P20 thru P23.

*3 P40 thru P43, P50 thru P53, P60 thru P63, P70 thru P73.

*4 P70 thru P73.

*5 P20 thru P23.

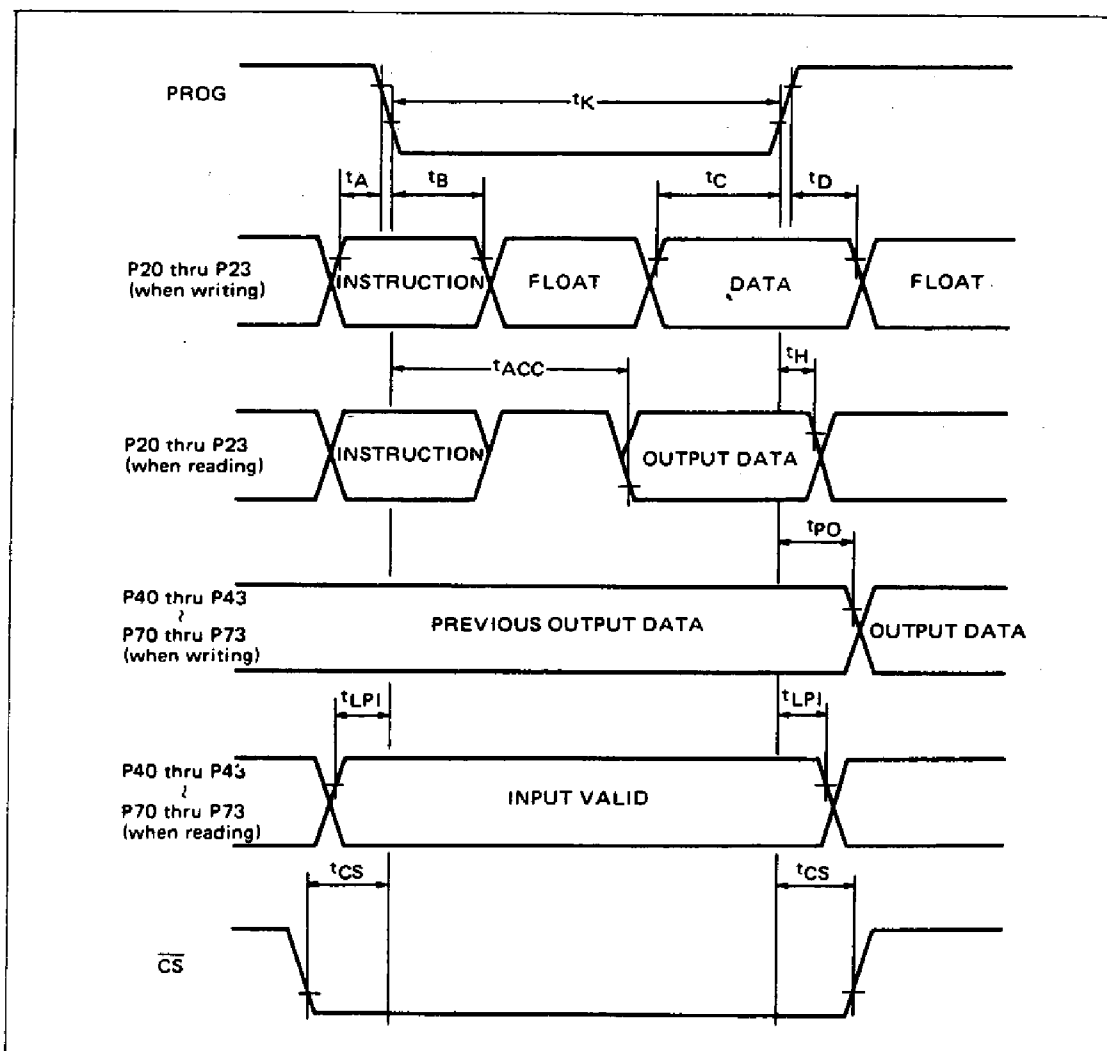
*6 P20 thru P23, CS, PROG.

● AC Characteristics

($V_{CC} = 4.0V \sim 6.0V$, $T_a = -40^{\circ}C \sim +85^{\circ}C$)

Parameter	Symbol	Conditions	MIN	MAX	Unit
Port Control Setting Time (up to PROG Falling Edge)	t_A	80pF LOAD	50	—	ns
Port Control Holding Time (From PROG Falling Edge)	t_B	20pF LOAD	60	—	ns
Output Data Setting Time	t_C	80pF LOAD	200	—	ns
Output Data Holding Time	t_D	20pF LOAD	20	—	ns
Input Data Holding Time	t_H	20pF LOAD	0	150	ns
PROG Pulse Width	t_K	—	700	—	ns
$\overline{CS}$ Valid Time (before and after PROG)	t_{CS}	—	50	—	ns
Output Data Valid Time (at Ports 4–7)	t_{PO}	100pF LOAD	—	700	ns
Input Data Holding Time (at Ports 4–7)	t_{LPI}	—	100	—	ns
Input Data Valid Time (from PROG Falling Edge)	t_{ACC}	80pF LOAD	—	650	ns

TIMING CHART



PIN FUNCTIONS

Pin	Function
PROG	Clock input from MSM80C49. When PROG is changed from "H" to "L", MSM82C43 STARTS operating in accordance with an order from MSM80C49.
$\overline{CS}$	Input for chip select. Outputs and internal status cannot be changed when $\overline{CS}$ is "H".
P20 - P23	4-bit bidirectional I/O ports. When connected to P20 thru P23 of MSM80C49, direct data transfer from port to accumulator and from accumulator to port is possible.
P40 - P43 P50 - P53 P60 - P63 P70 - P73	4-bit bidirectional I/O ports. Data is latched statistically when output to ports, but is only valid while PROG is at "L" level when input.
VCC	+5V power supply
GND	GROUND

FUNCTIONS

• Write mode

Execution of MOVDp, A, ORLDPp, A, ANLDPp, and A by MSM80C49 enables direct output of accumulator contents to ports 4 thru 7, and output to the ports after ORing or ANDing with port data. The port data is latched statistically at this time and remains unchanged until execution of the next instruction.

level. When at "H" level, ports 4 thru 7 are switched to tristate and port 2 is switched to input mode.

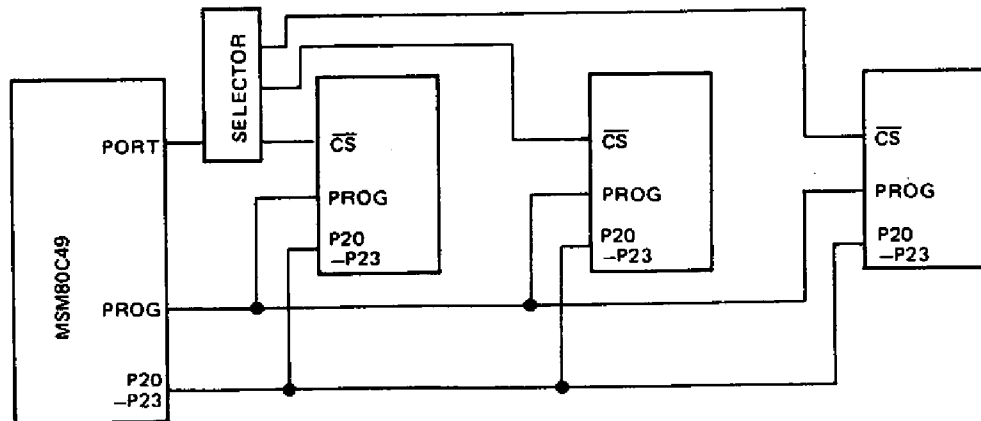
• Address and instruction code

Instruction	P23	P22	Port	P21	P20
Read	0	0	Port 4	0	0
Write	0	1	Port 5	0	1
OR	1	0	Port 6	1	0
AND	1	1	Port 7	1	1

• Read mode

Execution of MOVDA and Pp results in data of ports 4 thru 7 being accepted by the accumulator. Note that port data is valid only while PROG is at "L"

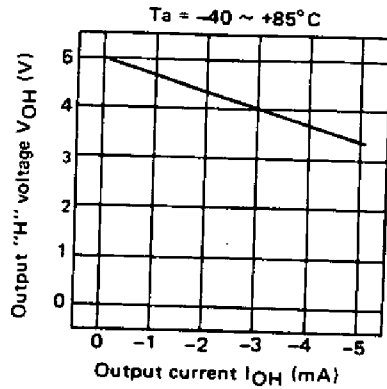
• MSM82C43 Multiple connection example



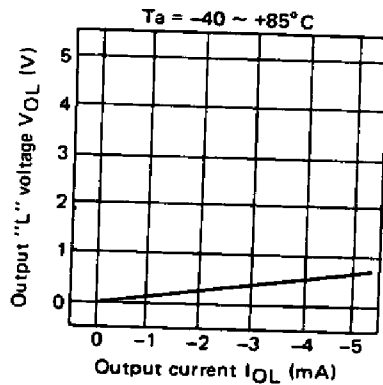
OUTPUT CHARACTERISTICS

• Standard DC characteristics

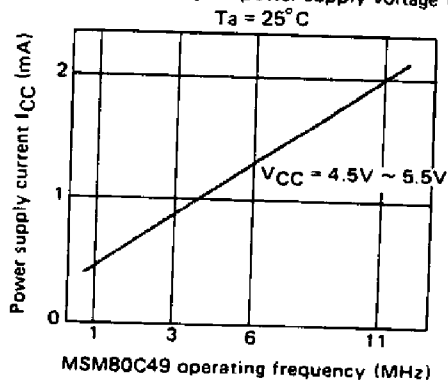
Output "H" voltage (V_{OH}) vs. output current (I_{OH})



Output "L" voltage (V_{OL}) vs. output current (I_{OL})

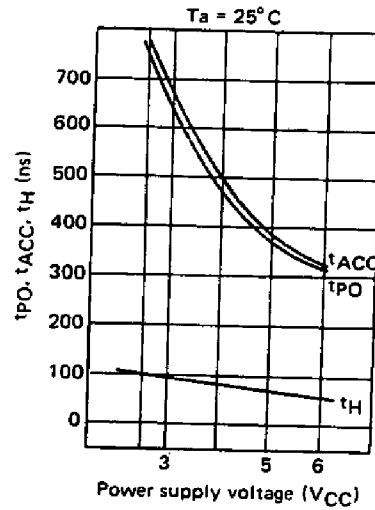


Operating frequency vs. power supply voltage (I_{CC})



• Standard AC characteristics

t_{PO} , t_H , and t_{ACC} vs. power supply voltage (V_{CC})



Note: The direction which the output current flows through the device is taken as the positive direction.

GUARANTEED MSM82C43 OPERATING RANGE

