



## absolute maximum ratings

Logic Supply Voltage ( $V_{GG}$ )	$V_{SS} + 0.3V$ to $V_{SS} - 33V$
Buffer Supply Voltage ( $V_{BB}$ )	$V_{SS} + 0.3V$ to $V_{SS} - 18V$
Trigger Input Voltage ( $V_{IT}$ )	$V_{SS} + 0.3V$ to $V_{SS} - 18V$
Power Dissipation ( $P_D$ )	250 mW
Storage Temperature ( $T_S$ )	$-55^\circ\text{C}$ to $+100^\circ\text{C}$
Operating Temperature ( $T_A$ )	$0^\circ\text{C}$ to $+70^\circ\text{C}$

## electrical characteristics

$T_A$  within operating range ( $V_{GG} = -27 \pm 2V$ ,  $V_{BB} = -10 \pm .5V$ ), unless otherwise noted.

PARAMETER	SYMBOL	MIN	TYP	MAX	UNITS
Trigger Inputs:					
Frequency	$f_{IT}$	DC		500	kHz
Rise and Fall Times (10% to 90%)	$t_r, t_f$			25	ns
Pulse Width (at 90%)	$p_w$	1			$\mu\text{s}$
Logical High Level	$V_{ITH}$	-2.5		$V_{SS}$	V
Logical Low Level	$V_{ITL}$	-18.0		-7.0	V
Leakage Current	$I_{ITL}$			1.0	$\mu\text{A}$
Trigger Outputs: (loaded 10M ohm to ground, $T_A = 25^\circ\text{C}$ )					
Logical High Level	$V_{OTH}$	-1.5		0	V
Logical Low Level	$V_{OTL}$			-10	V
Buffer Outputs: (loaded 20K ohm to ground and 20K ohm to $V_{BB}$ , $T_A = 25^\circ\text{C}$ )					
Logical High Level	$V_{OH}$	-1.0		0	V
Logical Low Level	$V_{OL}$	$V_{BB}$		-8.0	V
Supply Currents: (no output loads, $T_A = 25^\circ\text{C}$ )					
Logic Supply	$I_{GG}$			4	mA
Buffer Supply	$I_{BB}$			20	$\mu\text{A}$

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## timing diagram

