SSDI
SOLID STATE DEVICES, INC

PRELIMINARY

SFF240M
SFF240Z

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Designer's Data Sheet

**18 AMP
200 VOLTS
0.18Ω
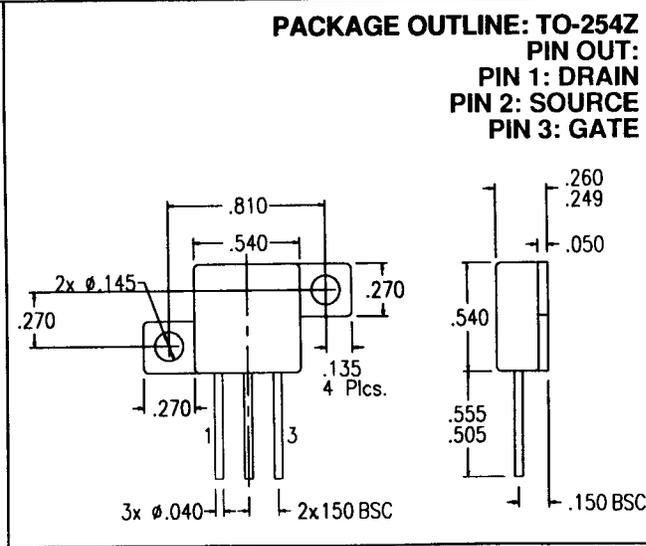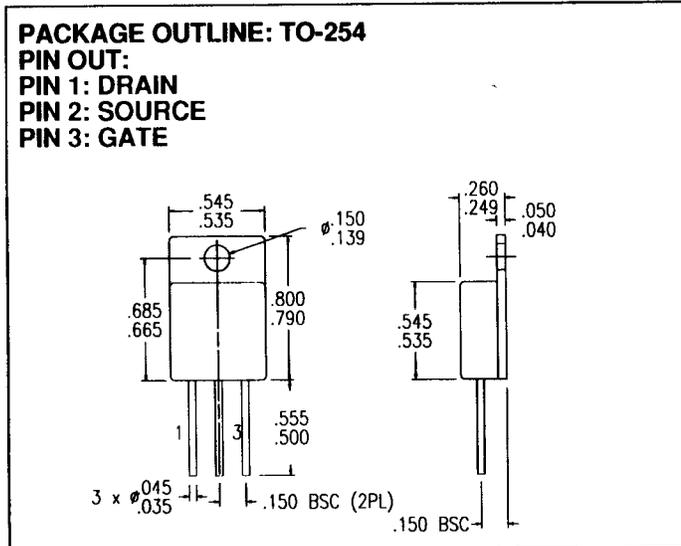N-CHANNEL
POWER MOSFET**

TO-254   **TO-254Z**

FEATURES:

- Rugged construction with polysilicon gate
- Low RDS(on) and high transconductance
- Excellent high temperature stability
- Very fast switching speed
- Fast recovery and superior dv/dt performance
- Increased reverse energy capability
- Low input and transfer capacitance for easy paralleling
- Ceramic Seals for improved hermeticity
- Hermetically sealed package
- TX, TXV and Space Level screening available
- Replaces: IRFM240 Types
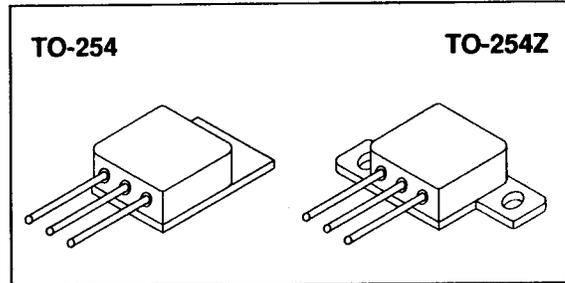
MAXIMUM RATINGS

CHARACTERISTIC	SYMBOL	VALUE	UNIT
Drain to Source Voltage	V_{DS}	200	Volts
Gate to Source Voltage	V_{GS}	±20	Volts
Continuous Drain Current	I_D	18	Amps
Operating and Storage Temperature	T_{op} & T_{stg}	-55 to +150	°C
Thermal Resistance, Junction to Case	$R_{\theta JC}$	1.7	°C/W
Total Device Dissipation @ TC=25°C Total Device Dissipation @ TC=55°C	P_D	74 56	Watts

PACKAGE OUTLINE: TO-254
PIN OUT:
PIN 1: DRAIN
PIN 2: SOURCE
PIN 3: GATE

.545
.535

⌀ .150
.139

.260
.249   .050
.040

.685
.665

.800
.790

.545
.535

.555
.500

3 x ⌀ .045/.035   .150 BSC (2PL)
.150 BSC

PACKAGE OUTLINE: TO-254Z
PIN OUT:
PIN 1: DRAIN
PIN 2: SOURCE
PIN 3: GATE

.810

.540

2x ⌀ .145

.270

.270

.270   .135
4 Plcs.

3x ⌀ .040   2x .150 BSC

.260
.249

.050

.540

.555
.505

.150 BSC

Available with Glass or Ceramic Seals. Contact Factory for details.

NOTE: All specifications are subject to change without notification. SCD's for these devices should be reviewed by SSDI prior to release.

DATA SHEET #: F00113 B   **MED**

SFF240M
SFF240Z

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ELECTRICAL CHARACTERISTICS @ TJ=25 C (Unless Otherwise Specified)

RATING		SYMBOL	MIN	TYP	MAX	UNIT
Drain to Source Breakdown Voltage (VGS=0 V, ID=250µA)		BVDSS	200	---	---	V
Drain to Source on State Resistance (VGS=10 V, ID= 10 A)		RDS(on)	---	0.13	0.18	Ω
On State Drain Current (VDS >ID(on) X RDS(on) Max, VGS=10 V		ID(on)	18	---	---	A
Gate Threshold Voltage (VDS=VGS, ID=250µA)		VGS(th)	2.0	---	4.0	V
Forward Transconductance (VDS ≥ 10 V, IDS= 10 A)		gfs	6.5	10	---	S($\mho$)
Zero Gate Voltage Drain Current (VDS=max rated voltage, VGS=0 V) (VDS=80% rated VDS, VGS=0 V, TA=125°C)		IDSS	--- ---	--- ---	250 1000	µA
Gate to Source Leakage Forward **Gate to Source Leakage Reverse**	At rated VGS	IGSS	--- ---	--- ---	100 -100	nA
Total Gate Charge **Gate to Source Charge** **Gate to Drain Charge**	VGS=10 Volts 80% rated VDS Rated ID	Qg Qgs Qgd	--- --- ---	40 7 21	60 10 32	nC
Turn on Delay Time **Rise Time** **Turn Off Delay Time** **Fall Time**	VDD=50% rated VDS rated ID RG= 9.1 Ω RD= 5.6Ω	td(on) tr td(off) tf	--- --- --- ---	14 52 45 36	21 77 68 54	nsec
Diode Forward Voltage (IS=rated ID, VGS=0 V, TJ=25°C)		VSD	---	---	2.0	V
Diode Reverse Recovery Time **Reverse Recovery Charge**	TJ=25°C IF=rated ID di/dt=100 A/µsec	trr QRR	120 1.3	250 2.6	530 5.6	nsec µC
Input Capacitance **Output Capacitance** **Reverse Transfer Capacitance**	VGS=0 Volts VDS=25 Volts f= 1 MHz	Ciss Coss Crss	--- --- ---	1300 380 93	--- --- ---	pF

SAFE OPERATING AREA (S.O.A.)
TC = 25 C, D.C. CONDITION

DRAIN CURRENT (ID)

DRAIN TO SOURCE VOLTAGE (VDS)