


Solid State Devices, Inc.

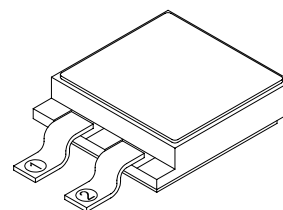
14701 Firestone Blvd * La Mirada, Ca 90638
 Phone: (562) 404-4474 * Fax: (562) 404-1773
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Designer's Data Sheet
FEATURES:

- Extremely Low Forward Voltage Drop
- Low Reverse Leakage
- Hermetically Sealed Custom Surface Mount Package
- Guard Ring for Overvoltage Protection
- Eutectic Die Attach
- 175 °C Operating Junction Temperature
- TX, TXV, and Space Level Screening Available

SSR1008G
SSR1009G
SSR1010G

10AMPS
60 - 100 VOLTS
SCHOTTKY
RECTIFIER

CERPACK (G)


MAXIMUM RATINGS		Symbol	Value	Units
Peak Repetitive Reverse and DC Blocking Voltage	SSR1008G SSR1009G SSR1010G	V_{RRM} V_{RWM} V_R	80 90 100	Volts
Average Rectified Forward Current ^{1/} (Resistive Load, 60 Hz, Sine Wave, $T_A = 25^\circ\text{C}$)		I_O	10	Amps
Peak Surge Current ^{1/} (8.3 ms Pulse, Half Sine Wave Superimposed on I_O , Allow Junction to Reach Equilibrium Between Pulses, $T_A = 25^\circ\text{C}$)		I_{FSM}	200	Amps
Operating & Storage Temperature		$T_{OP} \text{ \& } T_{stg}$	-65 to +175	°C
Maximum Thermal Resistance ^{1/} (Junction to Case)		$R_{\theta JC}$	1.2	°C/W

Notes:

^{1/} For optimal performance, leads 1 & 2 should be connected together.

NOTE: All specifications are subject to change without notification.
 SCD's for these devices should be reviewed by SSDI prior to release.

DATA SHEET #: RS0203E

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ELECTRICAL CHARACTERISTICS		Symbol	Min	Typ	Max	Units
Instantaneous Forward Voltage Drop ($T_A = 25^\circ\text{C}$, 300 μsec pulse)	$I_F = 1\text{ A}$	V_{F1}	—	—	0.56	V_{DC}
	$I_F = 5\text{ A}$		—	—	0.72	
	$I_F = 10\text{ A}$		—	—	0.82	
Instantaneous Forward Voltage Drop ($I_F = 5\text{ A}$, 300 μsec pulse)	$T_A = -55^\circ\text{C}$	V_{F2}	—	—	0.79	V_{DC}
Reverse Leakage Current (Rated V_R , $T_A = 25^\circ\text{C}$, 300 μsec pulse minimum)		I_{R1}	—	—	100	μA
Reverse Leakage Current (Rated V_R , $T_A = 100^\circ\text{C}$, 300 μsec pulse minimum)		I_{R2}	—	—	5	mA
Junction Capacitance ($V_R = 10\text{V}$, $f = 1\text{MHz}$, $T_A = 25^\circ\text{C}$)		C_J	—	—	400	pF

CASE OUTLINE:

CERPACK (Suffix G)

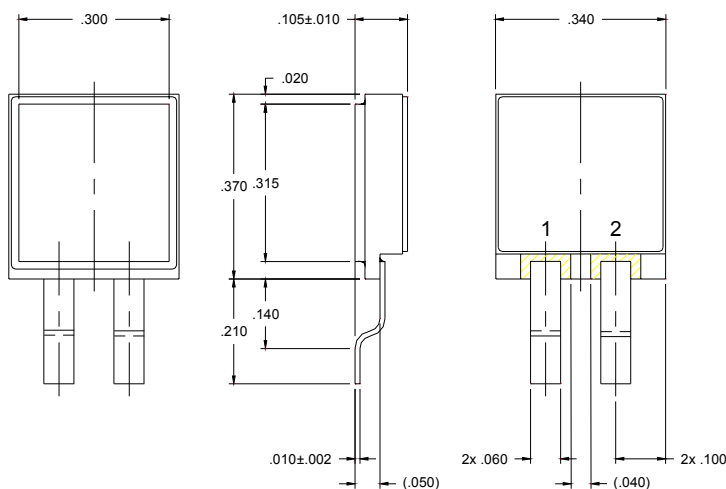
PIN OUT:

PIN 1: ANODE

PIN 2: ANODE

TAB: CATHODE

Note: For optimal
performance, connect
Anode terminals together.



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