



Solid State Devices, Inc.

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Designer's Data Sheet

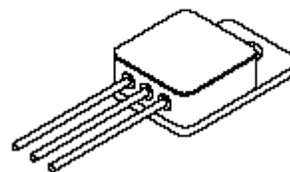
FEATURES:

- Isolated Low Profile Package
- Guard Ring for Overvoltage Protection
- -65 °C to 175°C Operating and Storage Temperature
- Reverse Voltage to 45 Volts
- Low Reverse Leakage
- Hermetically Sealed
- Very Low Forward Voltage Drop
- Available in Common Anode (CAJ) and Doubler (DJ) Configuration
- Higher Voltages Available. Consult Factory.
- TX, TX-V, and S-Level Screening Available. Consult Factory.

SSR2045CTJ

**20 AMPS
45 VOLTS
ISOLATED FLAT PACK
POSITIVE CENTER TAP
SCHOTTKY**

TO-257(J)



MAXIMUM RATINGS	Symbol	Value	Units
Peak Repetitive Reverse Voltage (Per Leg) and DC Blocking Voltage (Per Leg)	V_{RRM} V_R	45	Volts
RMS Reverse Voltage (Per Leg)	V_r	32	Volts
Half Wave Rectified Forward Current, Averaged Over Full Cycle (Resistive Load, 60 Hz, Sine Wave, $T_C=25^\circ C$)	I_O	20	Amps
Peak Repetitive Forward Current ($T_C=55^\circ C$, 8.3 ms Pulse, Allow Junction to Reach Equilibrium Between Pulses)	I_{FRM}	40	Amps
Peak Surge Current ($T_C=55^\circ C$, 8.3 ms Pulse, Superimposed on Rated Current at Rated Voltage)	I_{FSM}	200	Amps
Operating and Storage Temperature	T_J & T_{stg}	-65 to +175	°C

THERMAL CHARACTERISTICS	Symbol	Value	Units
Thermal Resistance, Junction to Case (Both legs tied together)	$R_{\theta JC}$	2.0	°C/W

NOTE: All specifications are subject to change without notification. SCD's for these devices should be reviewed by SSDI prior to release.

DATA SHEET #: SH0044A

DOC



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ELECTRICAL CHARACTERISTICS	Symbol	Value	Unit
Max Instantaneous Forward Voltage Drop, per leg * ($T_A = 25^\circ\text{C}$)	V_F	$I_F = 3$ Amps 0.50	Volts
		$I_F = 5$ Amps 0.55	
		$I_F = 10$ Amps 0.65	
Max Instantaneous Forward Voltage Drop, per leg * ($I_F = 5$ Amps, $T_A = 100^\circ\text{C}$)	V_F	0.5	Volts
Max Reverse Leakage Current per leg * ($V_R = 45$ V, $T_A = 100^\circ\text{C}$)	I_R	10	mA
Max Reverse Leakage Current, per leg * ($V_R = 45$ V, $T_A = 25^\circ\text{C}$)	I_R	200	μA
Max Junction Capacitance, per leg ($V_R = 10$ V _{DC} , $T_A = 25^\circ\text{C}$)	C_J	400	pF

NOTES:

* Pulse Test: Pulse Width = 300 μsec , Duty Cycle = 2%

1. Unless Otherwise Specified, All Electrical Characteristics @25 $^\circ\text{C}$

PACKAGE OUTLINE: TO-257 (J)

The drawing shows a top view and a side view of the TO-257 (J) package. Dimensions include: top view width of .420 (tolerance .410), hole diameter of .150 (tolerance .140), total height of .537 (tolerance .527), and pin spacing of .105 (tolerance .095). The side view shows a total height of .210 (tolerance .190) and a lead diameter of .045 (tolerance .035).

Pin configurations are shown for three types:

- SSR2045CTJ:** Common Cathode configuration with pins 1, 2, and 3.
- SSR2045CAJ:** Common Anode configuration with pins 1, 2, and 3.
- SSR2045DJ:** Doubler configuration with pins 1, 2, and 3.

*For information on curves, contact the Factory Representative for Engineering Assistance.