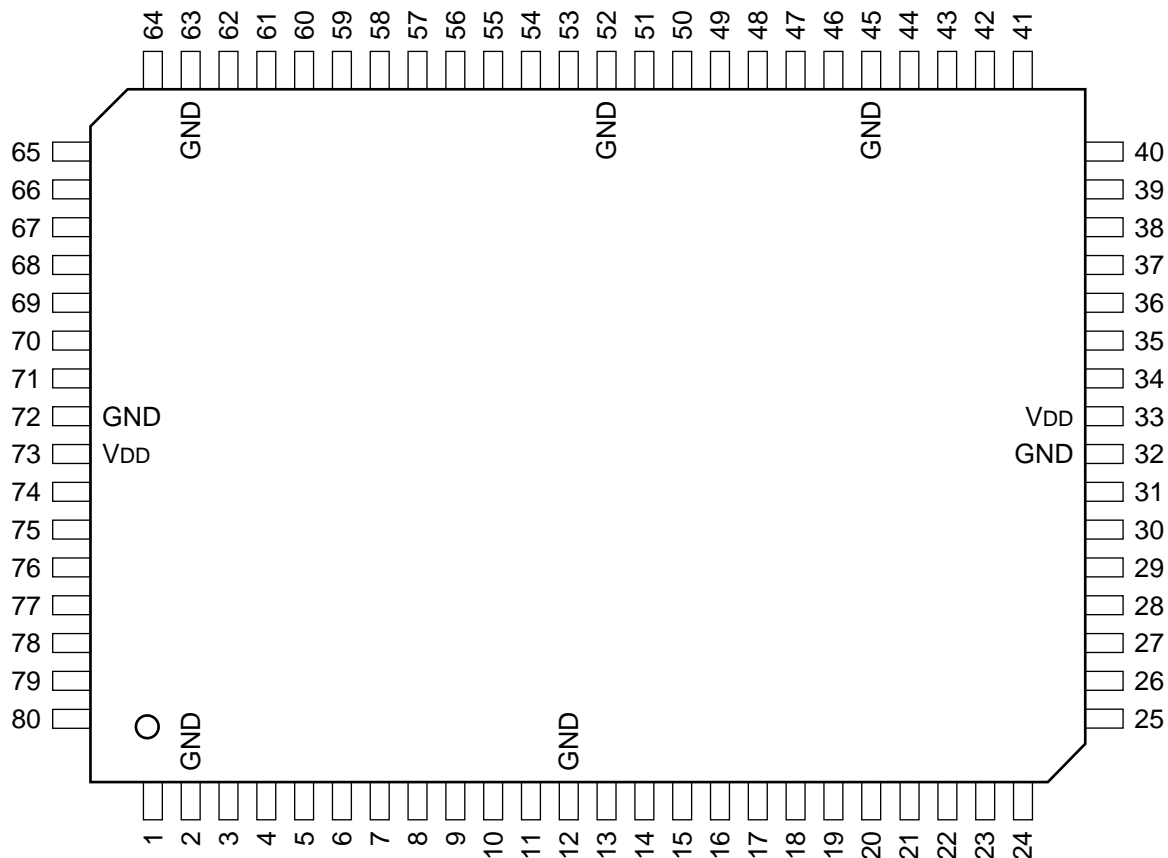


## C-MOS DIGITAL AUDIO SIGNAL PROCESSOR

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	O	EA0	21	O	TRDT	41	I	LRK0	61	I/O	ED8
2	—	GND	22	I	RVDT	42	I	LRK1	62	I/O	ED9
3	O	EA1	23	I	SCK	43	I	BCK0	63	—	GND
4	O	EA2	24	I	XLAT	44	I	BCK1	64	I/O	ED10
5	O	EA3	25	I	TA5	45	—	GND	65	I/O	ED11
6	O	EA4	26	I	TA4	46	O	D2BK	66	O	XOE
7	O	EA5	27	O	BFOT	47	O	D2LR	67	O	CAS
8	O	EA6	28	O	CLKO	48	O	D4BK	68	I/O	ED12
9	O	EA7	29	I	CLKI	49	O	D4LR	69	I/O	ED13
10	O	EA8	30	I	TA3	50	I/O	ED0	70	I	TD15
11	I	TA7	31	I	TA2	51	I	TST1	71	I	TD14
12	—	GND	32	—	GND	52	—	GND	72	—	GND
13	I	TA6	33	—	VDD	53	I	TST0	73	—	VDD
14	I	XRST	34	I	TA1	54	I/O	ED1	74	I	TD13
15	O	SP0	35	I	TA0	55	I/O	ED2	75	I	TD12
16	O	SP1	36	O	SOC	56	I/O	ED3	76	I/O	ED14
17	O	SP2	37	O	SOB	57	I/O	ED4	77	I/O	ED15
18	O	MOVF	38	O	SOA	58	I/O	ED5	78	O	XWE
19	O	AOVF	39	O	SIB	59	I/O	ED6	79	O	RAS
20	O	REDY	40	O	SIA	60	I/O	ED7	80	O	EA9

**INPUT**

BCK0, BCK1 : BIT CLOCK  
CLKI : CLOCK  
LRK0, LRK1 : LR CLOCK  
RVDT : DATA FOR HC I/F  
SCK : SIFT CLOCK FOR HC I/F  
SIA, SIB : SERIAL DATA  
TA0 - TA7 : TEST  
TD12 - TD15 : TEST  
TST0, TST1 : TEST (NORMAL "L")  
XLAT : MODE PARTITION SIGNAL  
XRST : RESET

**OUTPUT**

AOVF : ALU OVERFLOW SIGNAL  
BFOT : CLOCK BUFFER  
CAS : COLUMN-ADDRESS STROBE FOR EXT. DATA RAM  
CLKO : CLOCK  
D2BK : 1/2 BIT CLOCK  
D2LR : 1/2 LR CLOCK  
D4BK : 1/4 BIT CLOCK  
D4LR : 1/4 LR CLOCK  
EA0 - EA9 : ADDRESS FOR EXT. DATA RAM  
MOVF : MAC OVERFLOW SIGNAL  
RAS : ROW-ADDRESS STROBE FOR EXT. DATA RAM  
REDY : READY SIGNAL FOR HC I/F  
SOA-SOC : SERIAL DATA  
SP0-SP2 : STATIC PORT 0, 1, 2  
TRDT : DATA FOR HC I/F  
XOE : OUTPUT ENABLE FOR EXT. DATA RAM  
XWE : WRITE ENABLE FOR EXT. DATA RAM

**INPUT/OUTPUT**

ED0 - ED15 : DATA INPUT/OUTPUT FOR EXT. DATA RAM