

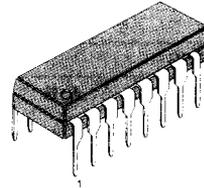
4 × 4 CROSSPOINT SWITCH WITH CONTROL MEMORY

The KT8592 consists of 4 × 4 matrix-array with 16 latches. Any one of 16 switches can be selected by applying its address to the device and a pulse to the strobe input pin. The selected switch can be turned on or off by applying a logical one or zero to the data in and the strobe input at logical one.

FEATURES

- Low on resistance (Typ: 75Ω at $V_{DD} = 12V$)
- Internal control latches
- 2V_{pp} analog signal capability
- High linearity: 0.5% distortion (Typ) at $f = 1KHz$, $V_{IN} = 5V_{p-p}$, $V_{DD} = 10V$, $R_L = 1K\Omega$

16 DIP



ORDERING INFORMATION

Device	Package	Operating Temperature
KT8592N	16DIP	-40 ~ +85°C

BLOCK DIAGRAM

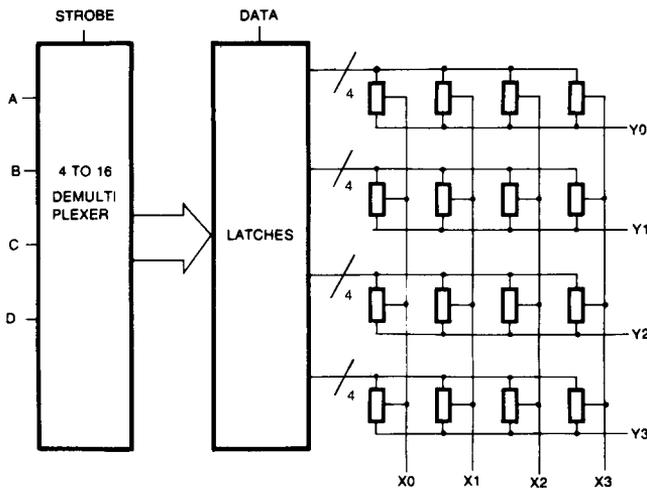


Fig. 1

PIN CONFIGURATION

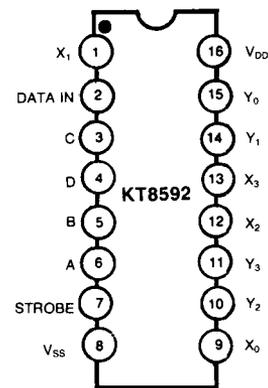


Fig. 2

PIN DESCRIPTION

Pin No	Symbol	Description
POWER		
16	V _{DD}	Positive power supply
8	V _{SS}	Negative power supply
ADDRESS		
6, 5	A, B	X Address lines. These 2 pins are used to select one of the 4 rows of switches. Refer to the thruth table.
3, 4	C, D	Y Address lines. These 2 pins are used to select one of the 4 columns of switches. Refer to the thruth table.
CONTROL		
2	DATA-IN	This input determines if the selected switch will be turned On (closed) or Off (opened). If the pin is held high. The selected switch will be closed. If the pin is held low, the switch will be opened.
7	STROBE	This pin enables whatever action is selected by the address and data pins. When the strobe pin is held low, no switch openings or closings take place. When the strobe pin is holding high. The switch addressed by the selected lines will be opened or closed. (Depending upon the state of the data pin)
DATA		
9,1,12,13	X0-X3	Analog input/outputs. These pins are connected to the rows of the switch matrix.
15,14,10,11	Y0-Y3	Analog input/outputs. These pins are connected to the columns of the switch matrix.

ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

Characteristic	Symbol	Value	Unit
Supply Voltage	V _{DD}	-0.5 - +20	V
Input Voltage, All Inputs	V _I	-0.5 - V _{DD} + 0.5	V
Input Current (Analog Inputs)	I _I	± 10	mA
Power Dissipation	P _D	500	mW
Operating Temperature Range	T _{OPR}	-40 - +85	°C
Storage Temperature Range	T _{STG}	-65 - +150	°C

4

DC ELECTRICAL CHARACTERISTICS

Characteristic	Symbol	Test Conditions	Value						Unit	
			V _{IN} (V)	V _{DD} (V)	-40°C	85°C	25°C			
							Min	Typ		Max
CONTROLS										
Input Low Voltage	V _{IL}	OFF Switch I _L < 0.2 µA		5	1.5			1.5	V	
				10	3		3			
				15	4		4			
Input High Voltage	V _{IH}	ON Switch see Ron Charac.		5	3.5	3.5			V	
				10	7	7				
				15	11	11				
Input Current	I _I	Any Control	0/18	18	± 0.1	± 1		± 10 ⁻⁵	± 0.1	µA
CROSSPOINTS										
Quiescent Current	I _{DD(Q)}			5	5	150	0.04	5	µA	
				10	10	300	0.04	10		
				15	20	600	0.04	20		
ON Resistance	R _{ON}	Any Switch Vis = 0 to V _{DD}		5	1000	1440	225	1250	Ω	
				10	145	205	85	180		
				12	110	155	75	135		
				15	75	110	65	95		
ΔON Resistance	ΔR _{ON}	Between Any Two Switches		5			35		Ω	
				10			20			
				12			18			
				15			15			
OFF Switch Leakage Current	I _{LKG(OFF)}	All Switches	0/18	18	± 100	± 100		± 1	± 100	nA

AC ELECTRICAL CHARACTERISTICS (Ta = 25°C)

Characteristic	Symbol	Test Conditions				Value			Unit	
		f _i (KHz)	R _L (KΩ)	V _{IS} (V)	V _{DD} (V)	Min	Typ	Max		
CONTROLS						See Fig				
Propagation Delay Time Strobe to Output (Switch Turn on to High Level)	t _{D (PHZ)1}	R _L = 1KΩ C _L = 50pF t _r , t _f = 20ns	—	—	5	5 10 15	—	300 125 80	600 250 160	ns
Data in to Output (Turn on to High Level)	t _{PZH1}		—	—	6	5 10 15	—	110 40 25	220 80 50	
Address to Output (Turn on to High Level)	t _{PZH2}		—	—	7	5 10 15	—	350 135 90	700 270 180	
Propagation Delay Time Strobe to Output (Switch Turn OFF)	t _{D (PHZ)2}		—	—	5	5 10 15	—	165 85 70	330 170 140	
Data in to Output (Turn on to Low Level)	t _{PHL}		—	—	6	5 10 15	—	210 110 100	420 220 200	
Address to Output (Turn OFF)	t _{PHZ}		—	—	7	5 10 15	—	435 210 160	870 420 320	
CROSSPOINTS										
Propagation Delay Time (Switch ON) Signal Input to Output	t _{D (PHL)} t _{D (PLH)}	C _L = 50pF t _r , t _f = 20ns Fig. 4	—	10	5 10 15	5 10 15	—	30 15 10	60 30 20	ns
Frequency Response (Any Switch ON) 20 log (V _{OS} /V _{IS}) = -3dB	f _{RES}	Sine Wave Input	1	1	5	10	—	40	—	MHz
Sine Wave Distortion	THD		1	1	5	10	—	0.5	—	%
Feedthrough (All Switches OFF)	G _F	Sine Wave Input Fig. 5	1.6	1	5	10	—	-80	—	dB
Frequency for Signal Crosstalk Attenuation of 40dB Attenuation of 110dB	f _{CT}	Sine Wave Input	—	1	10	10	—	1.5 0.1	—	MHz KHz
C, Capacitance Xn to Ground Yn to Ground Feedthrough	C _{XN} C _{YN} C _F		—	—	—	5 ~ 15 5 ~ 15	—	18 30 0.4	—	pF

AC ELECTRICAL CHARACTERISTICS (Continued)

Characteristic	Symbol	Test Conditions	Test Conditions			Value			Unit	
			R _L (K Ω)	V _{IS} (V)	See Fig.	V _{DD} (V)	Min	Typ		Max
Setup Time Data in to Strobe, Address	t _{SU}	R _L = 1K Ω , C _L = 50pF t _r , t _f = 20ns	—	—5	10 7	5 — 15	25	95 50 15	190 30	ns
			—	—	5	5 10 15	—	180 110 35	360 220 70	
Hold Time Data in to Strobe, Address	t _H		—	—	—	5 10 15	0.6 1.6 2.5	1.2 3.2 5	—	MHz
Switching Frequency	f _{SW}		—	—	—	5 10 15	—	300 120 90	600 240 180	ns
Strobe Pulse Width	t _W		—	—	—	5 10 15	—	75	—	mV (Peak)
Control Crosstalk Data In, Address or Strobe to Output	V _{OUT}	Square Wave Input t _r , t _f = 20ns	10	10	—	10	—	—	—	mV (Peak)
Input Capacitance	C _I	Any Control Input	—	—	—	—	—	5	7.5	pF

TEST CIRCUIT

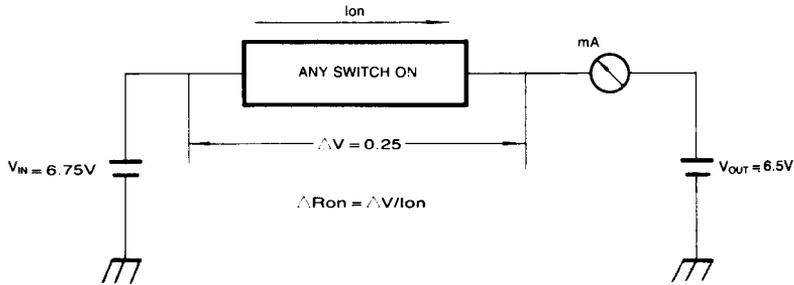
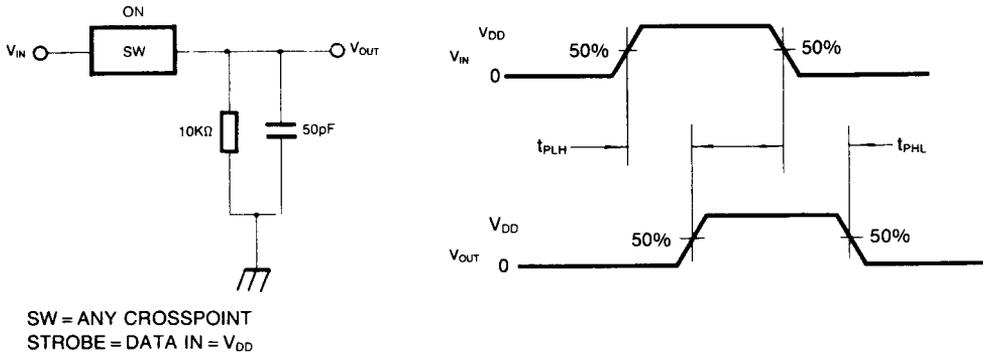


Fig. 3 Ron Measurement



**Fig. 4 Propagation Delay Time & Waveforms
 (Signal Input to Signal Output, Switch on)**

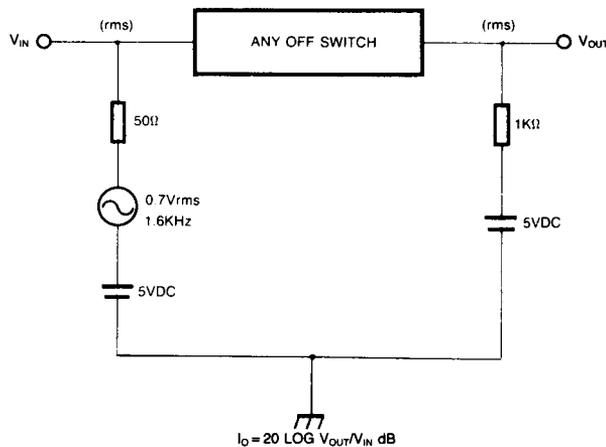


Fig. 5 OFF Isolation Measurement (Feedthrough)

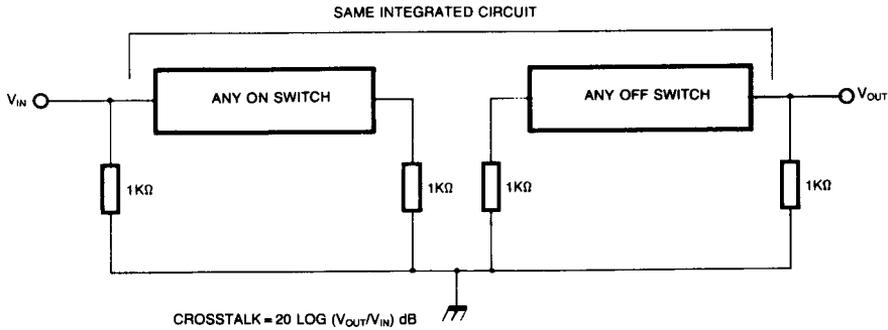


Fig. 6 Crosstalk Measurement

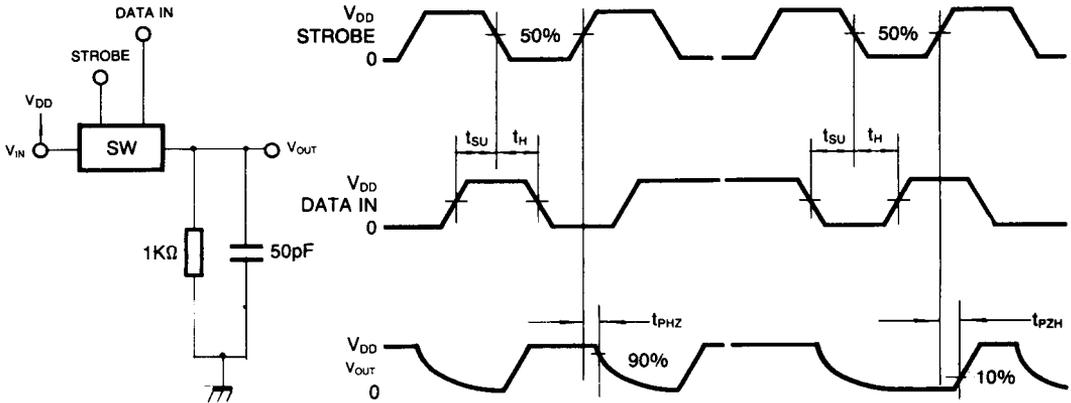
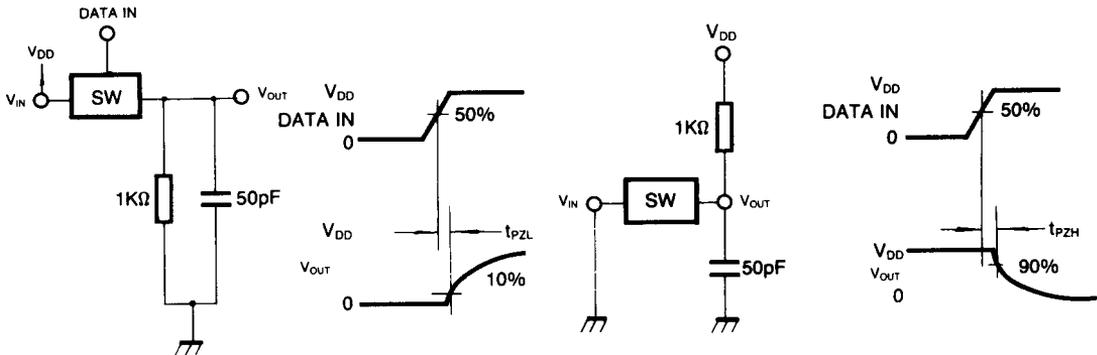
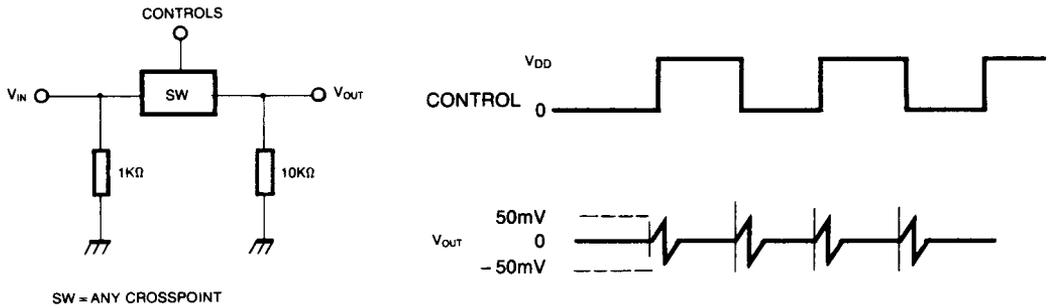
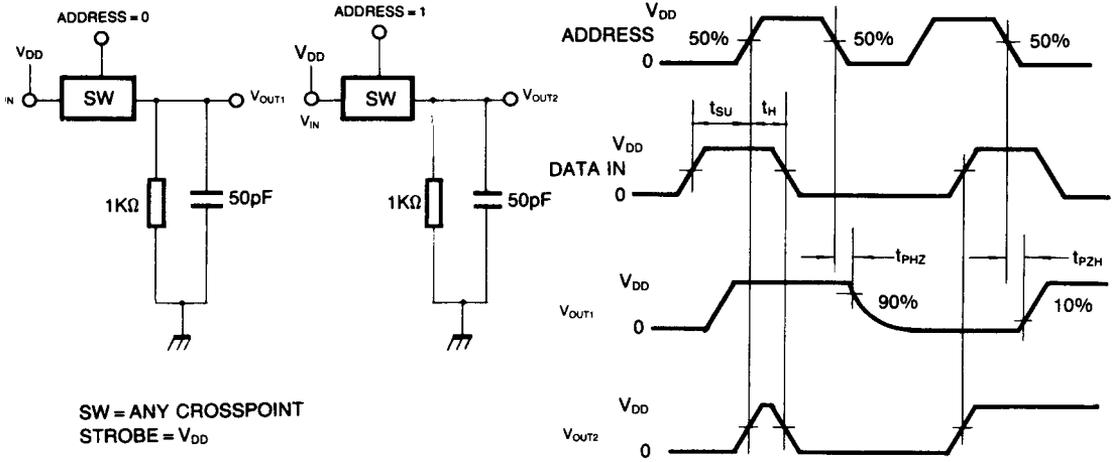


Fig. 7 Propagation Delay Time & Waveforms
(Strobe to Signal Output, Switch Turn On or Turn Off)



SW = ANY CROSSPOINT
STROBE = V_{DD}

Fig. 8 Propagation Delay Time & Waveforms
(Strobe to Signal Output, Switch Turn On to High or Low Level)



TRUTH TABLE

ADDRESS				CONNECTIONS
A	B	C	D	
0	0	0	0	X0 - Y0
1	0	0	0	X1 - Y0
0	1	0	0	X2 - Y0
1	1	0	0	X3 - Y0
0	0	1	0	X0 - Y1
1	0	1	0	X1 - Y1
0	1	1	0	X2 - Y1
1	1	1	0	X3 - Y1
0	0	0	1	X0 - Y2
1	0	0	1	X1 - Y2
0	1	0	1	X2 - Y2
1	1	0	1	X3 - Y2
0	0	1	1	X0 - Y3
1	0	1	1	X1 - Y3
0	1	1	1	X2 - Y3
1	1	1	1	X3 - Y3