

RT2N05M

Composite Transistor With Resistor
For Switching Application
Silicon NPN Epitaxial Type

DESCRIPTION

RT2N05M is composite transistor with built-in bias resistor.

FEATURE

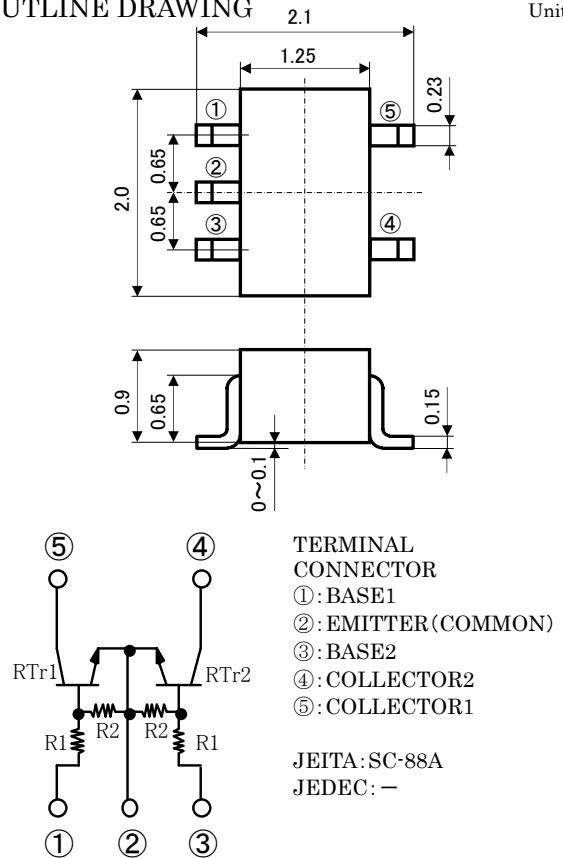
Built-in bias resistor ($R_1=47k\Omega$, $R_2=47k\Omega$)
Mini package for easy mounting

APPLICATION

Inverted circuit, Switching circuit,
Interface circuit, Driver circuit

OUTLINE DRAWING

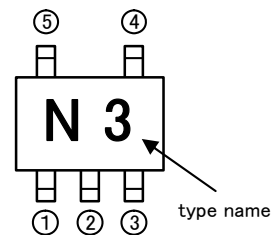
Unit: mm



MAXIMUM RATING ($T_a=25^\circ\text{C}$) (RTr1, RTr2 COMMON)

SYMBOL	PARAMETER	RATING	UNIT
VCBO	Collector to Base voltage	50	V
VEBO	Emitter to Base voltage	10	V
VCEO	Collector to Emitter voltage	50	V
V _{IN}	Input voltage	40	V
I _C	Collector current	100	mA
I _{CM}	Peak Collector current	200	mA
P _T	Total dissipation	200	mW
T _j	Junction temperature	+150	°C
T _{stg}	Storage temperature	-55~+150	°C

MARKING



ELECTRICAL CHARACTERISTICS ($T_a=25^\circ\text{C}$) (RTr1, RTr2 COMMON)

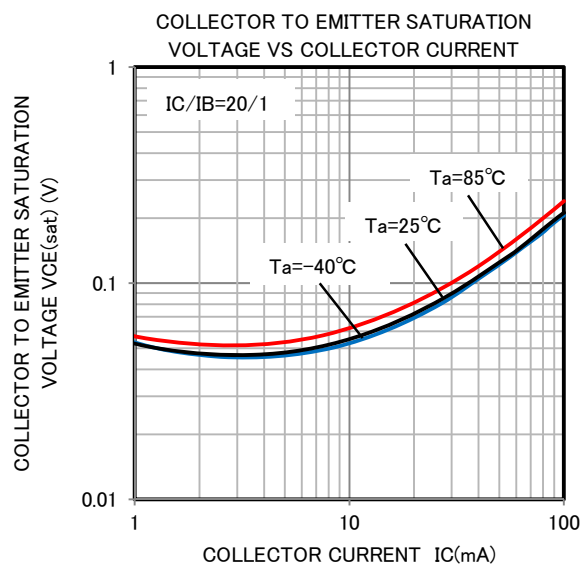
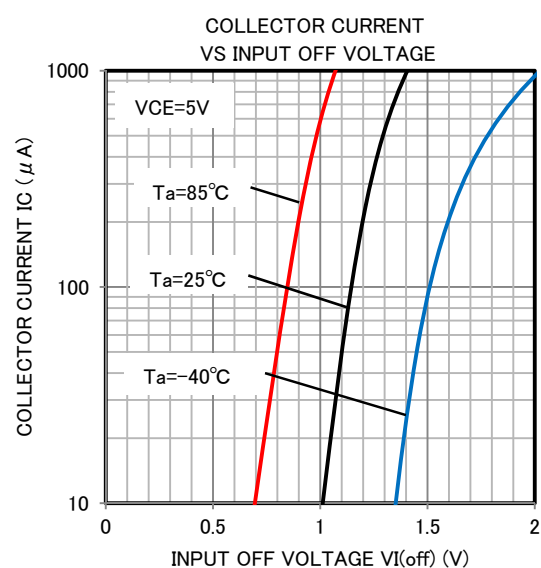
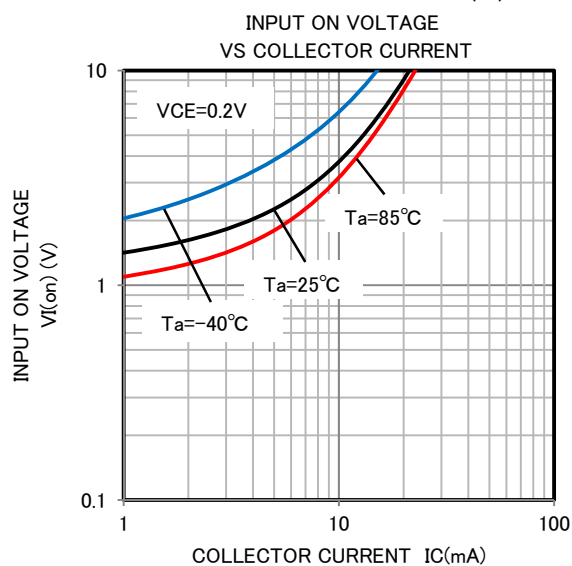
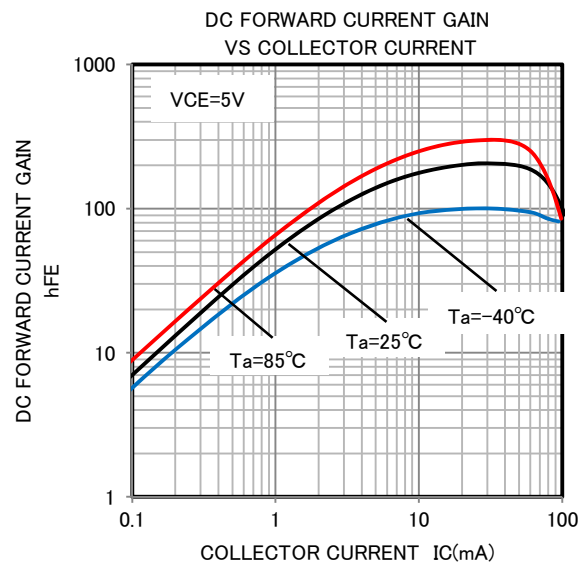
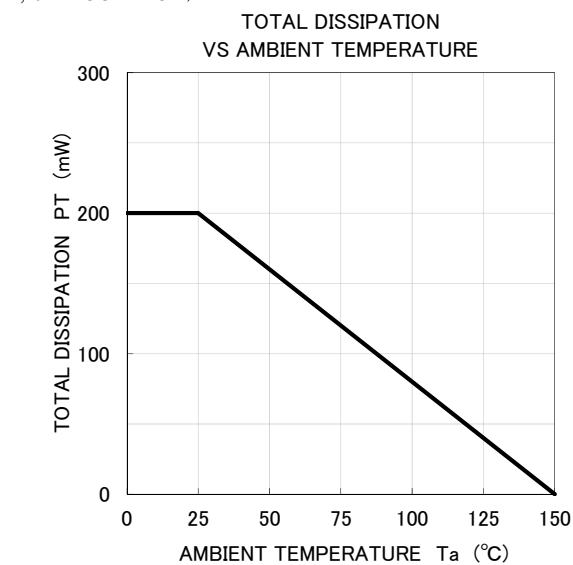
SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
V _{(BR)CEO}	Collector to Emitter breakdown voltage	I _C =100 μ A, R _{BE} = ∞	50	—	—	V
I _{CBO}	Collector cut off current	V _{CB} =50V, I _E =0	—	—	0.1	μ A
I _{EBO}	Emitter cut off current	V _{EB} =5V, I _C =0	41	53	76	μ A
h _{FE}	DC forward current gain	V _{CE} =5V, I _C =5mA	50	—	—	—
V _{CE(sat)}	Collector to Emitter saturation voltage	I _C =10mA, I _B =0.5mA	—	—	0.3	V
V _{I(ON)}	Input on voltage	V _{CE} =0.2V, I _C =5mA	—	2.2	5.0	V
V _{I(OFF)}	Input off voltage	V _{CE} =5V, I _C =100 μ A	0.8	1.1	—	V
R ₁	Input resistor	—	33	47	61	k Ω
R ₂ /R ₁	Resistor ratio	—	0.9	1.0	1.1	—
f _T	Gain band width product	V _{CE} =6V, I _E =-10mA	—	200	—	MHz

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For Switching Application
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TYPICAL CHARACTERISTICS

(RT_{r1}, RT_{r2} COMMON)





Keep safety first in your circuit designs!

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