

Parameter	Ratings	Units
Blocking Voltage	60	V _P
Load Current	150	mA
Max R _{ON}	16	Ω
LED Current to Operate	1	mA

Features

- Low Drive Power Requirements (TTL/CMOS Compatible)
- Arc-Free With No Snubbing Circuits
- 3750V_{rms} Input/Output Isolation
- FCC Compatible
- VDE Compatible
- No EMI/RFI Generation
- Machine Insertable, Wave Solderable
- Surface Mount Tape & Reel Version Available

Applications

- Security
 - Passive Infrared Detectors (PIR)
 - Data Signaling
 - Sensor Circuitry
- Instrumentation
 - Multiplexers
 - Data Acquisition
 - Electronic Switching
 - I/O Subsystems
 - Meters (Watt-Hour, Water, Gas)
 - Medical Equipment-Patient/Equipment Isolation
- Security
- Aerospace
- Industrial Controls

Description

Clare's XAA117 is a dual 1-Form-A Solid State Relay that has two independently controlled, optically coupled MOSFET switches.

The MOSFET switches and photovoltaic die use Clare's patented OptoMOS[®] architecture to provide 3750 V_{rms} of input-to-output isolation. The optically coupled output is controlled by a highly efficient GaAIAs infrared LED.

This dual single-pole OptoMOS relay provides a more compact design solution than discrete single-pole relays in a variety of applications, and saves board space by incorporating both switches in a single 8-Pin package.

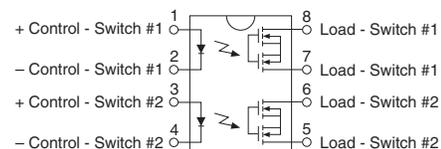
Approvals

- UL Recognized: File Number E76270
- CSA Certified: File Number LR 43639-10

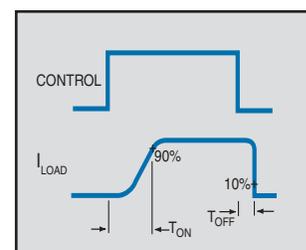
Ordering Information

Part #	Description
XAA117	8-Pin DIP (50/Tube)
XAA117S	8-Pin Surface Mount (50/Tube)
XAA117STR	8-Pin Surface Mount (1,000/Reel)
XAA117P	8-Pin Flat Pack (50/Tube)
XAA117PTR	8-Pin Flat Pack (1,000/Reel)

Pin Configuration



Switching Characteristics of Normally Open (Form A) Devices



Absolute Maximum Ratings

Parameter	Ratings	Units
Blocking Voltage	60	V _P
Reverse Input Voltage	5	V
Input Control Current	50	mA
Peak (10ms)	1	A
Input Power Dissipation ¹	150	mW
Total Power Dissipation ²	800	mW
Isolation Voltage, Input to Output	3750	V _{rms}
Operational Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	°C

¹ Derate Linearly 1.33 mW/°C

² Derate Linearly 6.67 mW/°C

Electrical absolute maximum ratings are at 25°C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

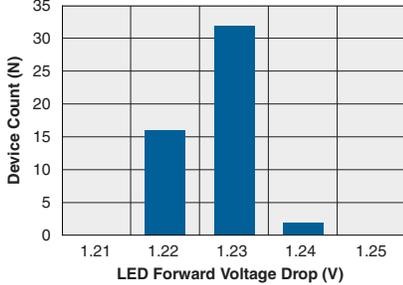
Electrical Characteristics

Parameter	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics @ 25°C						
Load Current						
Continuous ¹	-	I _L	-	-	150	mA
Peak	t = 10ms	I _{LPK}	-	-	400	
On-Resistance	I _L = 150mA	R _{ON}	-	7	16	Ω
Off-State Leakage Current	V _L = 60V	I _{LEAK}	-	-	1	μA
Switching Speeds						
Turn-On	I _F = 5mA, V _L = 10V	T _{ON}	-	0.1	5	ms
Turn-Off		T _{OFF}	-	0.5	5	
Output Capacitance	50V; f = 1MHz	C _{OUT}	-	25	-	pF
Input Characteristics @ 25°C						
Input Control Current	I _L = 150mA	I _F	-	-	1	mA
Input Dropout Current	-	-	0.05	-	-	mA
Input Voltage Drop	I _F = 5mA	V _F	0.9	1.2	1.4	V
Reverse Input Current	V _R = 5V	I _R	-	-	10	μA
Common Characteristics @ 25°C						
Input to Output Capacitance	-	C _{I/O}	-	3	-	pF

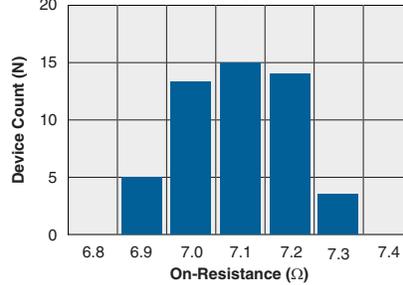
¹ If both poles operate, the load current must be derated so as not to exceed the package power dissipation value.

PERFORMANCE DATA*

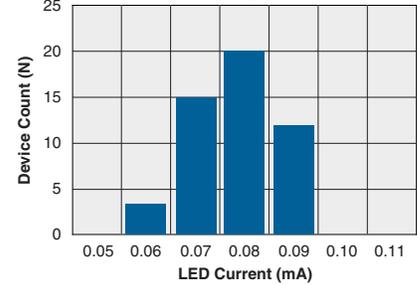
XAA117
Typical LED Forward Voltage Drop
($T_A=25^\circ\text{C}$, $I_F=5\text{mA}$)



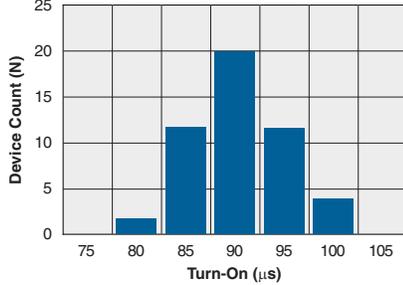
XAA117
Typical On-Resistance Distribution
($T_A=25^\circ\text{C}$, $I_L=100\text{mA}$)



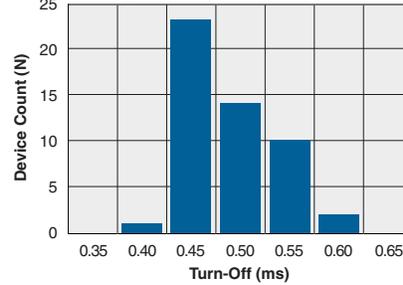
XAA117
Typical I_F for Switch Operation
($T_A=25^\circ\text{C}$, $I_L=100\text{mA}$)



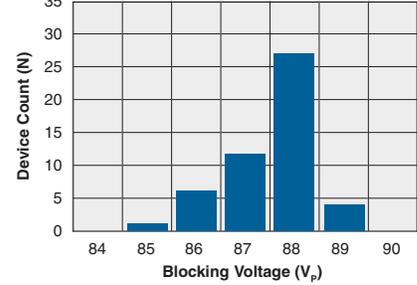
XAA117
Typical Turn-On Time
($T_A=25^\circ\text{C}$, $I_L=100\text{mA}$, $I_F=5\text{mA}$)



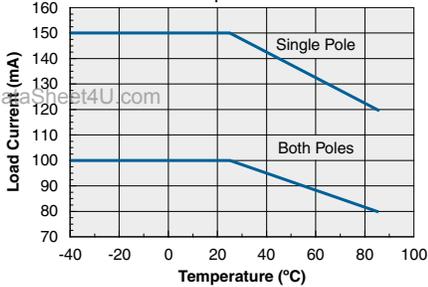
XAA117
Typical Turn-Off Time
($T_A=25^\circ\text{C}$, $I_L=100\text{mA}$, $I_F=5\text{mA}$)



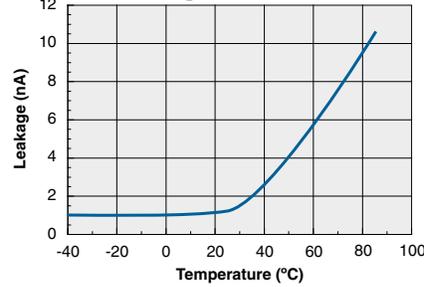
XAA117
Typical Blocking Voltage Distribution
($T_A=25^\circ\text{C}$)



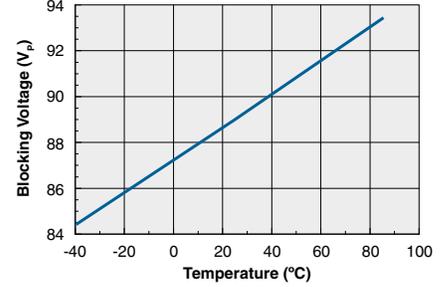
XAA117
Typical Load Current vs. Temperature
($I_F=2\text{mA}$)



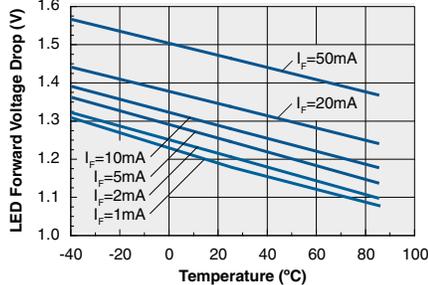
XAA117
Typical Leakage vs. Temperature
Measured Across Pins 5-6 or 7-8
($I_L=\text{max rated}$)



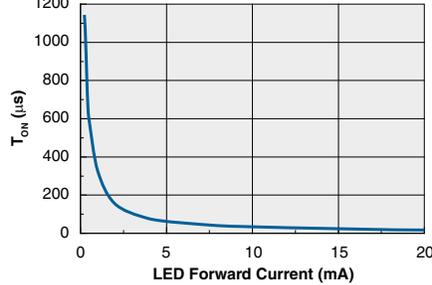
XAA117
Typical Blocking Voltage vs. Temperature



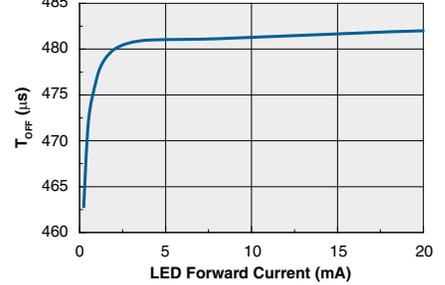
XAA117
Typical LED Forward Voltage Drop vs. Temperature



XAA117
Typical Turn-On vs. LED Forward Current
($I_L=100\text{mA}$)

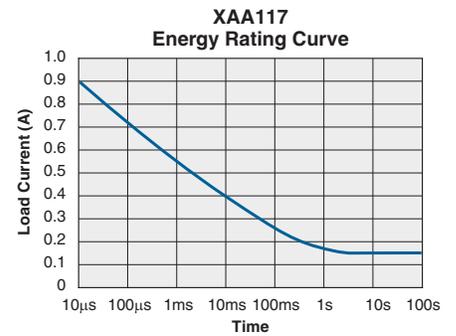
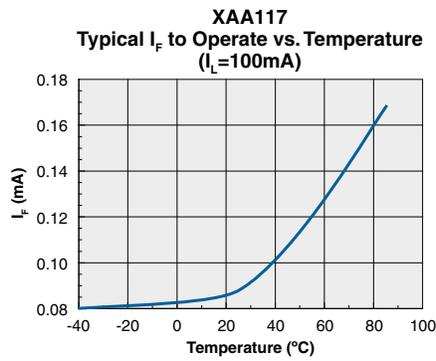
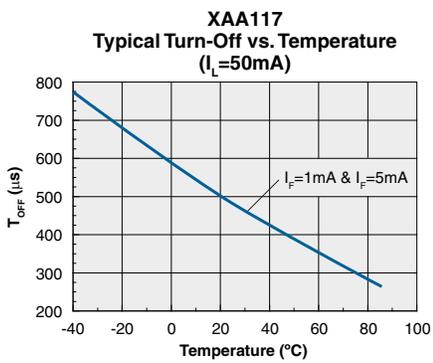
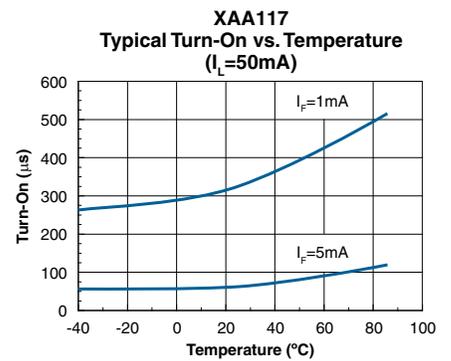
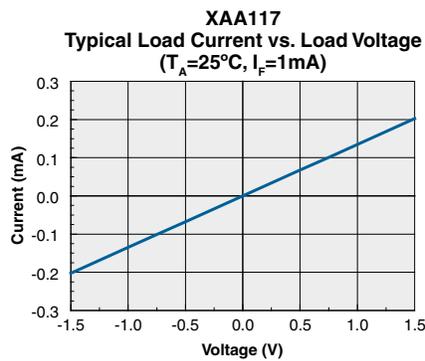
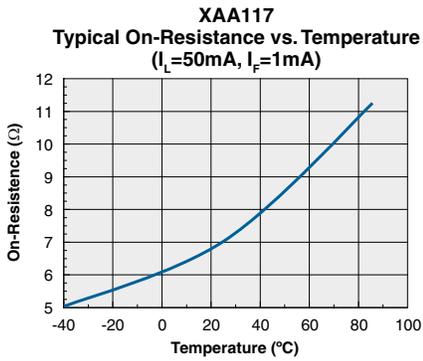


XAA117
Typical Turn-Off vs. LED Forward Current
($I_L=100\text{mA}$)



*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

PERFORMANCE DATA*



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MANUFACTURING INFORMATION

Soldering

For proper assembly, the component must be processed in accordance with the current revision of IPC/JEDEC standard J-STD-020. Failure to follow the recommended guidelines may cause permanent damage to the device resulting in impaired performance and/or a reduced lifetime expectancy.

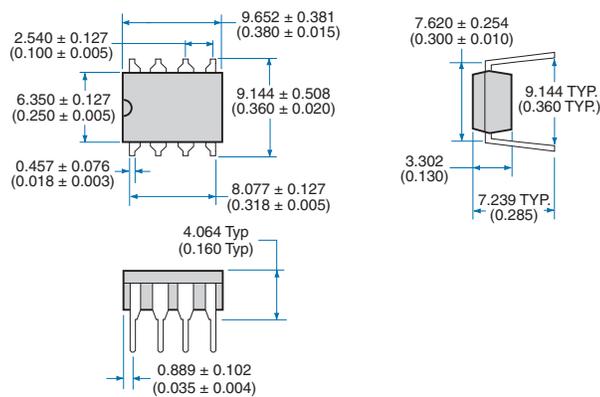
Washing

Clare does not recommend ultrasonic cleaning or the use of chlorinated solvents.

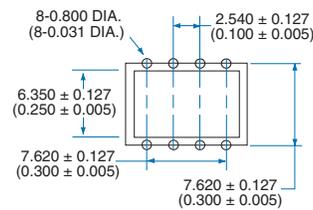


MECHANICAL DIMENSIONS

8-Pin DIP Through-Hole Package

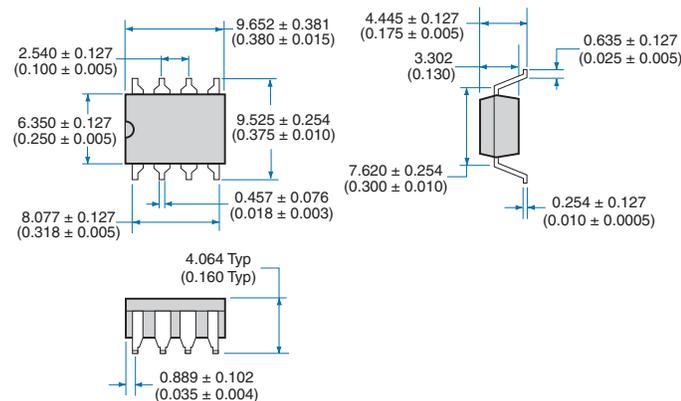


PC Board Pattern

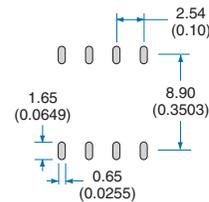


Dimensions
mm
(inches)

8-Pin Surface Mount Package

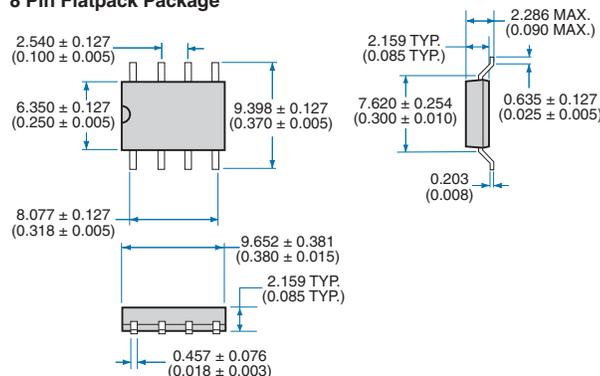


Recommended PCB Land Pattern

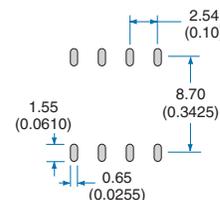


Dimensions
mm
(inches)

8 Pin Flatpack Package



Recommended PCB Land Pattern

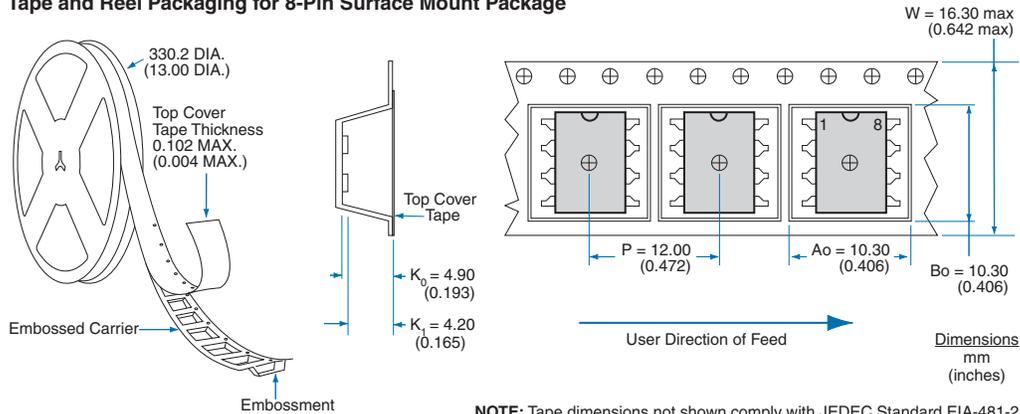


Dimensions
mm
(inches)



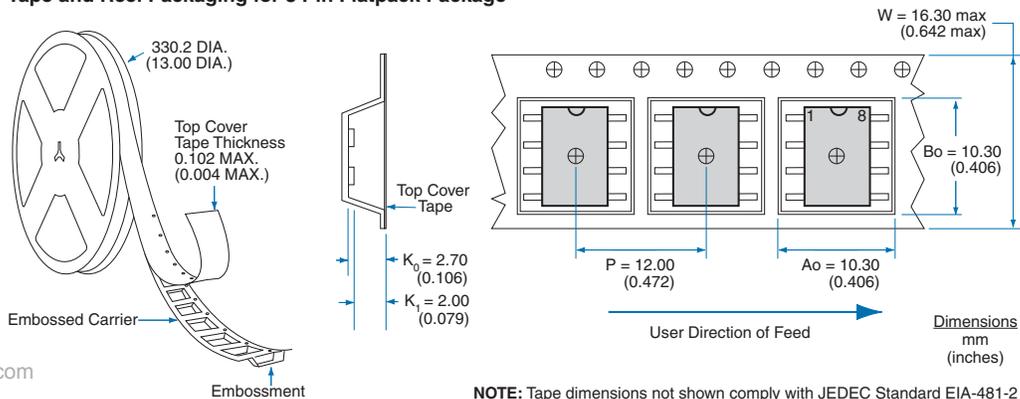
MECHANICAL DIMENSIONS

Tape and Reel Packaging for 8-Pin Surface Mount Package



NOTE: Tape dimensions not shown comply with JEDEC Standard EIA-481-2

Tape and Reel Packaging for 8 Pin Flatpack Package



NOTE: Tape dimensions not shown comply with JEDEC Standard EIA-481-2

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