

# MOSFET – Power, Dual, N-Channel, WDFN 2X2 mm

## 30 V, 4.6 A

### Features

- WDFN Package Provides Exposed Drain Pad for Excellent Thermal Conduction
- 2x2 mm Footprint Same as SC-88
- Lowest  $R_{DS(on)}$  Solution in 2x2 mm Package
- 1.5 V  $R_{DS(on)}$  Rating for Operation at Low Voltage Gate Drive Logic Level
- Low Profile (< 0.8 mm) for Easy Fit in Thin Environments
- This is a Pb-Free Device

### Applications

- DC-DC Converters (Buck and Boost Circuits)
- Low Side Load Switch
- Optimized for Battery and Load Management Applications in Portable Equipment such as, Cell Phones, PDA's, Media Players, etc.
- Level Shift for High Side Load Switch

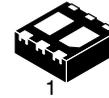
### MAXIMUM RATINGS ( $T_J = 25\text{ }^\circ\text{C}$ unless otherwise noted)

Parameter		Symbol	Value	Unit	
Drain-to-Source Voltage		$V_{DSS}$	30	V	
Gate-to-Source Voltage		$V_{GS}$	$\pm 8.0$	V	
Continuous Drain Current (Note 1)	Steady State	$T_A = 25\text{ }^\circ\text{C}$	$I_D$	3.7	A
		$T_A = 85\text{ }^\circ\text{C}$		2.7	
	$t \leq 5\text{ s}$	$T_A = 25\text{ }^\circ\text{C}$		4.6	
Power Dissipation (Note 1)	Steady State	$T_A = 25\text{ }^\circ\text{C}$	$P_D$	1.5	W
		$t \leq 5\text{ s}$		2.3	
Continuous Drain Current (Note 2)	Steady State	$T_A = 25\text{ }^\circ\text{C}$	$I_D$	2.5	A
		$T_A = 85\text{ }^\circ\text{C}$		1.8	
Power Dissipation (Note 2)	Steady State	$T_A = 25\text{ }^\circ\text{C}$	$P_D$	0.71	W
Pulsed Drain Current	$t_p = 10\text{ }\mu\text{s}$	$I_{DM}$	20	A	
Operating Junction and Storage Temperature		$T_J, T_{STG}$	-55 to 150	$^\circ\text{C}$	
Source Current (Body Diode) (Note 2)		$I_S$	2.0	A	
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)		$T_L$	260	$^\circ\text{C}$	

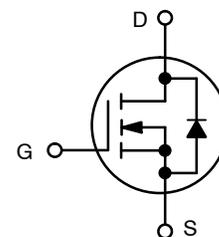
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Surface Mounted on FR4 Board using 1 in sq pad size (Cu area = 1.127 in sq [2 oz] including traces).
2. Surface Mounted on FR4 Board using the minimum recommended pad size of 30 mm<sup>2</sup>, 2 oz Cu.

$V_{(BR)DSS}$	$R_{DS(on)}$ MAX	$I_D$ MAX (Note 1)
30 V	70 m $\Omega$ @ 4.5 V	4.6 A
	90 m $\Omega$ @ 2.5 V	
	125 m $\Omega$ @ 1.8 V	
	250 m $\Omega$ @ 1.5 V	



WDFN6  
CASE 506AN



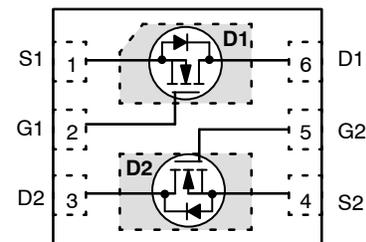
N-CHANNEL MOSFET

### MARKING DIAGRAM



JF = Specific Device Code  
M = Date Code  
▪ = Pb-Free Package  
(Note: Microdot may be in either location)

### PIN CONNECTIONS



(Top View)

### ORDERING INFORMATION

Device	Package	Shipping <sup>†</sup>
NTLJD4116NT1G	WDFN6 (Pb-Free)	3000/Tape & Reel

<sup>†</sup> For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, [BRD8011/D](#).

## THERMAL RESISTANCE RATINGS

Parameter	Symbol	Max	Unit
-----------	--------	-----	------

### SINGLE OPERATION (SELF-HEATED)

Junction-to-Ambient – Steady State (Note 3)	$R_{\theta JA}$	83	°C/W
Junction-to-Ambient – Steady State Min Pad (Note 4)	$R_{\theta JA}$	177	
Junction-to-Ambient – $t \leq 5$ s (Note 3)	$R_{\theta JA}$	54	

### DUAL OPERATION (EQUALLY HEATED)

Junction-to-Ambient – Steady State (Note 3)	$R_{\theta JA}$	58	°C/W
Junction-to-Ambient – Steady State Min Pad (Note 4)	$R_{\theta JA}$	133	
Junction-to-Ambient – $t \leq 5$ s (Note 3)	$R_{\theta JA}$	40	

3. Surface Mounted on FR4 Board using 1 in sq pad size (Cu area = 1.127 in sq [2 oz] including traces).
4. Surface Mounted on FR4 Board using the minimum recommended pad size (30 mm<sup>2</sup>, 2 oz Cu).

**MOSFET ELECTRICAL CHARACTERISTICS** ( $T_J = 25\text{ }^\circ\text{C}$  unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>OFF CHARACTERISTICS</b>						
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	30			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	$V_{(BR)DSS}/T_J$	$I_D = 250\text{ }\mu\text{A}$ , Ref to $25\text{ }^\circ\text{C}$		18.1		mV/ $^\circ\text{C}$
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 24\text{ V}, V_{GS} = 0\text{ V}$	$T_J = 25\text{ }^\circ\text{C}$		1.0	$\mu\text{A}$
			$T_J = 85\text{ }^\circ\text{C}$		10	
Gate-to-Source Leakage Current	$I_{GSS}$	$V_{DS} = 0\text{ V}, V_{GS} = \pm 8.0\text{ V}$			100	nA

**ON CHARACTERISTICS** (Note 5)

Gate Threshold Voltage	$V_{GS(TH)}$	$V_{GS} = V_{DS}, I_D = 250\text{ }\mu\text{A}$	0.4	0.7	1.0	V
Negative Gate Threshold Temperature Coefficient	$V_{GS(TH)}/T_J$			2.8		mV/ $^\circ\text{C}$
Drain-to-Source On-Resistance	$R_{DS(on)}$	$V_{GS} = 4.5\text{ V}, I_D = 2.0\text{ A}$		47	70	m $\Omega$
		$V_{GS} = 2.5\text{ V}, I_D = 2.0\text{ A}$		56	90	
		$V_{GS} = 1.8\text{ V}, I_D = 1.8\text{ A}$		88	125	
		$V_{GS} = 1.5\text{ V}, I_D = 1.5\text{ A}$		133	250	
Forward Transconductance	$g_{FS}$	$V_{DS} = 5.0\text{ V}, I_D = 2.0\text{ A}$		4.5		S

**CHARGES, CAPACITANCES AND GATE RESISTANCE**

Input Capacitance	$C_{ISS}$	$V_{GS} = 0\text{ V}, f = 1.0\text{ MHz}, V_{DS} = 15\text{ V}$		427		pF
Output Capacitance	$C_{OSS}$			51		
Reverse Transfer Capacitance	$C_{RSS}$			32		
Total Gate Charge	$Q_{G(TOT)}$	$V_{GS} = 4.5\text{ V}, V_{DS} = 15\text{ V}, I_D = 2.0\text{ A}$		5.4	6.5	nC
Threshold Gate Charge	$Q_{G(TH)}$			0.5		
Gate-to-Source Charge	$Q_{GS}$			0.8		
Gate-to-Drain Charge	$Q_{GD}$			1.24		
Gate Resistance	$R_G$			0.37		

**SWITCHING CHARACTERISTICS** (Note 6)

Turn-On Delay Time	$t_{d(ON)}$	$V_{GS} = 4.5\text{ V}, V_{DD} = 15\text{ V}, I_D = 2.0\text{ A}, R_G = 2.0\text{ }\Omega$		4.8		ns
Rise Time	$t_r$			11.8		
Turn-Off Delay Time	$t_{d(OFF)}$			14.2		
Fall Time	$t_f$			1.7		

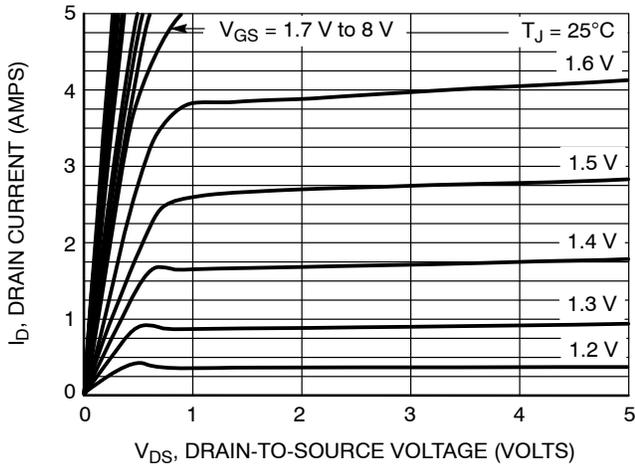
**DRAIN-SOURCE DIODE CHARACTERISTICS**

Forward Recovery Voltage	$V_{SD}$	$V_{GS} = 0\text{ V}, I_S = 2.0\text{ A}$	$T_J = 25\text{ }^\circ\text{C}$		0.78	1.2	V
			$T_J = 125\text{ }^\circ\text{C}$		0.62		
Reverse Recovery Time	$t_{RR}$	$V_{GS} = 0\text{ V}, d_{ISD}/d_t = 100\text{ A}/\mu\text{s}, I_S = 2.0\text{ A}$			10.5		ns
Charge Time	$t_a$				7.6		
Discharge Time	$t_b$				2.9		
Reverse Recovery Time	$Q_{RR}$				5.0		

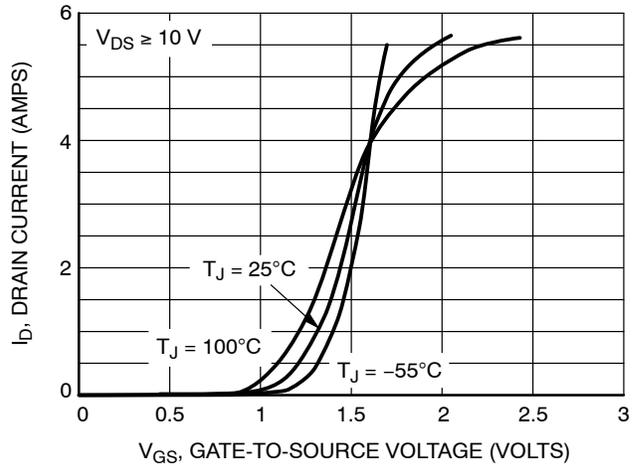
5. Pulse Test: Pulse Width  $\leq 300\text{ }\mu\text{s}$ , Duty Cycle  $\leq 2\%$ .

6. Switching characteristics are independent of operating junction temperatures.

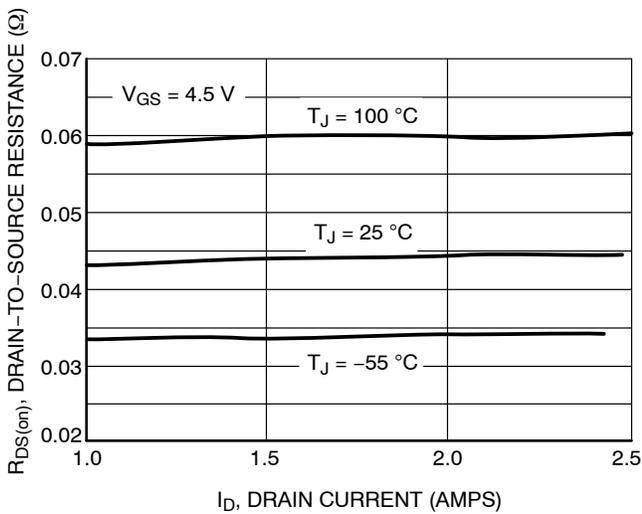
**TYPICAL PERFORMANCE CURVES** ( $T_J = 25\text{ }^\circ\text{C}$  UNLESS OTHERWISE NOTED)



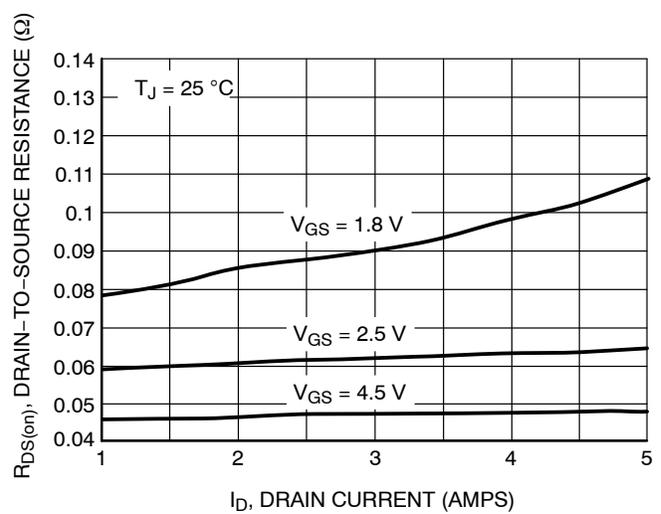
**Figure 1. On-Region Characteristics**



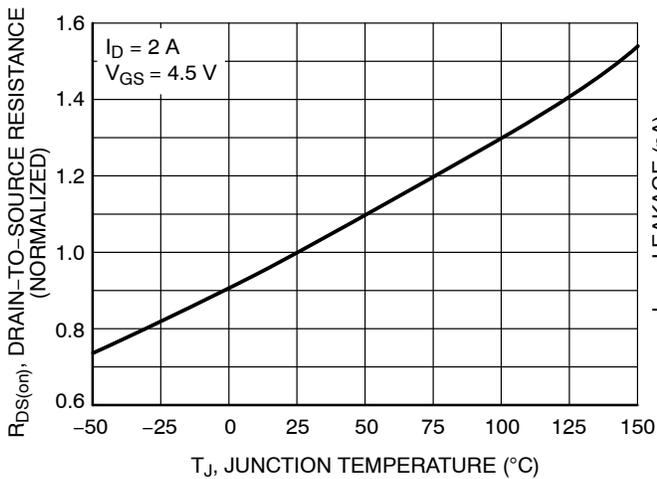
**Figure 2. Transfer Characteristics**



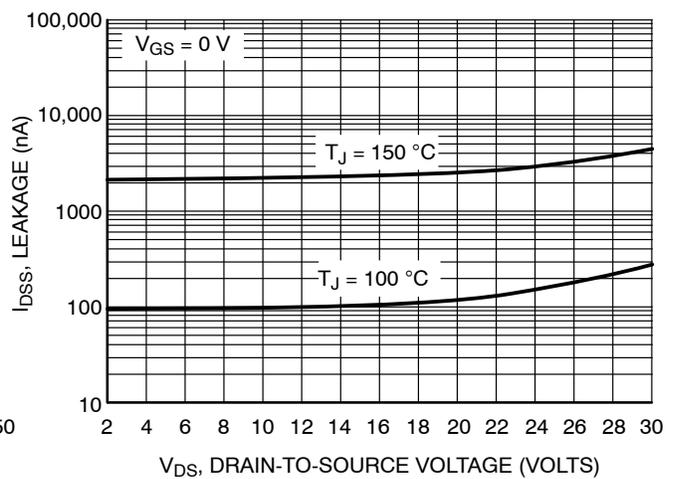
**Figure 3. On-Resistance versus Drain Current**



**Figure 4. On-Resistance versus Drain Current and Gate Voltage**

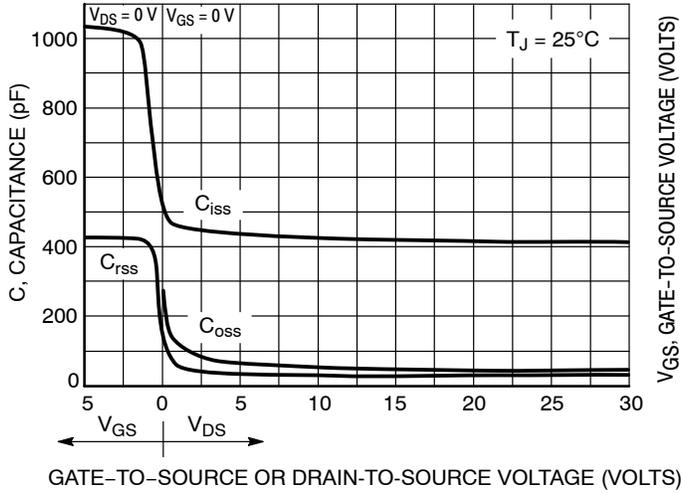


**Figure 5. On-Resistance Variation with Temperature**

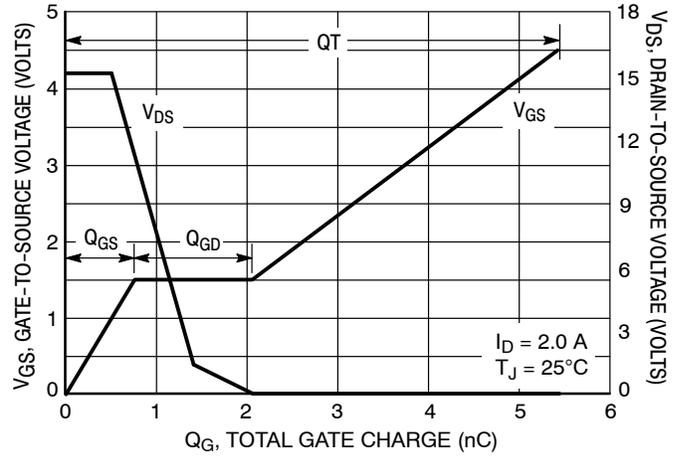


**Figure 6. Drain-to-Source Leakage Current versus Voltage**

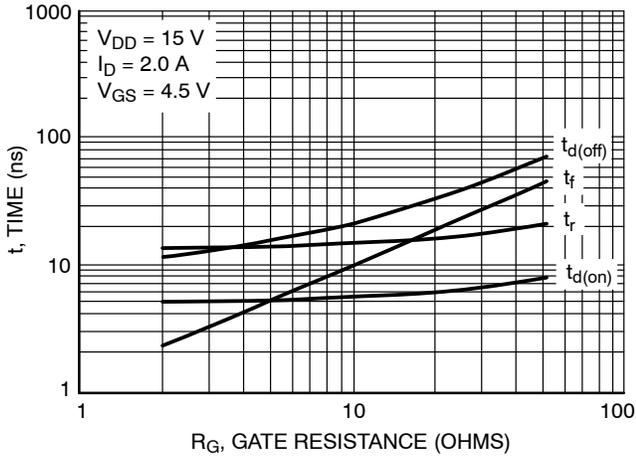
**TYPICAL PERFORMANCE CURVES** ( $T_J = 25^\circ\text{C}$  UNLESS OTHERWISE NOTED)



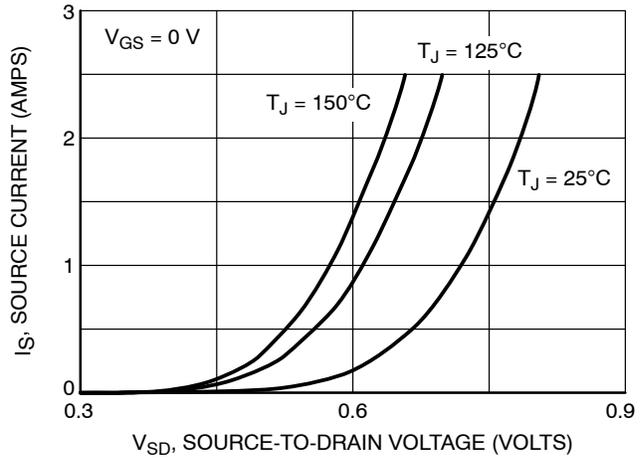
**Figure 7. Capacitance Variation**



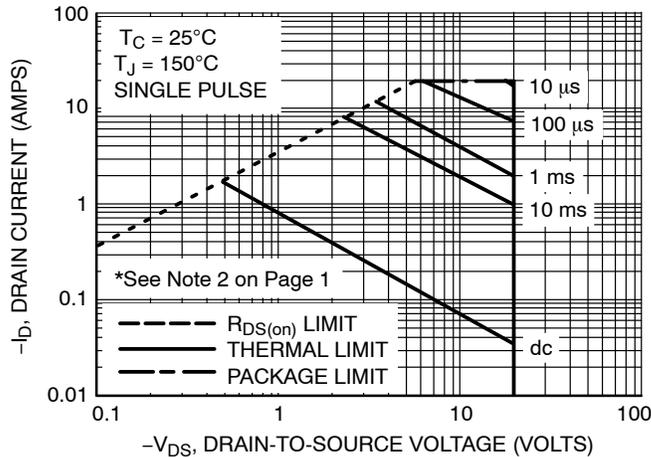
**Figure 8. Gate-To-Source and Drain-To-Source Voltage versus Total Charge**



**Figure 9. Resistive Switching Time Variation versus Gate Resistance**



**Figure 10. Diode Forward Voltage versus Current**



**Figure 11. Maximum Rated Forward Biased Safe Operating Area**

TYPICAL PERFORMANCE CURVES ( $T_J = 25\text{ }^\circ\text{C}$  UNLESS OTHERWISE NOTED)

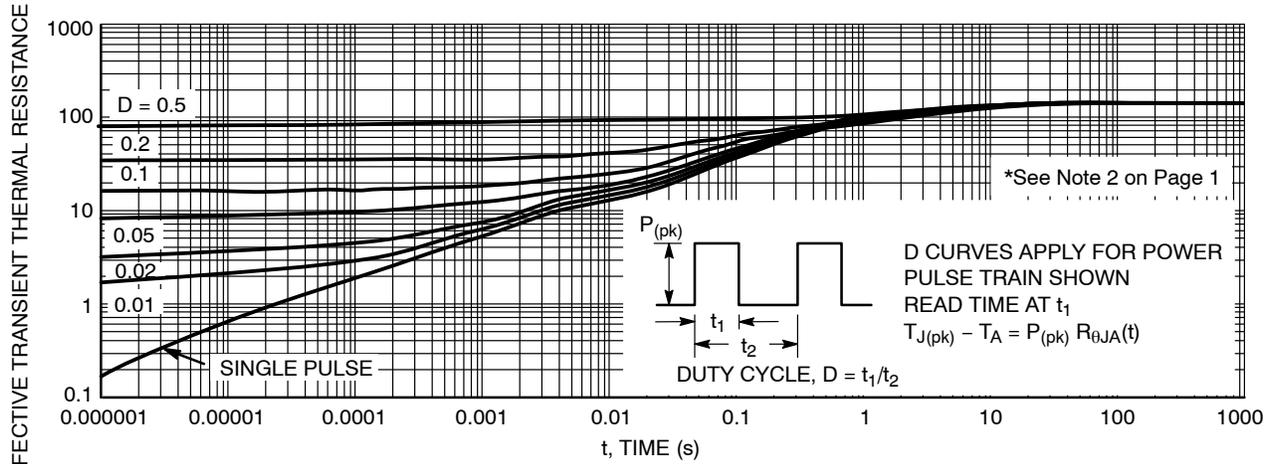


Figure 12. Thermal Response

## REVISION HISTORY

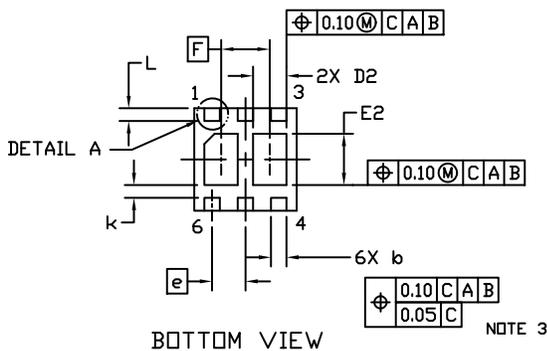
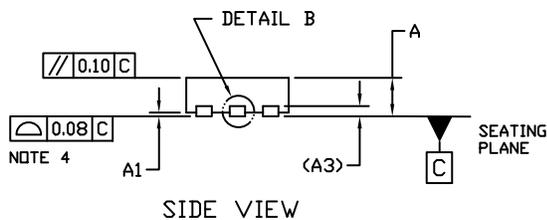
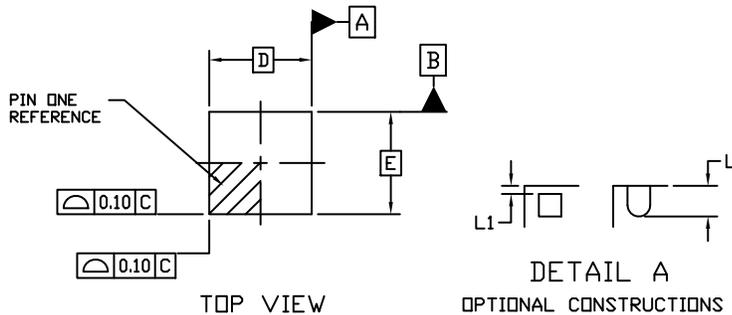
Revision	Description of Changes	Date
5	Rebranded the Data Sheet to <b>onsemi</b> format	1/14/2026

This document has undergone updates prior to the inclusion of this revision history table. The changes tracked here only reflect updates made on the noted approval dates.



**WDFN6 2x2, 0.65P**  
**CASE 506AN**  
**ISSUE H**

DATE 25 JAN 2022



**GENERIC MARKING DIAGRAM\***



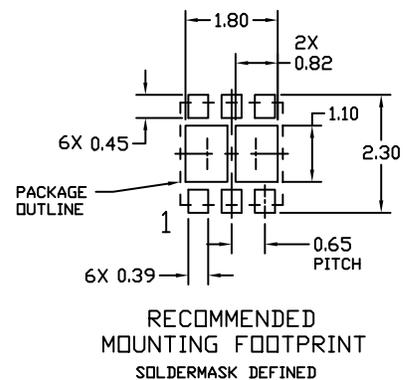
XX = Specific Device Code  
M = Date Code

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

**NOTES:**

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS
3. DIMENSION **b** APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30 MM FROM THE TERMINAL TIP.
4. COPLANARITY APPLIES TO THE EXPOSED PADS AS WELL AS THE TERMINALS.

DIM	MILLIMETERS	
	MIN.	MAX.
A	0.70	0.80
A1	0.00	0.05
A3	0.20 REF	
b	0.25	0.35
D	2.00 BSC	
D2	0.57	0.77
E	2.00 BSC	
E2	0.90	1.10
e	0.65 BSC	
F	0.95 BSC	
k	0.25 REF	
L	0.20	0.30
L1	---	0.10



<b>DOCUMENT NUMBER:</b>	<b>98AON20861D</b>	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
<b>DESCRIPTION:</b>	<b>WDFN6 2x2, 0.65P</b>	<b>PAGE 1 OF 1</b>

onsemi and onsemi are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

**onsemi**, **Onsemi**, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "**onsemi**" or its affiliates and/or subsidiaries in the United States and/or other countries. **onsemi** owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of **onsemi**'s product/patent coverage may be accessed at [www.onsemi.com/site/pdf/Patent-Marking.pdf](http://www.onsemi.com/site/pdf/Patent-Marking.pdf). **onsemi** reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and **onsemi** makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

## ADDITIONAL INFORMATION

### TECHNICAL PUBLICATIONS:

Technical Library: [www.onsemi.com/design/resources/technical-documentation](http://www.onsemi.com/design/resources/technical-documentation)  
onsemi Website: [www.onsemi.com](http://www.onsemi.com)

### ONLINE SUPPORT: [www.onsemi.com/support](http://www.onsemi.com/support)

For additional information, please contact your local Sales Representative at [www.onsemi.com/support/sales](http://www.onsemi.com/support/sales)