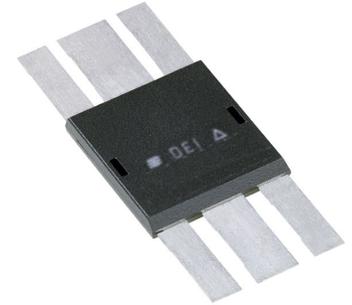
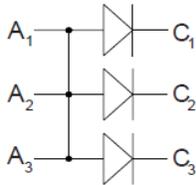
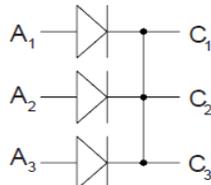
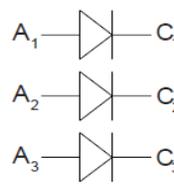


Silicon Carbide Schottky Diode

 $V_{RRM} = 600\text{ V}$
 $I_{F(AVG)} = 10\text{ A}$
 $C_J = 80\text{ pF}$

Part Number	V_{RRM} (V)	$I_{F(AVG)}$ (A)	Configuration
SS150TA60110	600	10	Triple Common Anode
SS150TC60110	600	10	Triple Common Cathode
SS150TI60110	600	10	Triple Independent


Triple Anode (TA)

Triple Cathode (TC)

Triple Independent (TI)


A = Anode C = Cathode

Symbol	Parameter	Test Conditions	Maximum Ratings
V_{RRM}	Repetitive Peak Reverse Voltage		600 V
V_{RSM}	Repetitive Surge Reverse Voltage		600 V
V_{DC}	DC Blocking Voltage		600 V
$I_{F(AVG)}$	Average Forward Current	$T_J = 175^\circ\text{C}$	10 A
I_{FRM}	Repetitive Peak Forward Surge Current	$T_{VJ} = 45^\circ\text{C}$, $t_p = 10\text{ ms}$ Half Sine Wave $D = 0.3$	25 A
T_{VJ}	Operating Virtual Junction Temperature		-55 to +175 °C
T_{STG}	Storage Temperature		-55 to +175 °C
P_{TOT}	$T_C = 25^\circ\text{C}$ (20 W/device)		60 W

Features

- 600 V SiC Schottky Diode
- Surface Mount Package
- Zero Reverse Recovery
- Zero Forward Recovery
- High Frequency Operation
- Temperature Independent Behavior
- Positive Temperature Coefficient for V_F

Applications

- MHz Switch Mode Power Supplies
- High Frequency Converters
- Resonant Converters
- Rectifier Circuits

Symbol	Parameter	Test Conditions	Characteristic Values		
$T_J = 25^\circ\text{C}$ unless otherwise specified			Typ.	Max.	Units
V_F	Forward Voltage	$I_F = 5\text{ A}$, $T_J = 25^\circ\text{C}$ $T_J = 175^\circ\text{C}$	1.6 2	1.8 2.4	V
I_R	Reverse Current	$V_R = 600\text{ V}$, $T_J = 25^\circ\text{C}$ $T_J = 175^\circ\text{C}$	10 20	50 200	μA
C_J	Junction Capacitance	$f = 1\text{ MHz}$, $V_R = 0\text{ V}$ $V_R = 200\text{ V}$ $V_R = 600\text{ V}$	485 85 80		pF
R_{THJC}	Thermal Resistance		2.5		$^\circ\text{C/W}$
T_L	Lead Soldering Temperature	1.6 mm (0.063 in) from case for 10 s	300		$^\circ\text{C}$
Isolation	Pin to Substrate Pin to Pin		>1800 >1500		V_{RMS}
Weight			2		g

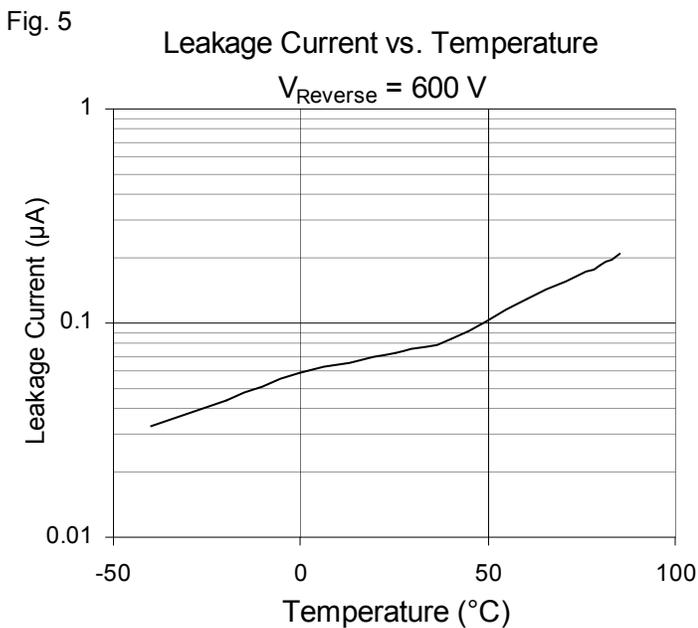
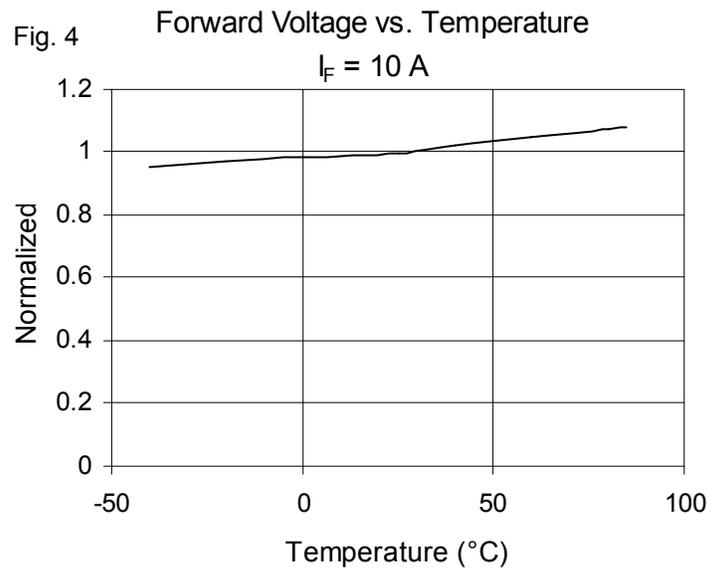
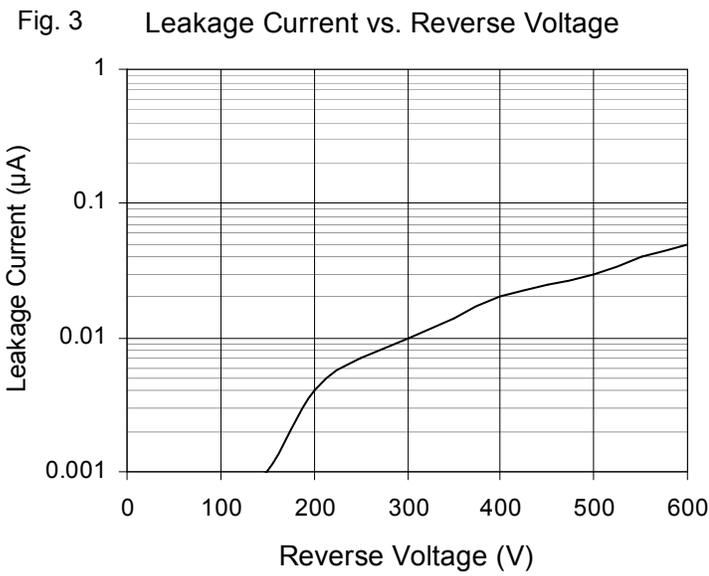
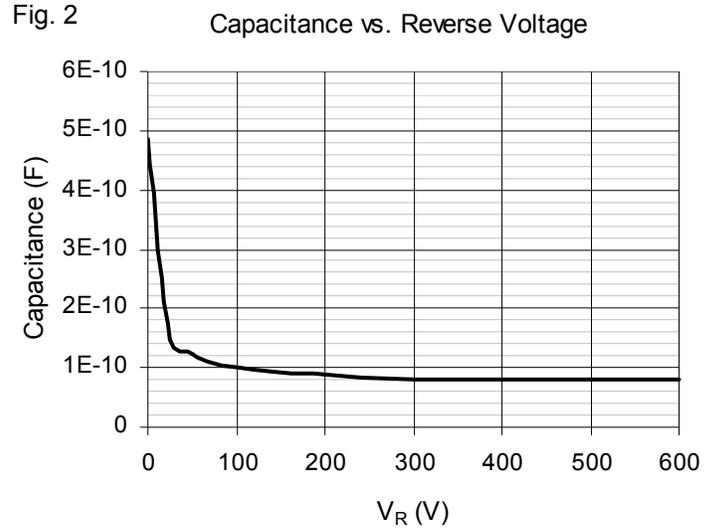
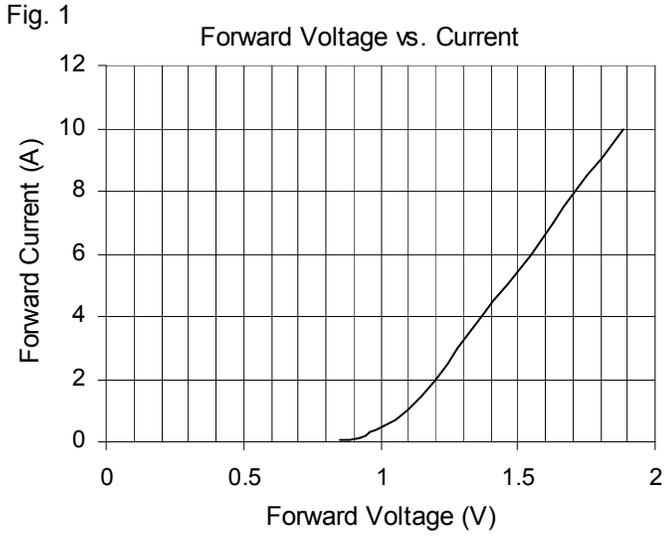


Fig. 6 Package Diagram

