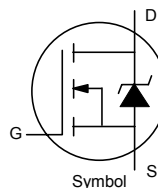
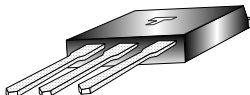




# IRF840LCS/LCL

Power MOSFET

$V_{DSS} = 500V$ ,  $R_{DS(on)} = 0.85 \text{ ohm}$ ,  $I_D = 8.0 \text{ A}$



N Channel

ELECTRICAL CHARACTERISTICS at $T_j = 25^\circ\text{C}$ Maximum. Unless stated Otherwise						
Parameter	Symbol	Test Conditions	Value			Unit
			Min	Typ	Max	
Drain to Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0 \text{ V}_{DC}$ , $I_D = 250\mu\text{A}$	500	-	-	Volt
Drain to Source Leakage Current	$I_{DSS}$	$V_{DS} = 500V_{DC}$ , $V_{GS} = 0V_{DC}$	-	-	25	$\mu\text{A}$
		$V_{DS} = 400V_{DC}$ , $V_{GS} = 0V_{DC}$ , $T_j = 125^\circ\text{C}$	-	-	250	
Gate to Source Leakage Current	$I_{GSS}$	$V_{GS} = +20V_{DC}$	-	-	100	nA
		$V_{GS} = -20V_{DC}$	-	-	-100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$ , $I_D = 250\mu\text{A}$	2.0	-	4.0	Volt
Static Drain to Source On - Resistance	$R_{DS(on)}$	$V_{GS} = 10V_{DC}$ , $I_D = 4.8\text{A}$	-	-	0.85	$\Omega$
Gate Charge	$Q_G$	$I_D = 8.0\text{A}$	-	-	39	nC
Gate to Source Charge	$Q_{GS}$	$V_{DS} = 400V_{DC}$ , $V_{GS} = 10V_{DC}$	-	-	10	nC
Gate to Drain Charge	$Q_{GD}$		-	-	19	nC
Input Capacitance	$C_{ISS}$		-	1100	-	pF
Output Capacitance	$C_{OSS}$	$V_{DS} = 25V_{DC}$ , $V_{GS} = 0V_{DC}$ , $f = 1.0\text{MHz}$	-	170	-	pF
Transfer Capacitance	$C_{RSS}$		-	18	-	pF
Turn On Delay Time	$t_{d(on)}$		-	12	-	nS
Turn Off Delay Time	$t_{d(off)}$	$V_{DD} = 250V_{DC}$ , $I_D = 8.0\text{A}$ , $R_G = 9.1\Omega$	-	27	-	nS
Rise Time	$t_r$	$R_D = 31\Omega$	-	25	-	nS
Fall Time	$t_f$		-	19	-	nS
Continuous Source Current	$I_S$		-	-	8.0	A
Pulsed Source Current	$I_{SM}$		-	-	28	A
Forward Voltage (Diode)	$V_{SD}$	$V_{GS} = 0V_{DC}$ , $I_S = 8.0\text{A}$ , $T_p = 300\mu\text{S}$	-	-	2.0	V
Single Pulse Avalanche Energy	$E_{AS}$				510	mJ

MAXIMUM RATINGS ( $T_j = 25^\circ\text{C}$ unless stated otherwise)				
Parameter	Symbol	Condition	Value	Unit
Gate to Source Voltage	$V_{GS}$		+/- 30V	Volt
Drain to Source Voltage	$V_{DSS}$		500	Volt
Continuous Drain Current	$I_D$		8.0	Amp
Pulsed Drain Current	$I_{DM}$	-	28	Amp
Total Power Dissipation	$P_D$	( $T_A = 25^\circ\text{C}$ )	125	W
Thermal Resistance (Junction to Ambient)	$R_{TH(j-a)}$		40	$^\circ\text{C/W}$

Maximum Operating Temperature Range ( $T_j$ ) -55 to +150  $^\circ\text{C}$   
Maximum Storage Temperature Range ( $T_{stg}$ ) -55 to +150  $^\circ\text{C}$