



DATE :Mar. 09 .2012

SAMSUNG TFT-LCD

MODEL :ZXXS400-L02

The Information Described in this Specification is Preliminary and can be changed without prior notice

APPROVED BY	DATE	PREPARED BY	DATE
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*** Revision History**

Date	Rev. No	Page	Summary
Nov. 29. 2011	000	all	First issued
Mar. 09. 2012	001	7	- Optical Characteristics Contrast = Min : 2000 → 3000 // Max : 3000 → 4000 Response Time = Typ : 8 // Max : 16
		16	- LVDS Interface option → JEIDA

General Description

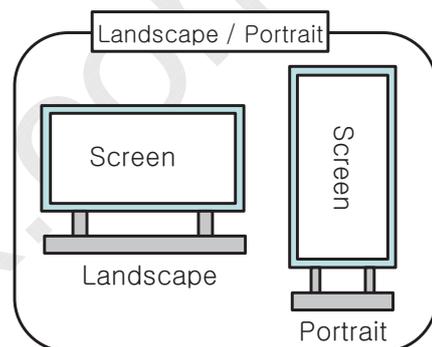


Description

ZXXS400-L02 is a color active matrix liquid crystal display (LCD) that uses amorphous silicon TFT (Thin Film Transistor) as switching components. This model is composed of a TFT LCD panel, a driver circuit and a back light unit. The resolution of a 40.0" is 1920 x 1080 and this model can display up to 16.7 million colors with wide viewing angle of 89° or higher in all directions. This panel is intended to support applications to provide an excellent performance for Flat Panel Display such as Home-alone Multimedia TFT-LCD TV, Display terminals for AV application products, and Digital Information Display (DID).

Features

- RoHS compliance (Pb-free)
- High contrast ratio, High aperture ratio
- SPVA (Super Patterned Vertical Align) mode
- Wide viewing angle ($\pm 178^\circ$)
- High speed response
- Landscape / Portrait type compatible
- Wide UXGA (1920 x 1080 pixels) resolution (16:9)
- Low power consumption
- Direct Type 12 CCFTs (Cold Cathode Fluorescent Tube)
- DE (Data Enable) mode
- LVDS (Low Voltage Differential Signaling) interface (2pixel/clock)



General Information

Items	Specification	Unit	Note
Module Size	952.0(W _{TYP}) x 551.0(H _{TYP})	mm	±1.0mm
	55.6(D _{TYP})		
Weight	10,000(Max.)	g	
Pixel Pitch	0.46125(H) x 0.46125(V)	mm	
Active Display Area	885.6(H) x 498.15(V)	mm	
Surface Treatment	Haze 44% , Hard-coating (3H)		
Display Colors	8 bit - 16.7M	colors	
Number of Pixels	1920 x 1080	pixel	
Pixel Arrangement	RGB vertical stripe		
Display Mode	Normally Black		
Luminance of White	1000 (Typ.)	cd/m ²	



1. Absolute Maximum Ratings

If the condition exceeds maximum ratings, it can cause malfunction or unrecoverable damage to the device.

Item	Symbol	Min.	Max.	Unit	Note	
Power Supply Voltage	V_{DD}	10.8	13.2	V	(1)	
Storage temperature	T_{STG}	-20	65	°C	(2)	
Glass surface temperature (Operation)	Center	T_{CENTER}	0	50	°C	(2),(5)
		T_{SUR}	0	65	°C	
Shock (non - operating)	S_{nop}	-	50	G	(3)	
Vibration (non - operating)	V_{nop}	-	1.5	G	(4)	

Note (1) $T_a = 25 \pm 2$ °C

(2) Temperature and relative humidity range are shown in the figure below.

a. 90 % RH Max. ($T_a \leq 39$ °C)

b. Relative Humidity is 90% or less. ($T_a > 39$ °C)

c. No condensation

(3) 11ms, sine wave, one time for $\pm X, \pm Y, \pm Z$ axis

(4) 10-300 Hz, Sweep rate 10min, 30min for X,Y,Z axis

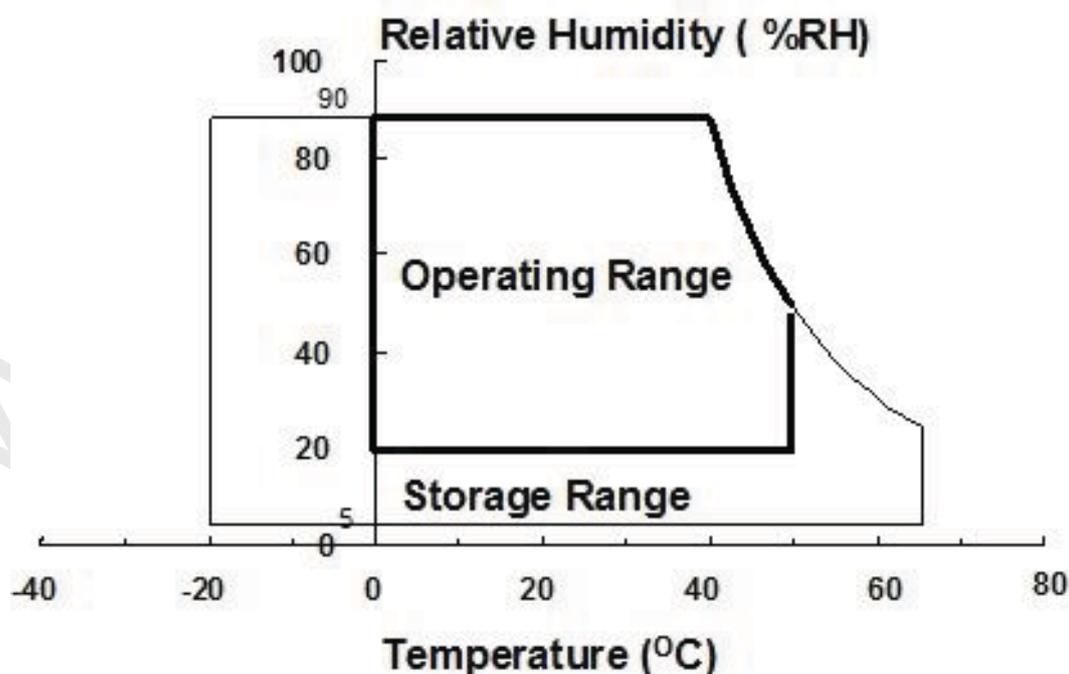
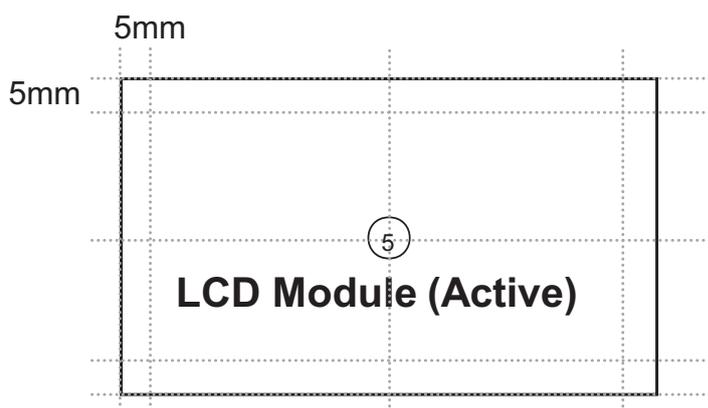


Fig. Temperature and Relative humidity range

(5) Definition of test point



T_{CENTER} : Temperature of the center of the glass surface (Test point 5)

2. Application information for DID (Digital Information Display)

A long-term display like DID application may cause uneven display including image retention. To optimize module's lifetime and function, several operating usages are required.

1. Normal operating condition

- Temperature: $20 \pm 15^{\circ}\text{C}$
- Humidity: $65 \pm 20\%$
- Display pattern: moving picture or regular switchover display

Note) Long-term static information image may cause uneven display.

2. Operating usages under abnormal operating condition. Note (1)

- a. Ambient condition
 - Well-ventilated place is recommended to set up DID system.
- b. Power off and screen saver
 - Periodical power-off or screen saver is needed after long-term static display. Note (2)

3. Operating usages to protect uneven display due to long-term static information display

- a. Suitable operating time for B-DID : under 12 hours a day.
- b. Periodical display contents change from static image to moving picture.
 - Liquid crystal refresh time is required.
- c. Periodical background color and character (image) color change
 - Use different colors for background and character (image), respectively.
 - Change colors periodically.
- d. Avoid combination of background and character with large different luminance.

Note (1) Abnormal condition means every operating condition except normal operating condition.

Note (2) Moving picture or black pattern is strongly recommended for screen saver.

4. Lifetime in this spec is guaranteed only when DID is used under right operating usages.

3. Optical Characteristics



The optical characteristics should be measured in a dark room or equivalent.
Measuring equipment : TOPCON BM-7,SPECTRORADIOMETER SR-3

(Ta = 25 ± 2°C, VDD = 12V, fv = 60Hz, f_{DCLK} = 148.5MHz, I_L = 14mA rms)

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Note	
Contrast Ratio (Center of screen)	C/R	Normal $\theta_{L,R}=0$ $\theta_{U,D}=0$ Viewing Angle	3000	4000	-		(3) SR-3	
Response Time G-to-G	Tg		-	8	16	msec	(5) BM-7	
Luminance of White (Center of screen)	Y _L		350	1000	-	cd/m ²	(6) SR-3	
Color Chromaticity (CIE 1931)	Red		Rx	TYP. -0.03	0.637	TYP. +0.03		(7),(8) SR-3
			Ry		0.331			
	Green		Gx		0.292			
			Gy		0.605			
	Blue		Bx		0.148			
			By		0.061			
	White		Wx		0.280			
		Wy	0.290					
Color Gamut	-	-	72	-	%	(7) SR-3		
Color Temperature	-	-	10,000	-	K	(7) SR-3		
Viewing Angle	Hor.	θ_L	C/R≥10	79	89	-	Degree	(8) EZ
		θ_R		79	89	-		
	Ver.	θ_U		79	89	-		
		θ_D		79	89	-		
Brightness Uniformity (9 Points)	B _{uni}	-	-	25	%	(4) SR-3		

Note (1) Test Equipment Setup

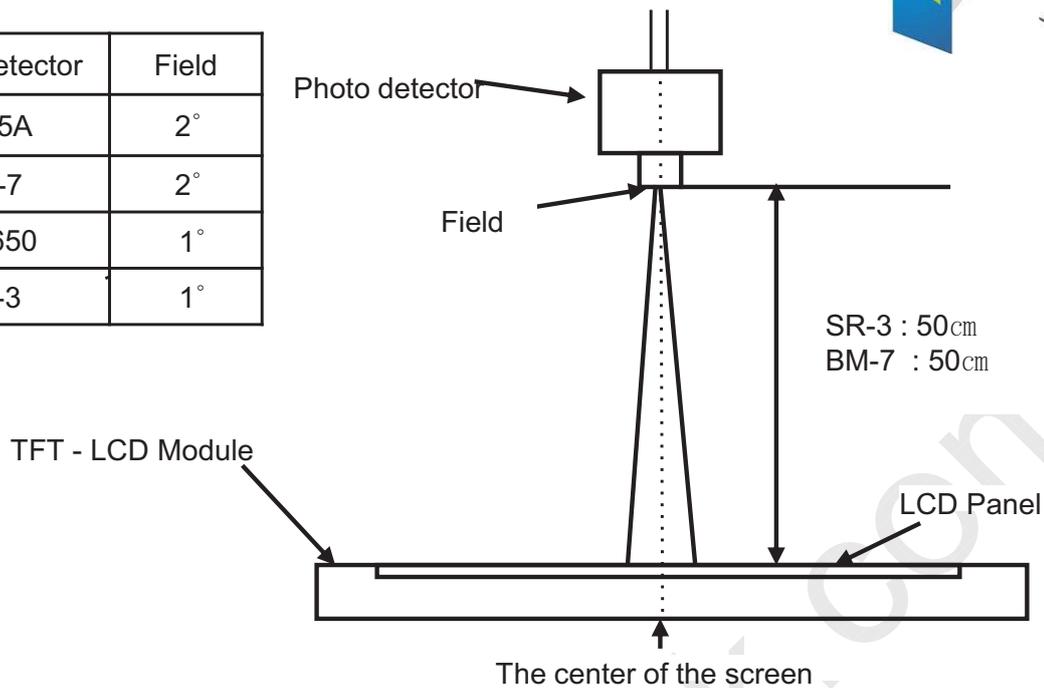
The measurement should be executed in a stable, windless and dark room between 40min and 60min after lighting the backlight at the given temperature for stabilization of the backlight. This should be measured in the center of screen.

Single lamp current : 14mA

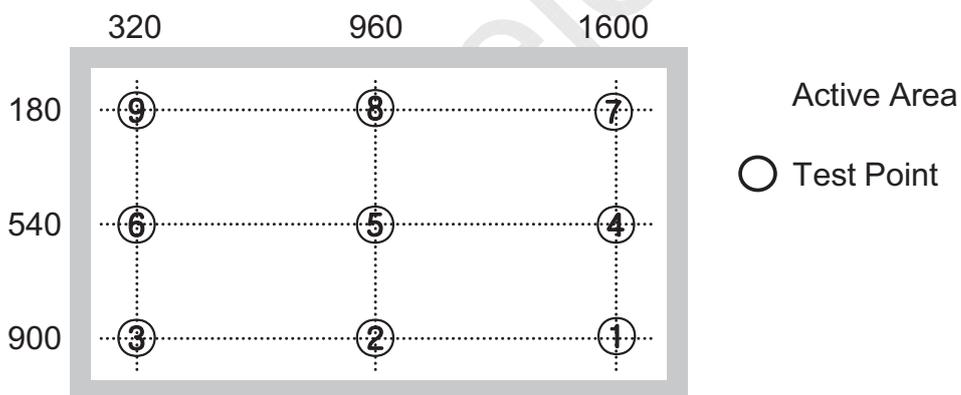
Environment condition : Ta = 25 ± 2 °C



Photo detector	Field
BM-5A	2°
BM-7	2°
PR-650	1°
SR-3	1°



Note (2) Definition of test point



Note (3) Definition of Contrast Ratio (C/R)

: Ratio of gray max (Gmax) & gray min (Gmin) at the center point ⑤ of the panel

$$C/R = \frac{G_{max}}{G_{min}}$$

Gmax : Luminance with all pixels white

Gmin : Luminance with all pixels black



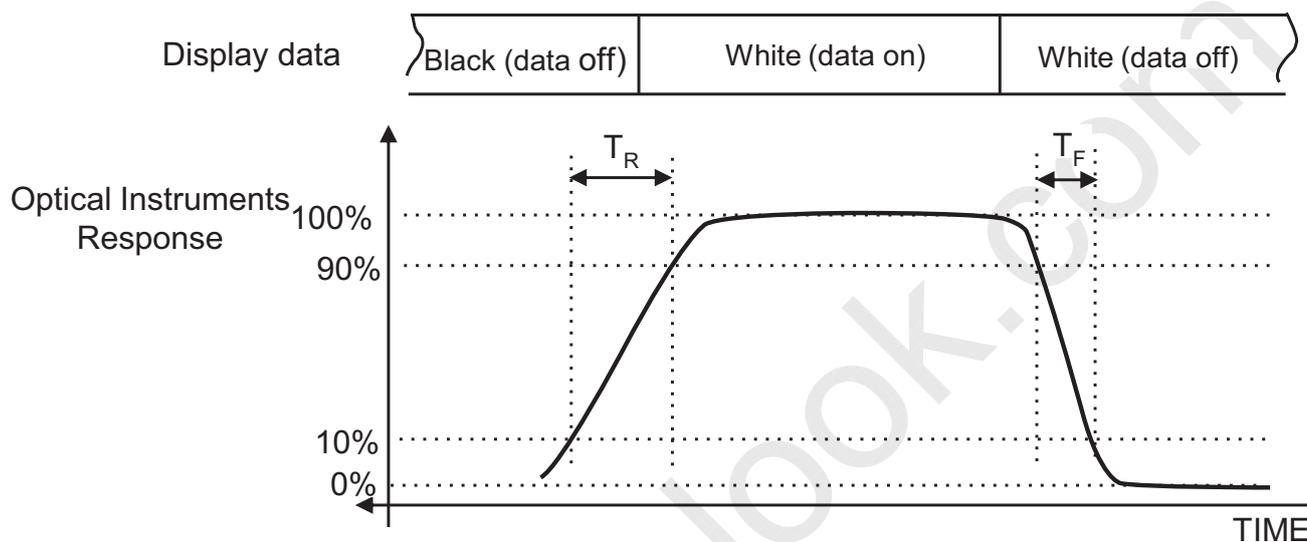
Note (4) Definition of 9 points brightness uniformity

$$B_{uni} = 100 * \frac{(B_{max} - B_{min})}{B_{max}}$$

Bmax : Maximum brightness

Bmin : Minimum brightness

Note (5) Definition of Response time : Sum of Tr, Tf



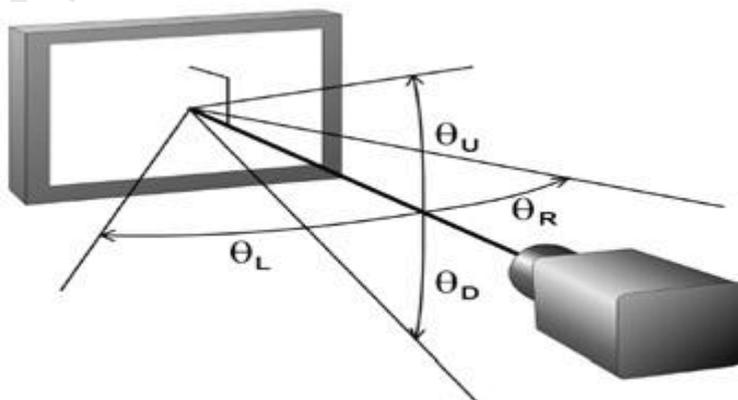
Note (6) Definition of Luminance of White : Luminance of white at center point ⑤

Note (7) Definition of Color Chromaticity (CIE 1931)

Color coordinate of Red, Green, Blue & White at center point ⑤

Note (8) Definition of Viewing Angle

: Viewing angle range (C/R ≥ 10)



4. Electrical Characteristics



4.1 TFT LCD Module

The connector for display data & timing signal should be connected.

$T_a = 25^{\circ}\text{C} \pm 2^{\circ}\text{C}$

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Voltage of Power Supply	V_{DD}	10.8	12.0	13.2	V	(1)
Current of Power Supply	(a) Black	-	720	-	mA	(2),(3)
	(b) White	-	1350	-	mA	
	(c) N-Pattern	-	1290	-	mA	
Vsync Frequency	f_V	48.0	60.0	62.0	Hz	
Hsync Frequency	f_H	50.0	67.5	75.0	kHz	
Main Frequency	f_{DCLK}	130.0	148.5	155.0	MHz	
Rush Current	I_{RUSH}	-	-	3	A	(4)

Note (1) The ripple voltage should be controlled under 10% of V_{DD} .

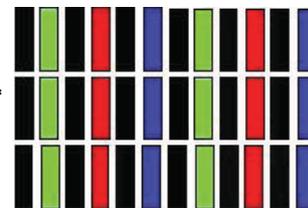
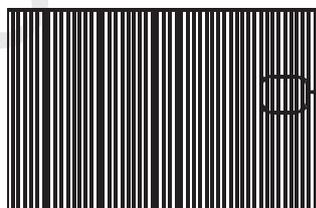
(2) $f_V = 60\text{Hz}$, $f_{DCLK} = 148.5\text{MHz}$, $V_{DD} = 12.0\text{V}$, DC Current.

(3) Power dissipation check pattern (LCD Module only)

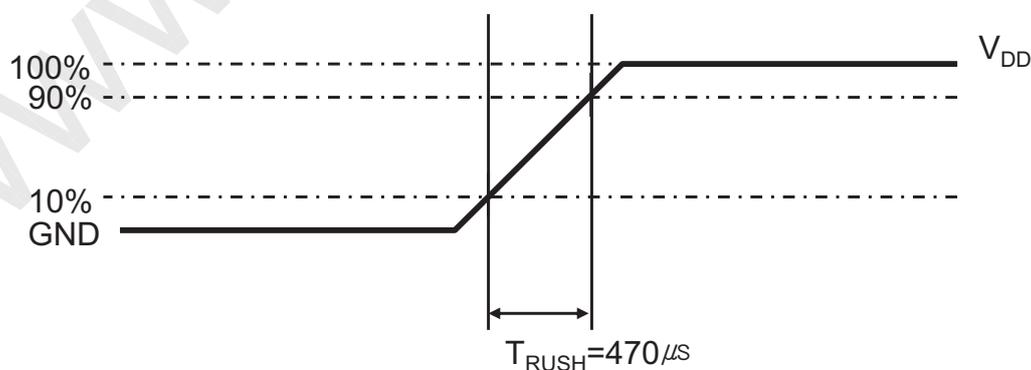
a) Black Pattern

b) White Pattern

c) N-Pattern



(4) Measurement Conditions



Rush Current I_{RUSH} can be measured when T_{RUSH} is $470\mu\text{s}$.



4.2 Back Light Unit

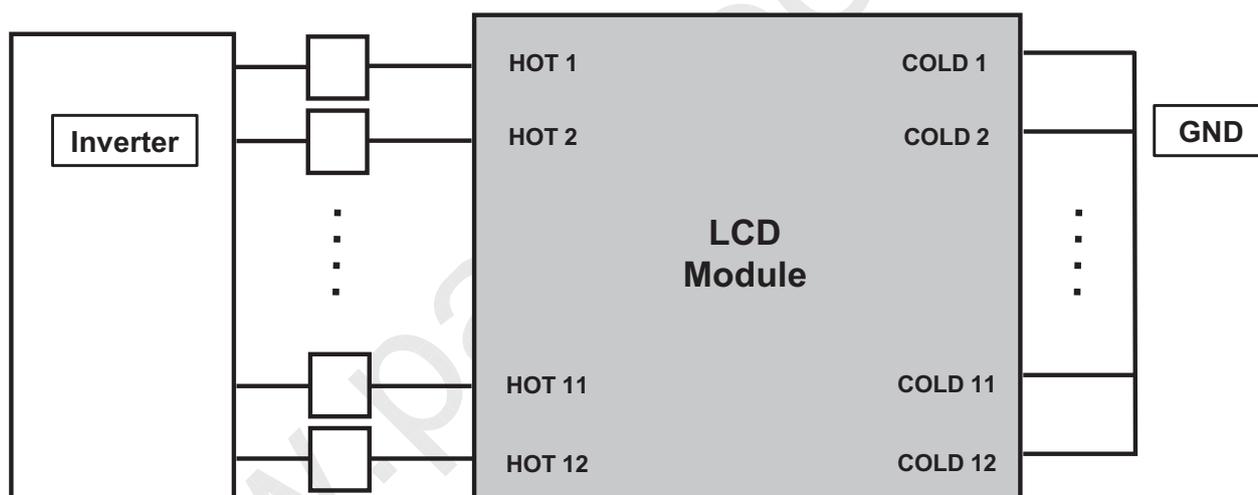
The back light unit contains 12 direct-lighting type CCFTs (Cold Cathode Fluorescent Tube). The characteristics of lamps are shown in the following tables.

$T_a = 25 \pm 2^\circ\text{C}$

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Lamp Current	I_L	8.0	14.0	16.0	mArms	
Lamp Voltage	V_L	825	855	955	Vrms	
Operating Life Time	Hr	50,000	-	-	Hour	(1)

Note (1) It is defined as the time to take until the brightness reduces to 50% of its original value.

[Operating condition : $T_a = 25 \pm 2^\circ\text{C}$, $I_L = 14.0\text{mArms}$, For single lamp only]





4.3 Inverter Input Condition & Specification

Items	Symbol	Conditions	Specifications			Unit	Note
			Min.	Typ.	Max.		
Input Voltage	V _{in}	-	22	24	26	V	Ta=25±2 °C
Input Current	I _{in}	V _{in} =24.0V V _{dim} =3.3V	-	5.32	5.68	A	After 1 hour Warm-up
Lamp Current	I _{O,MAX}	V _{dim} =3.3V	13.3	14.0	14.7	mArms	
Frequency	F _{LAMP}	V _{in} =24.0V V _{dim} =3.3V	46	48	50	kHz	
Backlight On/Off	ON	V _{in} =24.0V	2.4	-	5.25	V	-
	OFF		0	-	0.8		
Internal Dimming	-	V _{in} =24.0V	0	-	3.3	V	

Note (1) Power Consumption is measured at 1000[cd/m²] of luminance condition which is the typical luminance value. Lamp Current is measured at the point before Lamp.



5. Input Terminal Pin Assignment

5.1 Input Signal & Power

Connector : FI-RE51S-HF (JAE/UJU)

PIN No.	Description		PIN No.	Description
1	Vdd (12V)		26	RE[0]P
2	Vdd (12V)		27	RE[1]N
3	Vdd (12V)		28	RE[1]P
4	Vdd (12V)		29	RE[2]N
5	Vdd (12V)		30	RE[2]P
6	No Connection		31	GND
7	GND		32	RECLK-
8	GND		33	RECLK+
9	GND		34	GND
10	Odd LVDS Signal	RO[0]N	35	RE[3]N
11		RO[0]P	36	RE[3]P
12		RO[1]N	37	No Connection
13		RO[1]P	38	No Connection
14		RO[2]N	39	GND
15		RO[2]P	40	No Connection
16		GND	41	No Connection
17		ROCLK-	42	No Connection
18		ROCLK+	43	No Connection
19		GND	44	No Connection
20		RO[3]N	45	LVDS Option
21	RO[3]P	46	No Connection	
22	No Connection		47	No Connection
23	No Connection		48	No Connection
24	GND		49	No Connection
25	Even LVDS	RE[0]N	50	No Connection
			51	No Connection

Note(1) No Connection : These pins are only used for SAMSUNG internal purpose.

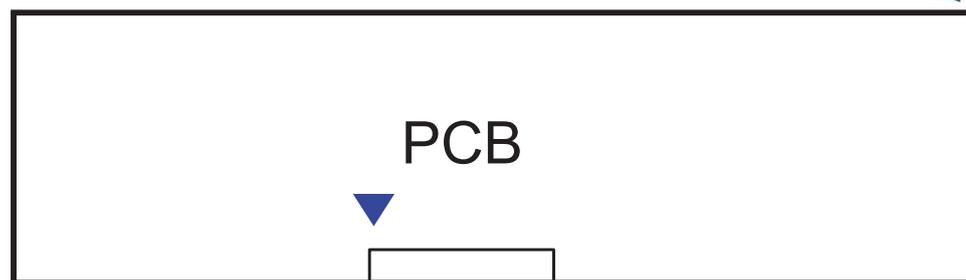
(2) LVDS Option : High (3.3 V) → Normal LVDS format

: Low (GND) or Open (N.C) → JEIDA LVDS format

Sequence : On = VDD(T1) ≥ LVDS Option ≥ Interface Signal(T2)

Off = Interface Signal(T3) ≥ LVDS Option ≥ VDD

Note (3) LVDS Connector



Pin No. 1 Pin No. 51

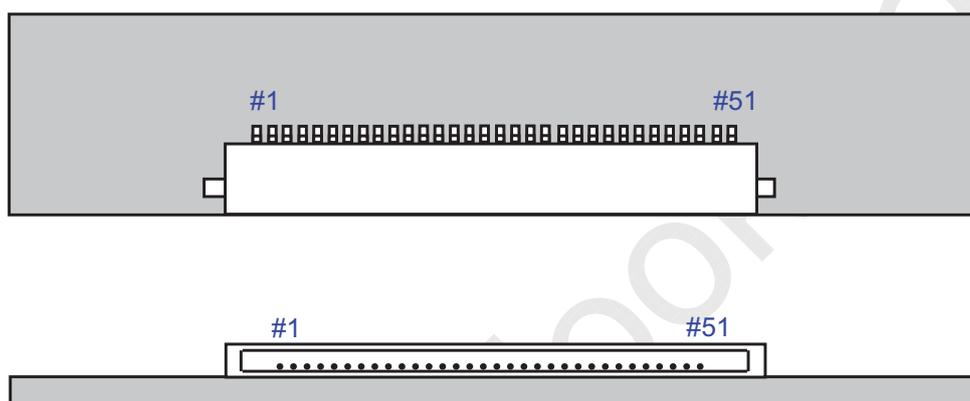


Fig. Connector diagram

- All GND pins should be connected together and also be connected to the LCD's metal chassis.
- All power input pins should be connected together.
- All N.C pins should be separated from other signal or power.



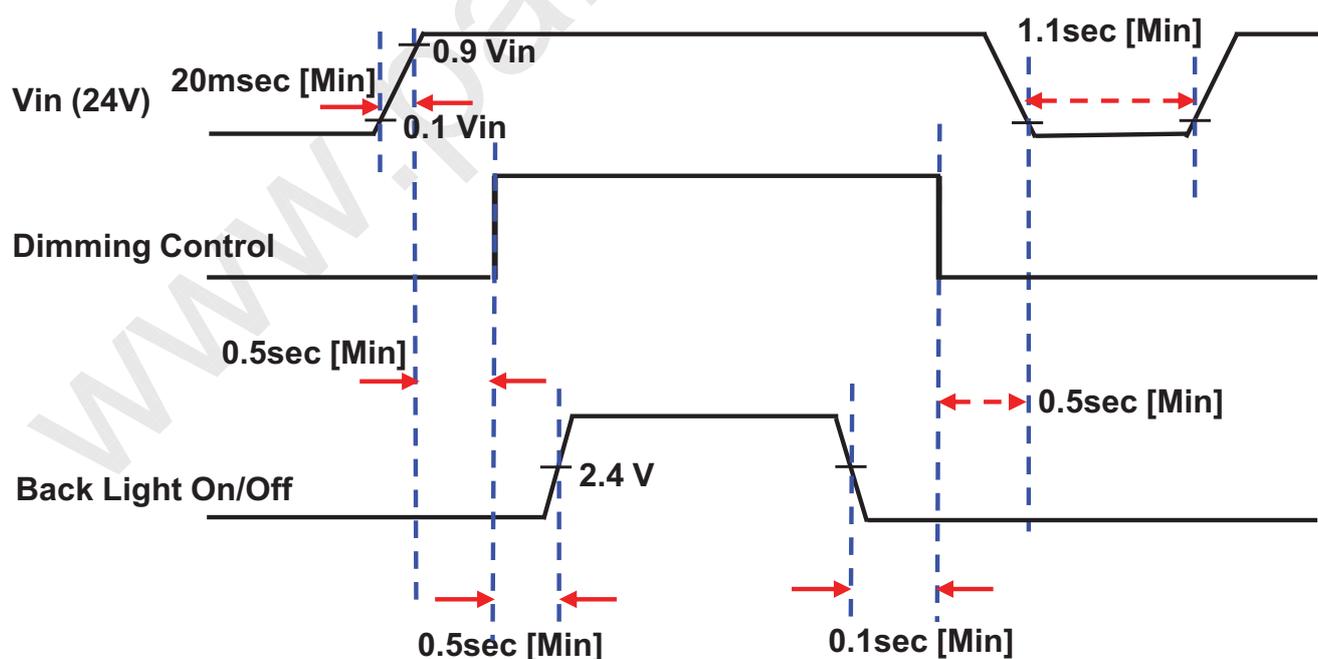
5.2 Inverter Input Pin Configuration

Connector : YEON HO, 20022WR-14B1

Pin No.	Pin Configuration(FUNCTION)
1	Vin (24V)
2	Vin (24V)
3	Vin (24V)
4	Vin (24V)
5	Vin (24V)
6	GND
7	GND
8	GND
9	GND
10	GND
11	No connection
12	Backlight On /Off [On: 2.4 ~ 5.25V, Off: 0 ~ 0.8V]
13	INT DIM :INTERNAL DIMMING SIGNAL (0V: Min 3.3V: Max)
14	No connection

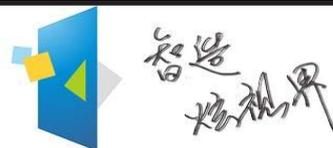
Note (5) LVDS Connector

5.3 Inverter Input Power Sequence



5.4 LVDS Interface

- LVDS Receiver : Tcon (merged)
- Data Format (JEIDA)



	LVDS pin	JEIDA -DATA
TxOUT/RxIN0	TxIN/RxOUT0	R4
	TxIN/RxOUT1	R5
	TxIN/RxOUT2	R6
	TxIN/RxOUT3	R7
	TxIN/RxOUT4	R8
	TxIN/RxOUT6	R9
	TxIN/RxOUT7	G4
TxOUT/RxIN1	TxIN/RxOUT8	G5
	TxIN/RxOUT9	G6
	TxIN/RxOUT12	G7
	TxIN/RxOUT13	G8
	TxIN/RxOUT14	G9
	TxIN/RxOUT15	B4
	TxIN/RxOUT18	B5
TxOUT/RxIN2	TxIN/RxOUT19	B6
	TxIN/RxOUT20	B7
	TxIN/RxOUT21	B8
	TxIN/RxOUT22	B9
	TxIN/RxOUT24	HSYNC
	TxIN/RxOUT25	VSYNC
	TxIN/RxOUT26	DEN
TxOUT/RxIN3	TxIN/RxOUT27	R2
	TxIN/RxOUT5	R3
	TxIN/RxOUT10	G2
	TxIN/RxOUT11	G3
	TxIN/RxOUT16	B2
	TxIN/RxOUT17	B3
	TxIN/RxOUT23	RESERVED
TxOUT/RxIN4	TxIN/RxOUT28	R0
	TxIN/RxOUT29	R1
	TxIN/RxOUT30	G0
	TxIN/RxOUT31	G1
	TxIN/RxOUT32	B0
	TxIN/RxOUT33	B1
	TxIN/RxOUT34	RESERVED



5.5 Input Signals, Basic Display Colors and Gray Scale of Each Color

COLOR	DISPLAY (8bit)	DATA SIGNAL																					GRAY SCALE LEVEL			
		RED							GREEN							BLUE										
		R0	R1	R2	R3	R4	R5	R6	R7	G0	G1	G2	G3	G4	G5	G6	G7	B0	B1	B2	B3	B4		B5	B6	B7
BASIC COLOR	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	-
	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	-
	CYAN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	MAGENTA	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	-
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	-
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-	
GRAY SCALE OF RED	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0	
	DARK ↑	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1	
		0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2	
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	R3~R252
	LIGHT ↓	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
		1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R253	
		0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R254	
	RED	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R255	
GRAY SCALE OF GREEN	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0	
	DARK ↑	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G1	
		0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	G2	
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	G3~G252	
	LIGHT ↓	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:		
		0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	G253	
		0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	G254	
	GREEN	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G255	
GRAY SCALE OF BLUE	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	B0	
	DARK ↑	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	B1	
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	B2	
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	B3~B252	
	LIGHT ↓	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:		
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	B253	
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	B254	
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	B255	

Note) Definition of Gray :
 Rn : Red Gray, Gn : Green Gray, Bn : Blue Gray (n = Gray level)
 Input Signal : 0 = Low level voltage, 1 = High level voltage



6. Interface Timing

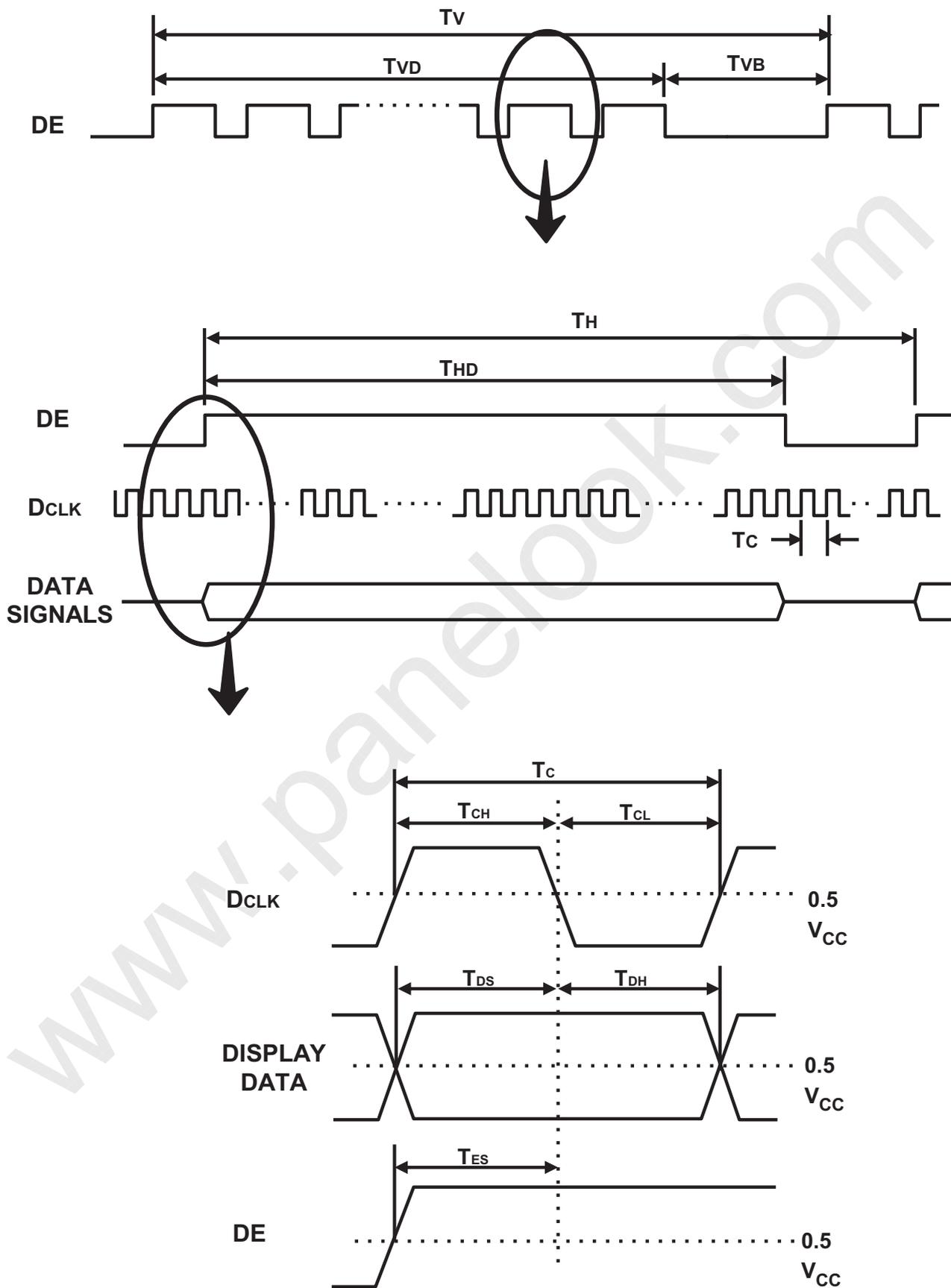
6.1 Timing Parameters (DE only mode)

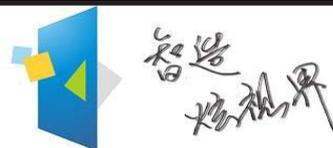
Signal	Item	Symbol	Min.	Typ.	Max.	Unit	Note
Clock	Frequency	$1/T_C$	130.0	148.5	155.0	MHz	-
Hsync		F_H	50.0	67.5	75.0	KHz	-
Vsync		F_V	48	60	62	Hz	-
Vertical Display Term	Active Display Period	T_{VD}	-	1080	-	Lines	-
	Vertical Total	T_V	1100	1125	1158	Lines	-
Horizontal Display Term	Active Display Period	T_{HD}	-	1920	-	Clocks	-
	Horizontal Total	T_H	2090	2200	2350	Clocks	-

Note) Note) Requirement: Vsync → ODD channel



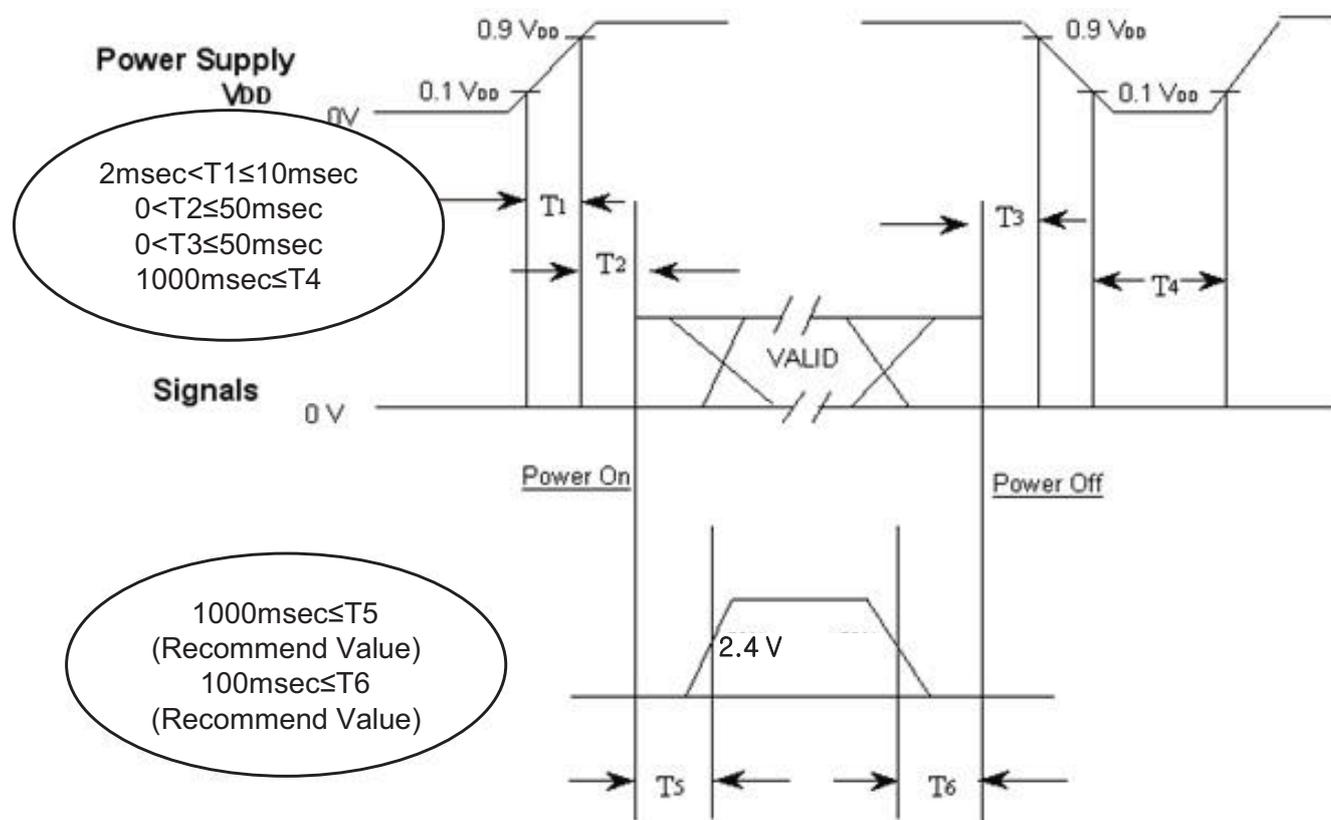
6.2 Timing diagrams of interface signal (DE only mode)





6.3 Power ON/OFF Sequence

To prevent a latch-up or DC operation of the LCD Module, the power on/off sequence should be as the diagram below.



T1 : V_{DD} rising time from 10% to 90%

T2 : The time from V_{DD} to valid data at power ON.

T3 : The time from valid data off to V_{DD} off at power Off.

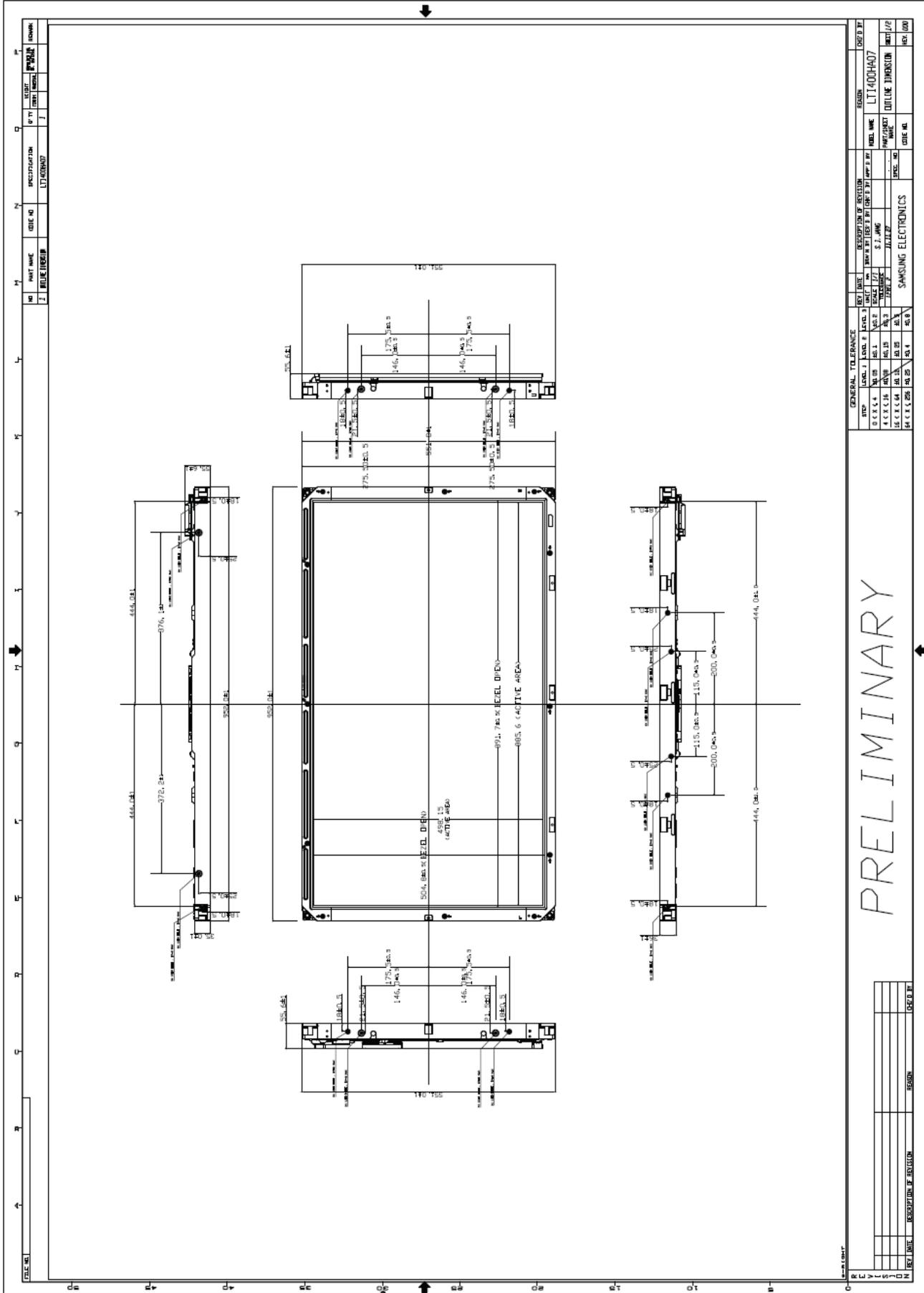
T4 : V_{DD} off time for Windows restart

T5 : The time from valid data to B/L enable at power ON.

T6 : The time from valid data off to B/L disable at power Off.

- The supply voltage of the external system for the Module input should be the same as the definition of V_{DD}.
- Apply the lamp voltage within the LCD operation range. When the back light turns on before the LCD operation or the LCD turns off before the back light turns off, the display may momentarily show abnormal screen.
- In case of V_{DD} = off level, please keep the level of input signals low or keep a high impedance.
- T4 should be measured after the Module has been fully discharged between power off and on period.
- Interface signal should not be kept at high impedance when the power is on.

7. Outline Dimension (Front View)



PRELIMINARY

GENERAL TOLERANCE		DESCRIPTION OF REVOLUTION		REVISION	
STEP	LEVEL	NO.	DATE	BY	CHK
0	1	1	1/1	LI	LI
1	1	1	1/1	LI	LI
2	1	1	1/1	LI	LI
3	1	1	1/1	LI	LI
4	1	1	1/1	LI	LI
5	1	1	1/1	LI	LI
6	1	1	1/1	LI	LI
7	1	1	1/1	LI	LI
8	1	1	1/1	LI	LI
9	1	1	1/1	LI	LI
10	1	1	1/1	LI	LI
11	1	1	1/1	LI	LI
12	1	1	1/1	LI	LI
13	1	1	1/1	LI	LI
14	1	1	1/1	LI	LI
15	1	1	1/1	LI	LI
16	1	1	1/1	LI	LI
17	1	1	1/1	LI	LI
18	1	1	1/1	LI	LI
19	1	1	1/1	LI	LI
20	1	1	1/1	LI	LI
21	1	1	1/1	LI	LI
22	1	1	1/1	LI	LI
23	1	1	1/1	LI	LI
24	1	1	1/1	LI	LI
25	1	1	1/1	LI	LI
26	1	1	1/1	LI	LI
27	1	1	1/1	LI	LI
28	1	1	1/1	LI	LI
29	1	1	1/1	LI	LI
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97	1	1	1/1	LI	LI
98	1	1	1/1	LI	LI
99	1	1	1/1	LI	LI
100	1	1	1/1	LI	LI

Model

8. PACKING

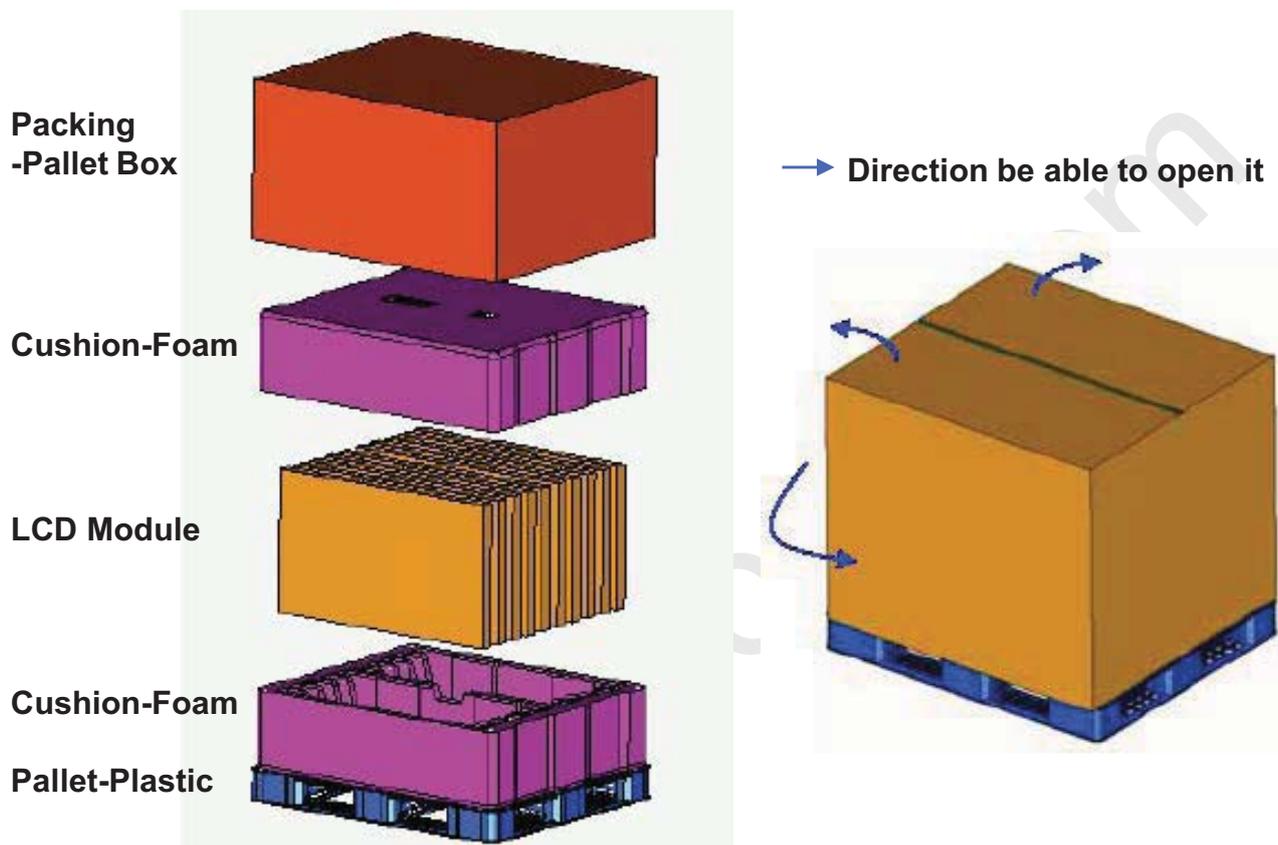


8.1 CARTON (Internal Package)

(1) Packing Form

Corrugated fiberboard box and corrugated cardboard as shock absorber

(2) Packing Method



8.2 Packing Specification

Item	Specification	Remark
LCD Packing	10ea / (Packing-Pallet Box)	1. 100 Kg / LCD (10ea) 2. 7 Kg / Cushion-pallet (2ea) 3. 6.7 Kg / Packing-Pallet Box (1ea) 4. Cushion-pallet Material : EPS 5. Packing-Pallet Box Material : DW4
Pallet	1Box / Pallet	1. Pallet weight = 8kg
Packing Direction	Vertical	
Total Pallet Size	H x V x height	1150mm(H) x 985mm(V) x 609mm(height)
Total Pallet Weight	121.7 kg	Pallet(8kg) + Module (10.0*10=100kg) + Cushion (up + bottom=7kg) + Pallet-BOX(6.7kg)



8.3 Packing Storage condition

ITEM	Unit	Min.	Max.
Storage Temperature	(°C)	5	40
Storage Humidity	(%rH)	35	75
Storage life	12 months		
Storage Condition	<ul style="list-style-type: none"> - The storage room should provide good ventilation and temperature control. - Products should not be placed on the floor, but on the Pallet away from a wall. - Prevent products from direct sunlight, moisture nor water; Be cautious of a build up of condensation. - Avoid other hazardous environment while storing goods. - If products delivered or kept in conditions of over the storage period of 3 months, the recommended temperature or humidity range, we recommend you leave them at a temperature of 20 °C and a humidity of 50% for 24 hours. 		

8.4 Packing long-term Storage guide

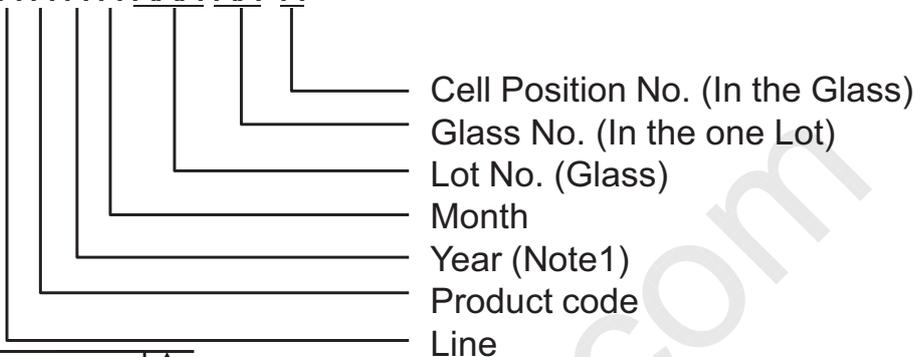
Long –term Storage Process	More than 3months Storage or Low temp. Delivery/under 5 °C Storage, →On the 20 °C 50%rH Condition , More than 10hrs release.
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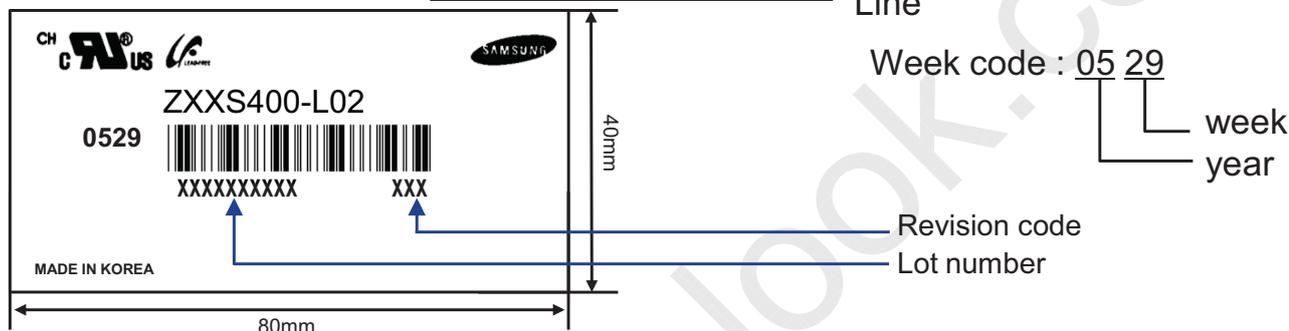
9. MARKING & OTHERS

A nameplate bearing followed by is affixed to a shipped product at the specified location on each product.

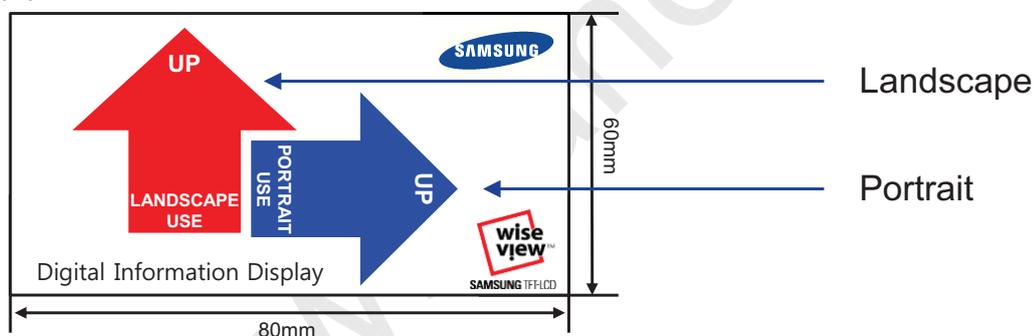
- (1) Part number : ZXXS400-L02
- (2) Revision: Three letters
- (3) Lot number : X X X X XXX XX X



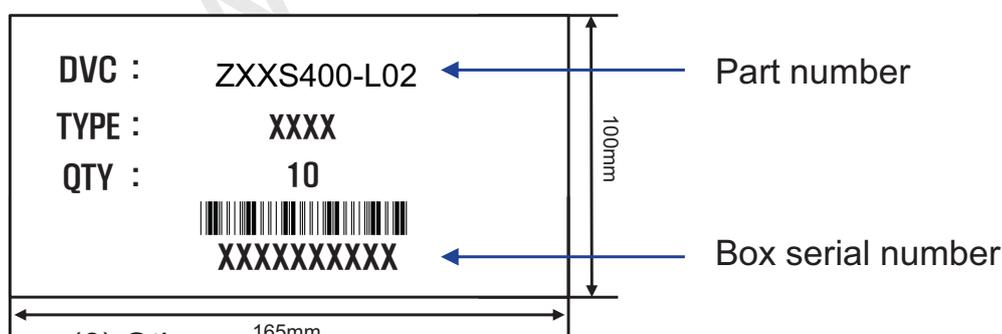
(4) Nameplate Indication



(5) Landscape / Portrait Direction Indication



(6) Packing box attach



(6) Others

- 1. After service part
Lamps cannot be replaced because of the narrow bezel structure.

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10. General Precautions



10.1 Handling

- (a) When the Module is assembled, it should be attached to the system firmly using all mounting holes. Be careful not to twist and bend the Module.
- (b) Because the inverter use high voltage, it should be disconnected from power before it is assembled or disassembled.
- (c) Refrain from strong mechanical shock and / or any force to the Module. In addition to damage, this may cause improper operation or damage to the Module and CCFT back light.
- (d) Note that polarizers are very fragile and could be damage easily. Do not press or scratch the surface harder than a HB pencil lead.
- (e) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, staining or discoloration may occur.
- (f) If the surface of the polarizer is dirty, clean it using absorbent cotton or soft cloth.
- (g) Desirable cleaners are water, IPA(Isopropyl Alcohol) or Hexane. Do not use Ketone type materials(ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
- (h) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth . In case of contact with hands, legs or clothes, it must be washed away with soap thoroughly.
- (i) Protect the Module from static, or the CMOS Gate Array IC would be damaged.
- (j) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (k) Do not disassemble the Module.
- (l) Do not pull or fold the lamp wire.
- (m) Do not adjust the variable resistor located on the Module.
- (n) Protection film for polarizer on the Module should be slowly peeled off just before use so that the electrostatic charge can be minimized.
- (o) Pins of I/F connector should not be touched directly with bare hands.



10.2 Storage

- (a) Do not leave the Module in high temperature, and high humidity for a long time. It is highly recommended to store the Module with temperature from 0 to 35 °C and relative humidity of less than 70%.
- (b) Do not store the TFT-LCD Module in direct sunlight.
- (c) The Module should be stored in a dark place. It is prohibited to apply sunlight or fluorescent light in storing.

10.3 Operation

- (a) Do not connect or disconnect the Module in the "Power On" condition.
- (b) Power supply should always be turned on/off by the "Power on/off sequence"
- (c) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference should be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- (d) The cable between the back light connector and its inverter power supply should be connected directly with a minimized length. A longer cable between the back light and the inverter may cause lower luminance of lamp(CCFT) and may require higher startup voltage(Vs).

10.4 Operation Condition Guide

- (a) The LCD product should be operated under normal conditions. Normal condition is defined as below;
 - Temperature : $20 \pm 15^{\circ}\text{C}$
 - Humidity : $55 \pm 20\%$
 - Display pattern : continually changing pattern (Not stationary)
- (b) If the product will be used in extreme conditions such as high temperature, humidity, display patterns or operation time etc., It is strongly recommended to contact SEC for Application engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at Airports, Transit Stations, Banks, Stock market, and Controlling systems.

10.5 Others



- (a) Ultra-violet ray filter is necessary for outdoor operation.
- (b) Module should be turned clockwise (regular front view perspective) when used in portrait mode
- (c) Avoid condensation of water. It may result in improper operation or disconnection of electrode.
- (d) Do not exceed the absolute maximum rating value. (supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on)
Otherwise the Module may be damaged.
- (e) If the Module keeps displaying the same pattern for a long period of time, the image may be "sticked" to the screen.
To avoid image sticking, it is recommended to use a screen saver.
- (f) This Module has its circuitry PCB's on the rear side and should be handled carefully in order not to be stressed.
- (g) Please contact SEC in advance when you display the same pattern for a long time.