

Main Product Characteristics

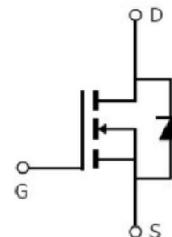
V_{DSS}	500V
$R_{DS(on)}$	0.22ohm(typ.)
I_D	18A



TO-220F



Marking and Pin Assignment



Schematic Diagram

Features and Benefits

- Advanced Process Technology
- Special designed for PWM, load switching and general purpose applications
- Ultra low on-resistance with low gate charge
- Fast switching and reverse body recovery
- 150°C operating temperature
- Lead free product



Description

These N-Channel enhancement mode power field effect transistors are produced using silikron proprietary MOSFET technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switch mode power supplies

Absolute Max Rating

Symbol	Parameter	Max.	Units
$I_D @ TC = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$ ①	18	A
$I_D @ TC = 100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$ ①	10.8	
I_{DM}	Pulsed Drain Current②	72	
$P_D @ TC = 25^\circ C$	Power Dissipation③	38	W
	Linear Derating Factor	0.3	W/°C
V_{DS}	Drain-Source Voltage	500	V
V_{GS}	Gate-to-Source Voltage	± 30	V
E_{AS}	Single Pulse Avalanche Energy @ L=5.2mH	315	mJ
I_{AS}	Avalanche Current @ L=5.2mH	11	A
$T_J T_{STG}$	Operating Junction and Storage Temperature Range	-55 to + 150	°C

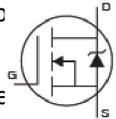
Thermal Resistance

Symbol	Characteristics	Typ.	Max.	Units
$R_{\theta JC}$	Junction-to-case ^③	—	3.3	$^{\circ}C/W$
$R_{\theta JA}$	Junction-to-ambient ($t \leq 10s$) ^④	—	62	$^{\circ}C/W$
	Junction-to-Ambient (PCB mounted, steady-state) ^④	—	40	$^{\circ}C/W$

Electrical Characteristics @ $T_A=25^{\circ}C$ unless otherwise specified

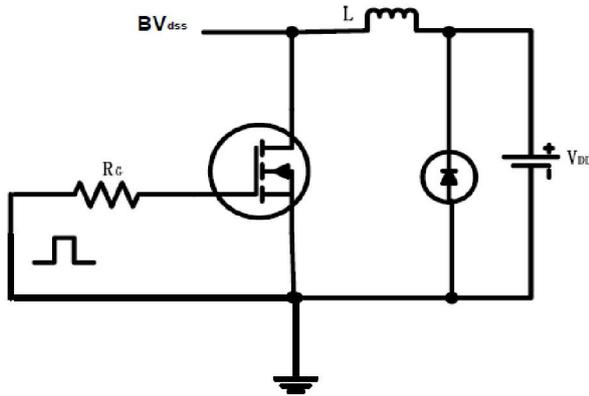
Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(BR)DSS}$	Drain-to-Source breakdown voltage	500	—	—	V	$V_{GS} = 0V, I_D = 250\mu A$
$R_{DS(on)}$	Static Drain-to-Source on-resistance	—	0.22	0.35	Ω	$V_{GS}=10V, I_D = 9A$
		—	0.49	—		$T_J = 125^{\circ}C$
$V_{GS(th)}$	Gate threshold voltage	2	—	4	V	$V_{DS} = V_{GS}, I_D = 250\mu A$
		—	3.1	—		$T_J = 125^{\circ}C$
I_{DSS}	Drain-to-Source leakage current	—	—	1	μA	$V_{DS} = 500V, V_{GS} = 0V$
		—	—	50		$T_J = 125^{\circ}C$
I_{GSS}	Gate-to-Source forward leakage	—	—	100	nA	$V_{GS} = 30V$
		-100	—	—		$V_{GS} = -30V$
Q_g	Total gate charge	—	39.4	—	nC	$I_D = 18A,$ $V_{DS}=400V,$ $V_{GS} = 10V$
Q_{gs}	Gate-to-Source charge	—	18.3	—		
Q_{gd}	Gate-to-Drain("Miller") charge	—	8.9	—		
$t_{d(on)}$	Turn-on delay time	—	54.3	—	nS	$V_{GS}=10V, V_{DS} =270V,$ $R_L=15\Omega,$ $R_{GEN}=25\Omega$ $I_D =18A$
t_r	Rise time	—	80.3	—		
$t_{d(off)}$	Turn-Off delay time	—	77.3	—		
t_f	Fall time	—	45.0	—		
C_{iss}	Input capacitance	—	2360	—	pF	$V_{GS} = 0V$ $V_{DS} = 25V$ $f = 400KHz$
C_{oss}	Output capacitance	—	290	—		
C_{rss}	Reverse transfer capacitance	—	6	—		

Source-Drain Ratings and Characteristics

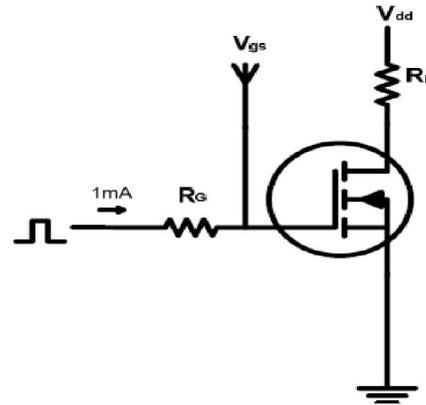
Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
I_S	Continuous Source Current (Body Diode)	—	—	18	A	MOSFET symb showing the integral reverse p-n junction diode. 
I_{SM}	Pulsed Source Current (Body Diode)	—	—	72	A	
V_{SD}	Diode Forward Voltage	—	0.92	1.3	V	$I_S=18A, V_{GS}=0V$
t_{rr}	Reverse Recovery Time	—	450	—	nS	$T_J = 25^{\circ}C, I_F = 18A, di/dt = 100A/\mu s$
Q_{rr}	Reverse Recovery Charge	—	6250	—	nC	

Test Circuits and Waveforms

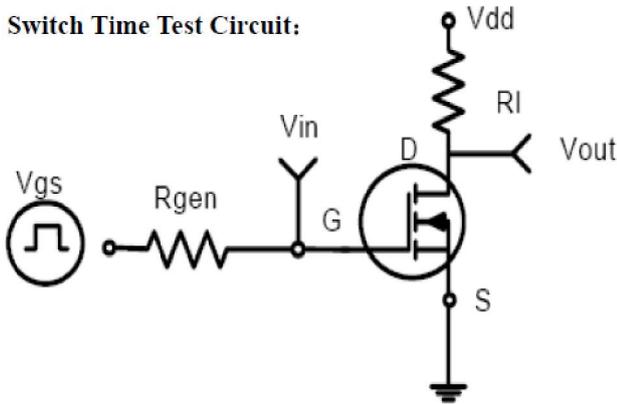
EAS test circuits:



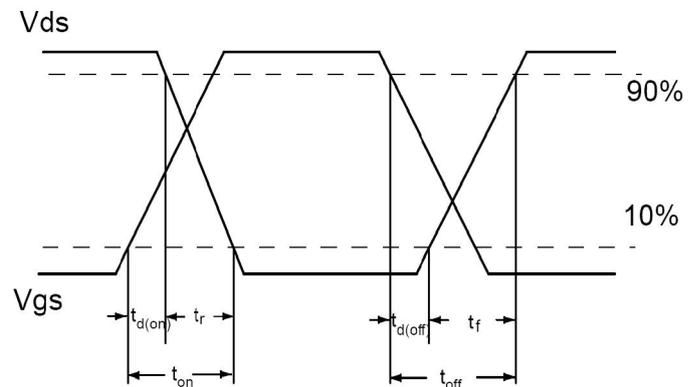
Gate charge test circuit:



Switch Time Test Circuit:



Waveforms:



Notes:

- ① The maximum current rating is limited by bond-wires.
- ② Repetitive rating; pulse width limited by max. junction temperature.
- ③ The power dissipation PD is based on max. junction temperature, using junction-to-case thermal resistance.
- ④ The value of $R_{\theta JA}$ is measured with the device mounted on 1in 2 FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^\circ\text{C}$
- ⑤ These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of $T_{J(MAX)} = 150^\circ\text{C}$.
- ⑥ The maximum current rating is limited by bond-wires.

Typical Electrical and Thermal Characteristics

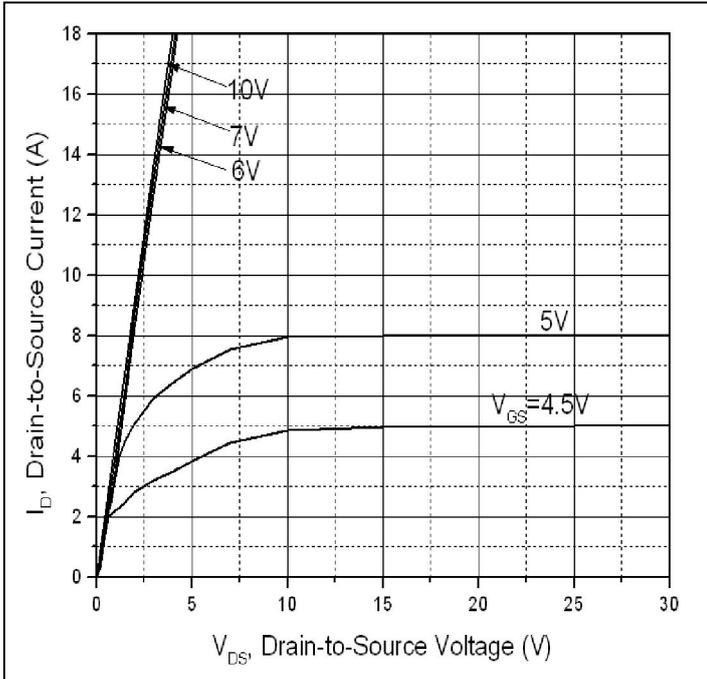


Figure 1: Typical Output Characteristics

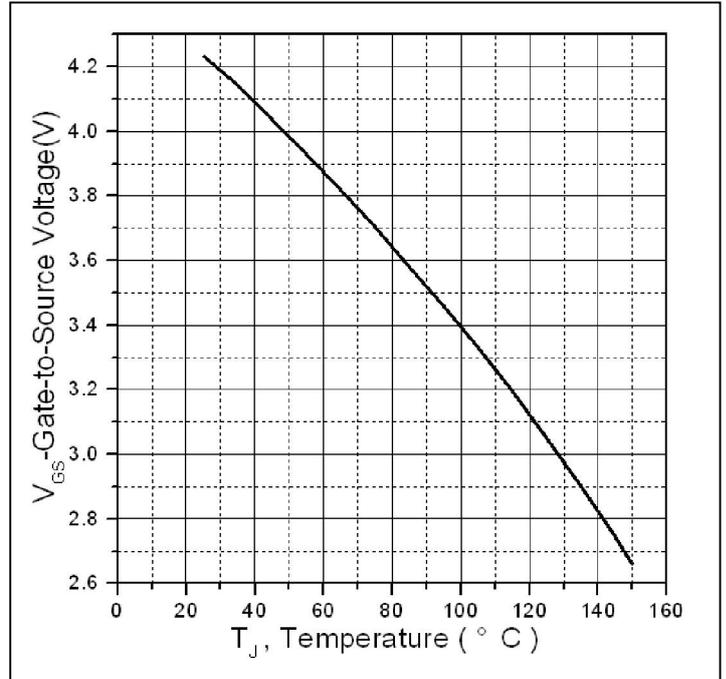


Figure 2: Gate to source cut-off voltage

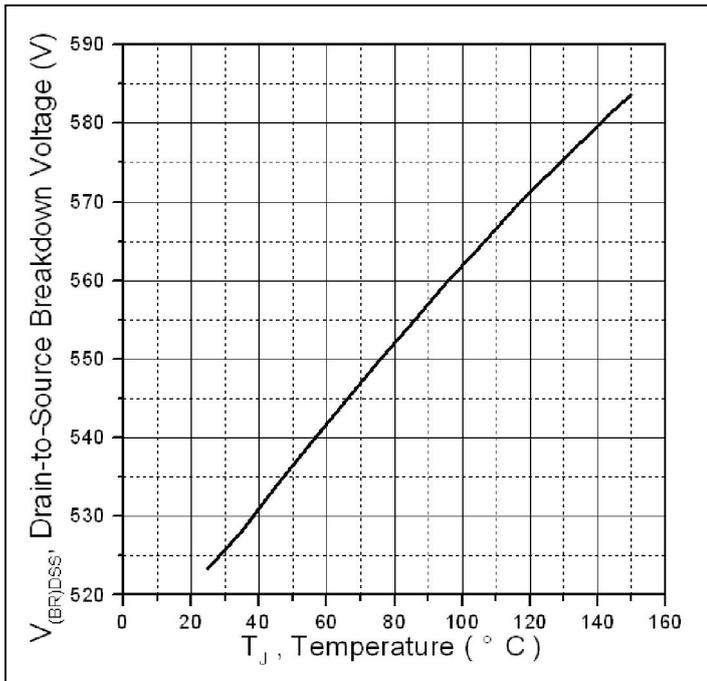


Figure 3: Drain-to-Source Breakdown Voltage vs. Temperature

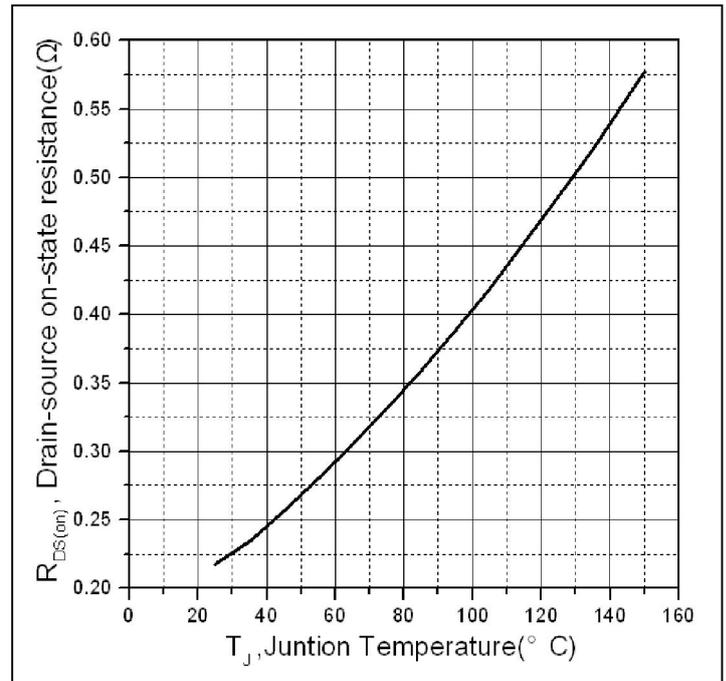


Figure 4: Normalized On-Resistance Vs. Case Temperature

Typical Electrical and Thermal Characteristics

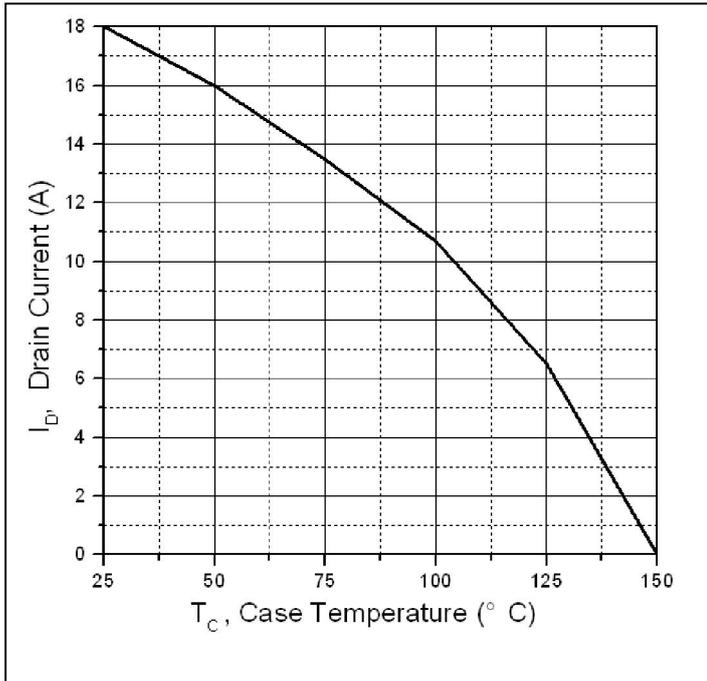


Figure 5. Maximum Drain Current Vs. Case Temperature

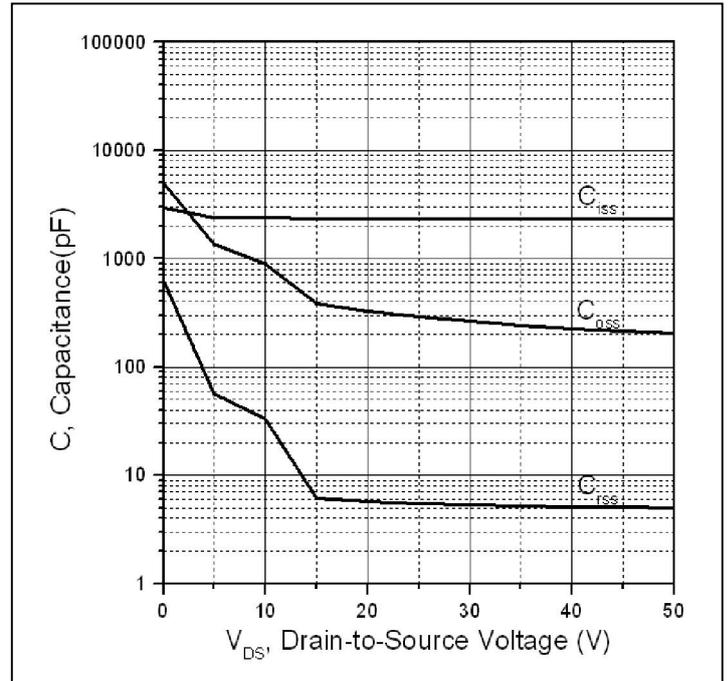


Figure 6. Typical Capacitance Vs. Drain-to-Source Voltage

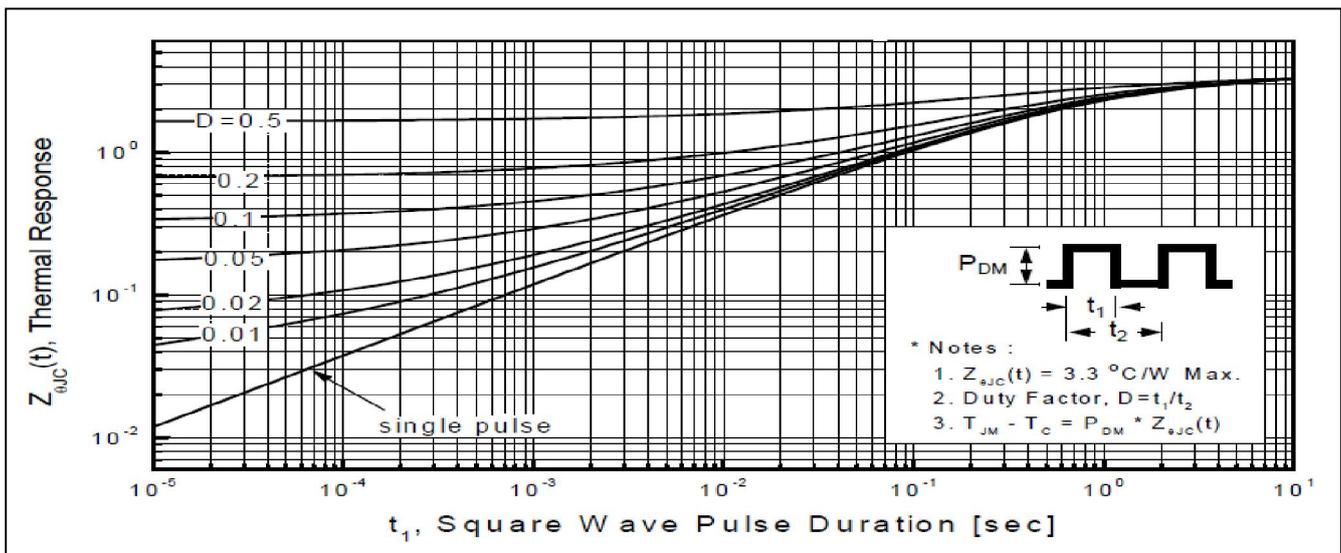
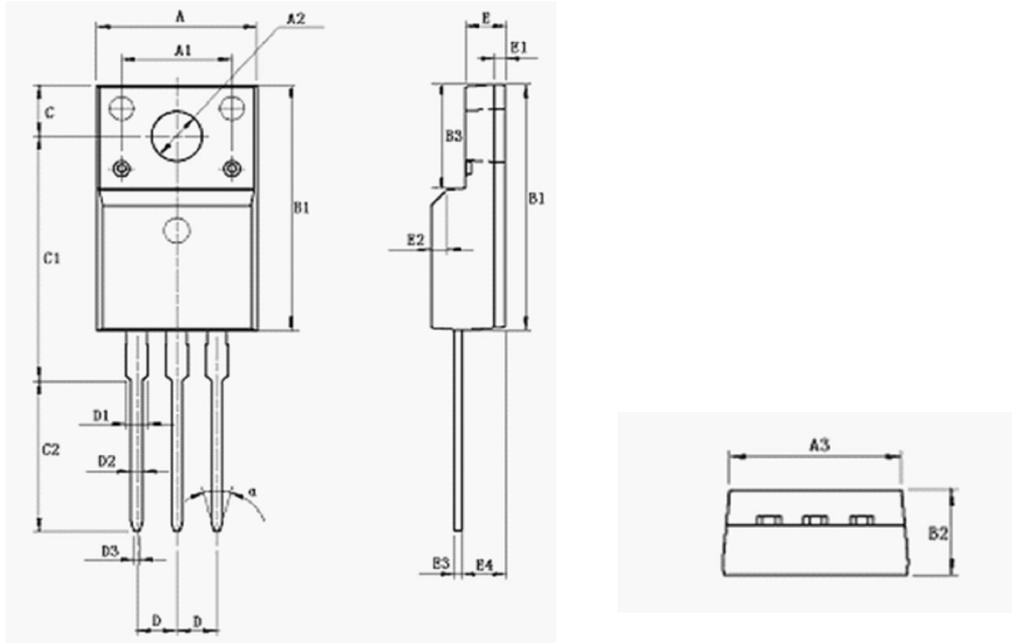


Figure7. Maximum Effective Transient Thermal Impedance, Junction-to-Case

Mechanical Data

TO220F PACKAGE OUTLINE DIMENSION



Symbol	Dimension In Millimeters			Dimension In Inches		
	Min	Nom	Max	Min	Nom	Max
A	9.960	10.160	10.360	0.392	0.400	0.408
A1	7.000			0.276	0.000	0.000
A2	3.080	3.180	3.280	0.121	0.125	0.129
A3	9.260	9.460	9.660	0.365	0.372	0.380
B1	15.670	15.870	16.070	0.617	0.625	0.633
B2	4.500	4.700	4.900	0.177	0.185	0.193
B3	6.480	6.680	6.880	0.255	0.263	0.271
C	3.200	3.300	3.400	0.126	0.130	0.134
C1	15.600	15.800	16.000	0.614	0.622	0.630
C2	9.550	9.750	9.950	0.376	0.384	0.392
D	2.54 (TYP)			1.00 (TYP)		
D1	-	-	1.470	-	-	0.058
D2	0.700	0.800	0.900	0.028	0.031	0.035
D3	0.250	0.350	0.450	0.010	0.014	0.018
E	2.340	2.540	2.740	0.092	0.100	0.108
E1	0.700			0.028		
E2	1.0*45 ⁰			1.0*45 ⁰		
E3	0.450	0.500	0.600	0.018	0.020	0.024
E4	2.560	2.760	2.960	0.101	0.109	0.117
□	30 ⁰			30 ⁰		



Ordering and Marking Information

Device Marking: SSF18N50F

Package (Available)
TO220F
Operating Temperature Range
C : -55 to 150 °C

Devices per Unit

Package Type	Units/Tube	Tubes/Inner Box	Units/Inner Box	Inner Boxes/Carton Box	Units/Carton Box
TO-220F	50	20	1000	6	6000

Reliability Test Program

Test Item	Conditions	Duration	Sample Size
High Temperature Reverse Bias(HTRB)	$T_j=125^{\circ}\text{C}$ to 150°C @ 80% of Max $V_{DSS}/V_{CES}/V_R$	168 hours 500 hours 1000 hours	3 lots x 77 devices
High Temperature Gate Bias(HTGB)	$T_j=150^{\circ}\text{C}$ @ 100% of Max V_{GSS}	168 hours 500 hours 1000 hours	3 lots x 77 devices