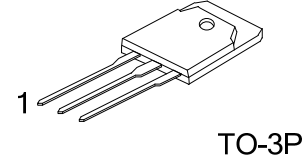
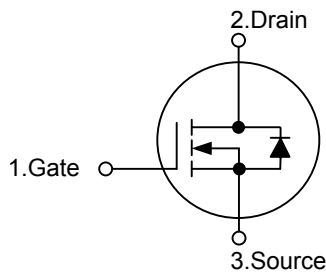


**9N90-Q****Power MOSFET****9A, 900V N-CHANNEL
POWER MOSFET****DESCRIPTION**

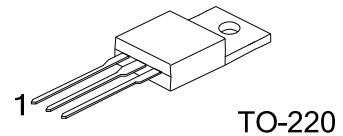
The UTC **9N90-Q** uses UTC's advanced proprietary, planar stripe, DMOS technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with low gate voltages. This device is suitable for use as a load switch or in PWM applications.

FEATURES

- * $R_{DS(ON)} < 1.4\Omega$ @ $V_{GS} = 10V$, $I_D = 4.5A$
- * Fast Switching Capability
- * Avalanche Energy Specified
- * Improved dv/dt Capability, High Ruggedness

SYMBOL

TO-3P

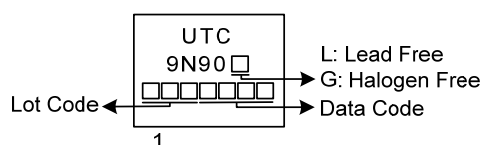


TO-220

ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
9N90L-TA3-T	9N90G-TA3-T	TO-220	G	D	S	Tube
9N90L-T3P-T	9N90G-T3P-T	TO-3P	G	D	S	Tube

	(1) T: Tube (2) TA3: TO-220, T3P: TO-3P (3) L: Lead Free, G: Halogen Free and Lead Free
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MARKING

■ **ABSOLUTE MAXIMUM RATING** ($T_C = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	900	V
Gate-Source Voltage		V_{GSS}	± 30	V
Continuous Drain Current ($T_C = 25^\circ\text{C}$)		I_D	9.0	A
Pulsed Drain Current (Note 2)		I_{DM}	36	A
Avalanche Current (Note 2)		I_{AR}	9.0	A
Avalanche Energy	Single Pulsed(Note 3)	E_{AS}	500	mJ
	Repetitive(Note 2)	E_{AR}	28	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.0	V/ns
Power Dissipation	TO-220	P_D	147	W
	TO-3P		208	W
Linear Derating Factor above $T_C = 25^\circ\text{C}$	TO-220		1.176	W/ $^\circ\text{C}$
	TO-3P		1.66	W/ $^\circ\text{C}$
Junction Temperature		T_J	150	$^\circ\text{C}$
Storage Temperature		T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating : Pulse width limited by maximum junction temperature

3. $L = 12.35\text{mH}$, $I_{AS} = 9.0\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 25\ \Omega$, Starting $T_J = 25^\circ\text{C}$

4. $I_{SD} \leq 9.0\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$

■ **THERMAL DATA**

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	TO-220	θ_{JA}	62.5	$^\circ\text{C}/\text{W}$
	TO-3P		40	$^\circ\text{C}/\text{W}$
Junction to Case	TO-220	θ_{JC}	0.85	$^\circ\text{C}/\text{W}$
	TO-3P		0.6	$^\circ\text{C}/\text{W}$

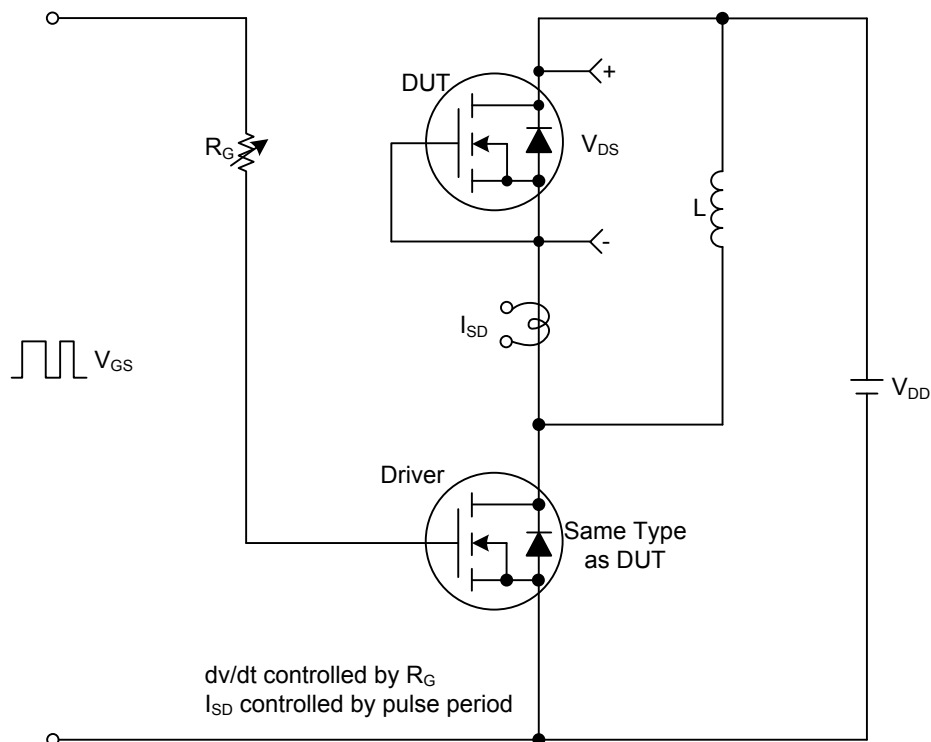
■ **ELECTRICAL CHARACTERISTICS** ($T_J = 25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} = 0 V, I _D = 250μA	900			V
Drain-Source Leakage Current		I _{DSS}	V _{DS} = 900 V, V _{GS} = 0 V			10	μA
Gate-Body Leakage Current	Forward	I _{GSSF}	V _{GS} = 30 V, V _{DS} = 0 V			100	nA
	Reverse	I _{GSSR}	V _{GS} = -30 V, V _{DS} = 0 V			-100	nA
Breakdown Voltage Temperature Coefficient		ΔBV _{DSS} /ΔT _J	I _D =250μA, Referenced to 25°C		0.99		V/°C
ON CHARACTERISTICS							
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} = V _{GS} , I _D = 250μA	3.0		5.0	V
Static Drain-Source On-Resistance		R _{DS(ON)}	V _{GS} = 10V, I _D = 4.5A		1.26	1.4	Ω
DYNAMIC PARAMETERS							
Input Capacitance		C _{ISS}	V _{DS} = 25V, V _{GS} = 0V, f = 1.0 MHz		1450	2730	pF
Output Capacitance		C _{OSS}			157	230	pF
Reverse Transfer Capacitance		C _{RSS}			21	26	pF
SWITCHING CHARACTERISTICS							
Turn-On Delay Time		t _{D(ON)}	V _{DD} = 30V, I _D =0.5 A, R _G = 25Ω (Note 1, 2)		95	110	ns
Turn-On Rise Time		t _R			200	250	ns
Turn-Off Delay Time		t _{D(OFF)}			340	390	ns
Turn-Off Fall Time		t _F			200	250	ns
Total Gate Charge		Q _G	V _{DS} =50V, I _D = 1.3A, V _{GS} = 10 V (Note 1,2)		56	70	nC
Gate-Source Charge		Q _{GS}			7.4		nC
Gate-Drain Charge		Q _{GD}			17		nC
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS							
Drain-Source Diode Forward Voltage		V _{SD}	V _{GS} = 0 V, I _S = 9.0 A			1.4	V
Maximum Continuous Drain-Source Diode Forward Current		I _S				9.0	A
Maximum Pulsed Drain-Source Diode Forward Current		I _{SM}				36	A

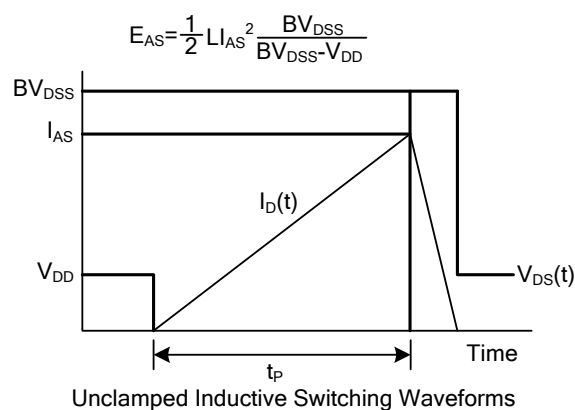
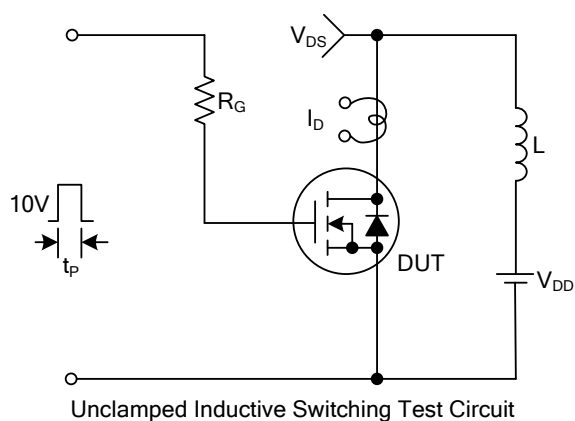
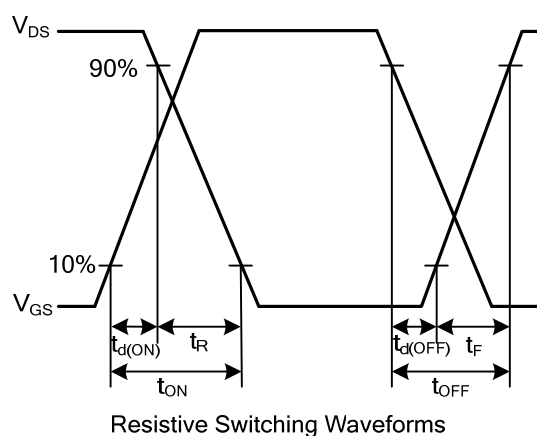
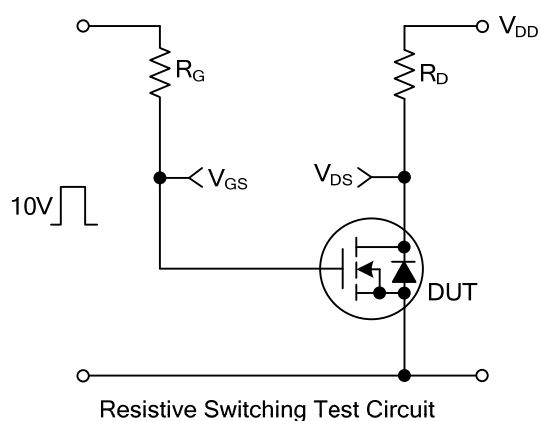
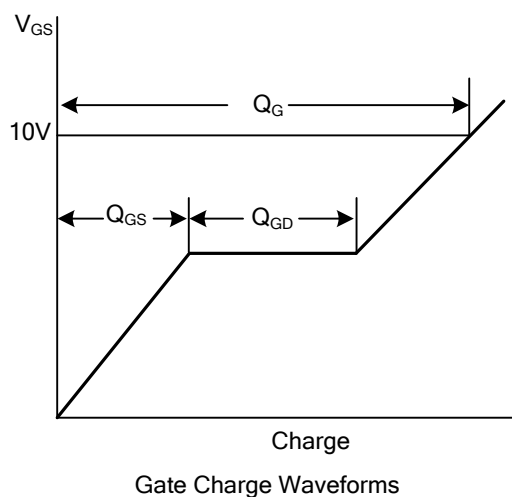
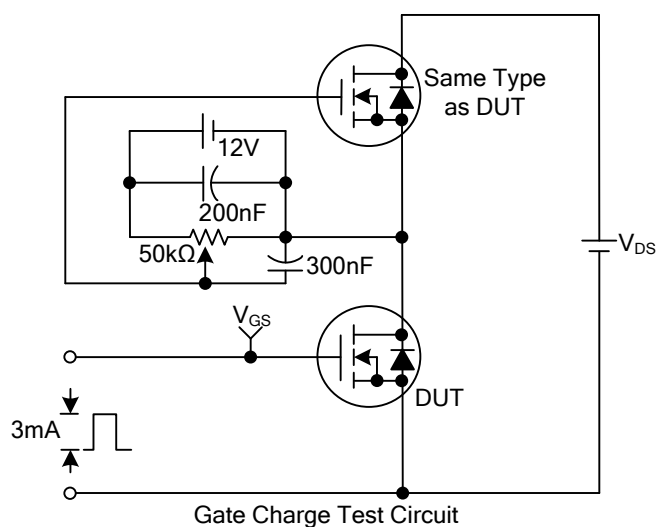
Notes: 1. Pulse Test : Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$

2. Essentially independent of operating temperature

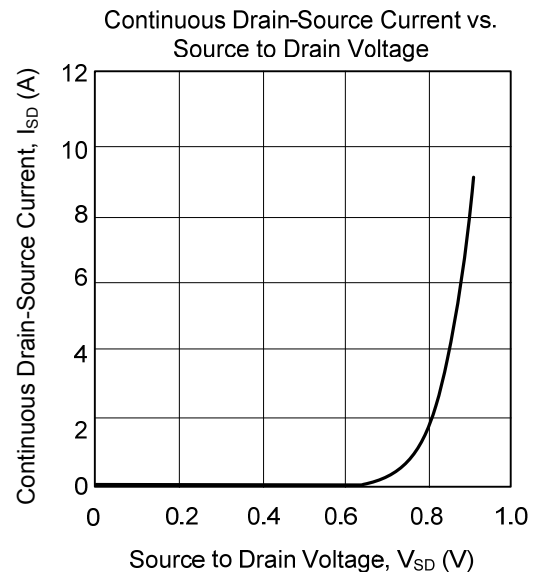
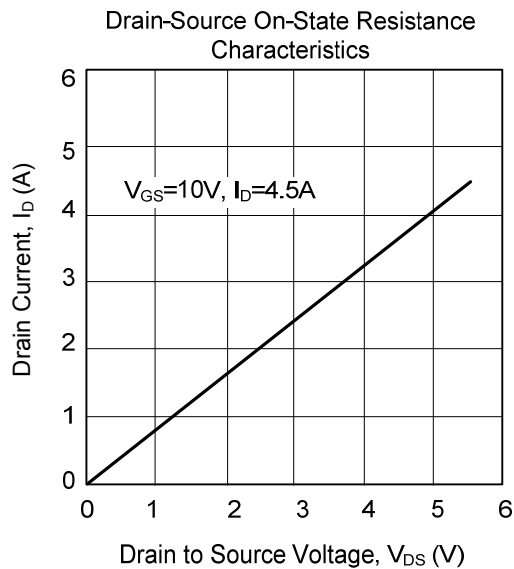
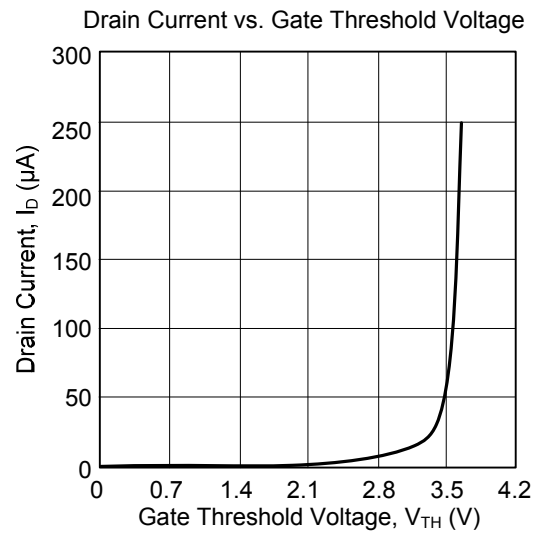
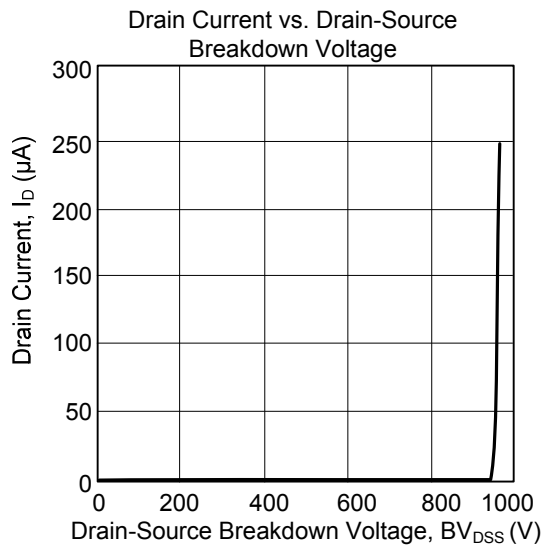
■ TEST CIRCUITS AND WAVEFORMS

Peak Diode Recovery dv/dt Test Circuit & Waveforms

■ TEST CIRCUITS AND WAVEFORMS(Cont.)



TYPICAL CHARACTERISTICS



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