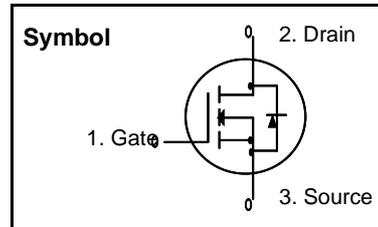


## N-Channel MOSFET

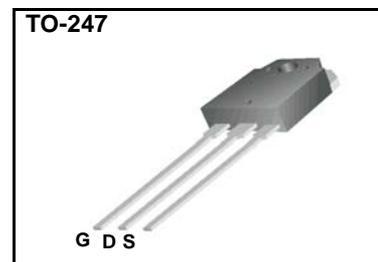
### Features

- $R_{DS(on)}$  (Max 1.2  $\Omega$ )@ $V_{GS}=10V$
- Gate Charge (Typical 45nC)
- Improved dv/dt Capability, High Ruggedness
- 100% Avalanche Tested
- Maximum Junction Temperature Range (150°C)



### General Description

This Power MOSFET is produced using Wisdom's advanced planar stripe, DMOS technology. This latest technology has been especially designed to minimize on-state resistance, have a high rugged avalanche characteristics. These devices are well suited for high efficiency switch mode power supplies, active power factor correction, electronic lamp ballasts based on half bridge topology.



### Absolute Maximum Ratings

Symbol	Parameter	Value	Units
$V_{DSS}$	Drain to Source Voltage	700	V
$I_D$	Continuous Drain Current(@ $T_C = 25^\circ C$ )	9.0	A
	Continuous Drain Current(@ $T_C = 100^\circ C$ )	5.4	A
$I_{DM}$	Drain Current Pulsed (Note 1)	36	A
$V_{GS}$	Gate to Source Voltage	$\pm 30$	V
$E_{AS}$	Single Pulsed Avalanche Energy (Note 2)	654	mJ
$E_{AR}$	Repetitive Avalanche Energy (Note 1)	17.8	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	4.5	V/ns
$P_D$	Total Power Dissipation(@ $T_C = 25^\circ C$ )	178	W
	Derating Factor above 25 °C	1.42	W/°C
$T_{STG}, T_J$	Operating Junction Temperature & Storage Temperature	- 55 ~ 150	°C
$T_L$	Maximum Lead Temperature for soldering purpose, 1/8 from Case for 5 seconds.	300	°C

### Thermal Characteristics

Symbol	Parameter	Value			Units
		Min.	Typ.	Max.	
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	-	-	0.7	°C/W
$R_{\theta CS}$	Thermal Resistance, Case to Sink	-	0.24	-	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	-	-	40	°C/W

# WFW9N70

## Electrical Characteristics (T<sub>C</sub> = 25 °C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
<b>Off Characteristics</b>						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250uA	700	-	-	V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature coefficient	I <sub>D</sub> = 250uA, referenced to 25 °C	-	0.6	-	V/°C
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> = 700V, V <sub>GS</sub> = 0V	-	-	10	uA
		V <sub>DS</sub> = 560V, T <sub>C</sub> = 125 °C	-	-	100	uA
I <sub>GSS</sub>	Gate-Source Leakage, Forward	V <sub>GS</sub> = 30V, V <sub>DS</sub> = 0V	-	-	100	nA
	Gate-source Leakage, Reverse	V <sub>GS</sub> = -30V, V <sub>DS</sub> = 0V	-	-	-100	nA
<b>On Characteristics</b>						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250uA	2.5	-	4.5	V
R <sub>DS(ON)</sub>	Static Drain-Source On-state Resistance	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 4.5A	-	0.90	1.2	Ω
<b>Dynamic Characteristics</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 25V, f = 1MHz	-	1600	-	pF
C <sub>oss</sub>	Output Capacitance		-	170	-	
C <sub>rss</sub>	Reverse Transfer Capacitance		-	20	-	
<b>Dynamic Characteristics</b>						
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DD</sub> = 350V, I <sub>D</sub> = 9.0A, R <sub>G</sub> = 25Ω (Note 4, 5)	-	30	-	ns
t <sub>r</sub>	Rise Time		-	70	-	
t <sub>d(off)</sub>	Turn-off Delay Time		-	150	-	
t <sub>f</sub>	Fall Time		-	80	-	
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> = 560V, V <sub>GS</sub> = 10V, I <sub>D</sub> = 9.0A (Note 4, 5)	-	45	-	nC
Q <sub>gs</sub>	Gate-Source Charge		-	7	-	
Q <sub>gd</sub>	Gate-Drain Charge(Miller Charge)		-	20	-	

## Source-Drain Diode Ratings and Characteristics

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit.
I <sub>S</sub>	Continuous Source Current	Integral Reverse p-n Junction Diode in the MOSFET	-	-	9.0	A
I <sub>SM</sub>	Pulsed Source Current		-	-	36.0	
V <sub>SD</sub>	Diode Forward Voltage	I <sub>S</sub> = 9.0A, V <sub>GS</sub> = 0V	-	-	1.4	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>S</sub> = 9.0A, V <sub>GS</sub> = 0V, di <sub>F</sub> /dt = 100A/us	-	0.45	-	us
Q <sub>rr</sub>	Reverse Recovery Charge		-	4.5	-	uC

### ※ NOTES

1. Repeatability rating : pulse width limited by junction temperature
2. L = 15mH, I<sub>AS</sub> = 9.0A, V<sub>DD</sub> = 50V, R<sub>G</sub> = 25Ω, Starting T<sub>J</sub> = 25°C
3. I<sub>SD</sub> ≤ 9.0A, di/dt ≤ 200A/us, V<sub>DD</sub> ≤ BV<sub>DSS</sub>, Starting T<sub>J</sub> = 25°C
4. Pulse Test : Pulse Width ≤ 300us, Duty Cycle ≤ 2%
5. Essentially independent of operating temperature.