



Parameter	Ratings	Units
Blocking Voltage	800	V_P
Load Current	250	mA_{rms}
On State Voltage Drop	3	V_{rms} (at $I_L = 250 mA_{rms}$)
Operating Voltage	550	V_{rms}

Features

- Load Current up to $250mA_{rms}$
- $800V_P$ Blocking Voltage
- 5mA Sensitivity
- Zero-Crossing Detection
- DC Control, AC Output
- Optically Isolated
- TTL and CMOS Compatible
- Low EMI and RFI Generation
- High Noise Immunity
- Machine Insertable, Wave Solderable
- Flammability Classification Rating of V-0

Applications

- Programmable Control
- Process Control
- Power Control Panels
- Remote Switching
- Gas Pump Electronics
- Contactors
- Large Relays
- Solenoids
- Motors
- Heaters

Description

The CPC1972 is an AC Solid State Switch using optical coupling with dual power silicon controlled rectifier (SCR) outputs to produce an alternative to optocoupler and Triac circuits. The CPC1972 switches are robust enough to provide a blocking voltage of up to $800V_P$. In addition, tightly controlled zero cross circuitry ensures switching of AC loads without the generation of transients. The input and output circuits are optically coupled to provide $3750V_{rms}$ of isolation and noise immunity between the control and load circuits. As a result the CPC1972 is well suited for industrial environments where electromagnetic interference could disrupt the operation of electromechanical relays.

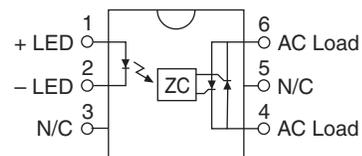
Approvals

- UL Recognized Component: File E69938
- CSA Certified Component: Certificate 1172007

Ordering Information

Part Number	Description
CPC1972G	6-Pin DIP (50/Tube)
CPC1972GS	6-Pin Surface Mount (50/Tube)
CPC1972GSTR	6-Pin Surface Mount (1000/Reel)

Pin Configuration



Absolute Maximum Ratings @ 25°C

Parameter	Ratings	Units
Blocking Voltage	800	V _P
Reverse Input Voltage	5	V
Input Control Current	50	mA
Peak (10ms)	1	A
Input Power Dissipation ¹	150	mW
Total Package Dissipation ²	800	mW
Isolation Voltage, Input to Output	3750	V _{rms}
Operational Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	°C

¹ Derate linearly 1.33 mW / °C

² Derate linearly 6.67 mW / °C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Electrical Characteristics @ 25°C

Parameters	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics						
Operating Voltage Range	-	V _L	5	-	550	V _{rms}
Load Current						
Continuous	-	I _L	5	-	250	mA _{rms}
Peak	t = 10ms	I _{TSM}	-	-	2	A _P
Off State Leakage Current	V _L =800V _P	I _{LEAK}	-	-	1	μA
On-State Voltage Drop	I _L = 250mA _{rms}	V _{ON}	-	-	3	V _{rms}
Critical Rate of Rise	-	dV/dt	500	-	-	V/μs
Holding Current	I _F =5mA	I _H	-	300	-	μA
Switching Speeds						
Turn-on	I _F =5mA	t _{on}	-	-	0.5	cycles
Turn-off		t _{off}	-	-	0.5	
Zero-Cross Turn-On Voltage ¹	1st half-cycle	-	-	5	20	V
	Subsequent half-cycle	-	-	1	-	V
Operating Frequency	-	-	20	-	500	Hz
Load Power Factor for Guaranteed Turn-On ²	f=60Hz	PF	0.25	-	-	-
Input Characteristics						
Input Control Current ³	-	I _F	-	-	5	mA
Input Voltage Drop	I _F =5mA	V _F	0.9	1.2	1.4	V
Input Drop-out Voltage	-	-	0.8	-	-	V
Reverse Input Current	V _R =5V	I _R	-	-	10	μA
Common Characteristics						
Input to Output Capacitance	-	C _{I/O}	-	3	-	pF

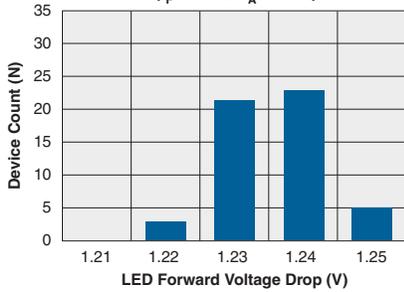
¹ Zero Cross 1st half-cycle @ <100Hz

² Snubber circuits may be required at low power factors.

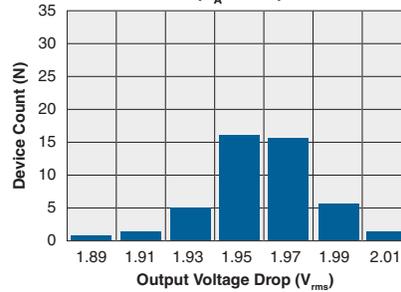
³ For high noise environment use at least 10mA LED current

PERFORMANCE DATA *

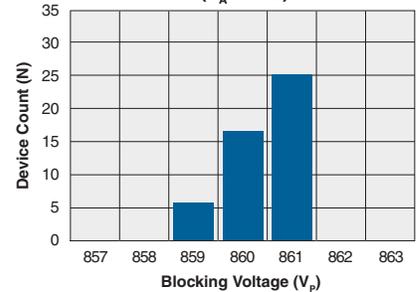
Typical LED Forward Voltage Drop
($I_F=5\text{mA}$, $T_A=25^\circ\text{C}$)



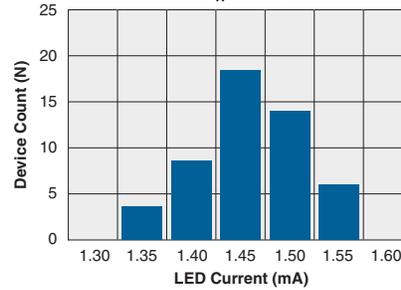
Typical On-State Output Forward Voltage Distribution
($T_A=25^\circ\text{C}$)



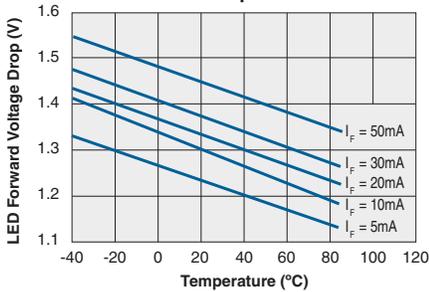
Typical Blocking Voltage Distribution
($T_A=25^\circ\text{C}$)



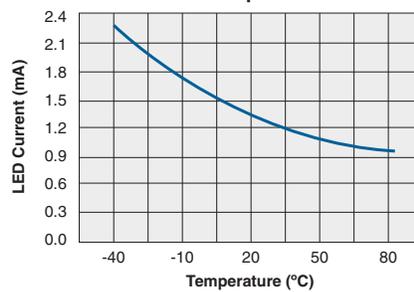
Typical I_F for Switch Operation
($T_A=25^\circ\text{C}$)



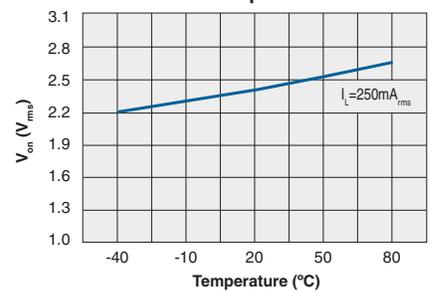
Typical LED Forward Voltage Drop vs. Temperature



Typical I_F for Switch Operation vs. Temperature



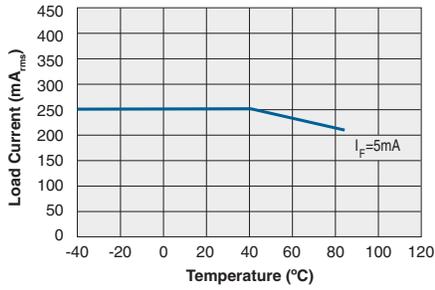
Typical Output Voltage Drop vs. Temperature



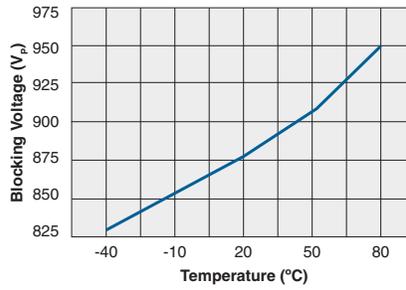
*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

PERFORMANCE DATA*

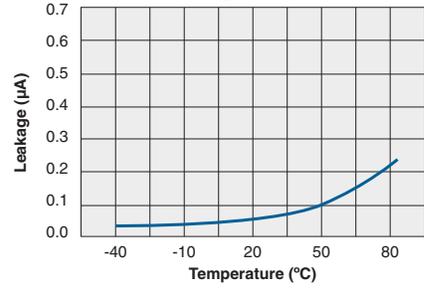
Typical Maximum Load Current vs. Temperature



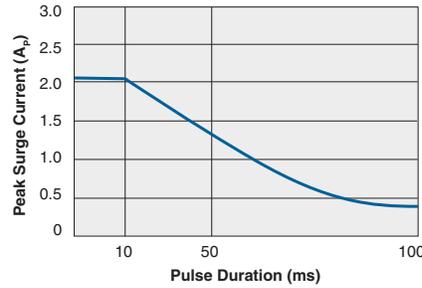
Typical Blocking Voltage vs. Temperature



Typical Leakage vs. Temperature (V_L=800V_p)



Maximum Surge Current (Non-Repetitive) (T_J=50°C max)



*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

Manufacturing Information

Moisture Sensitivity



All plastic encapsulated semiconductor packages are susceptible to moisture ingress. Clare classified all of its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, **IPC/JEDEC J-STD-020**, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL) rating** as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Rating
CPC1972G / CPC1972GS	MSL 1

ESD Sensitivity



This product is **ESD Sensitive**, and should be handled according to the industry standard **JESD-625**.

Reflow Profile

This product has a maximum body temperature and time rating as shown below. All other guidelines of **J-STD-020** must be observed.

Device	Maximum Temperature x Time
CPC1972G / CPC1972GS	250°C for 30 seconds

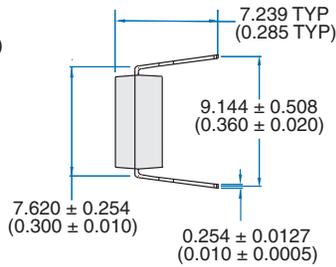
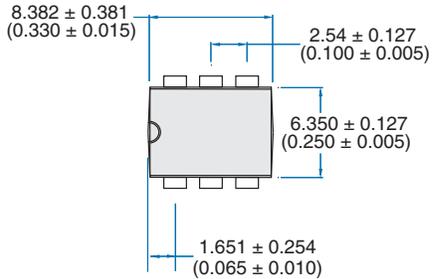
Board Wash

Clare recommends the use of no-clean flux formulations. However, board washing to remove flux residue is acceptable. Since Clare employs the use of silicone coating as an optical waveguide in many of its optically isolated products, the use of a short drying bake may be necessary if a wash is used after solder reflow processes. Chlorine- or Fluorine-based solvents or fluxes should not be used. Cleaning methods that employ ultrasonic energy should not be used.

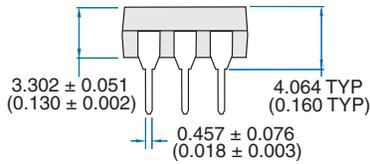
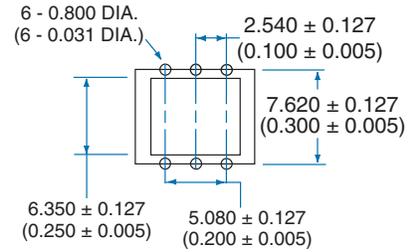


MECHANICAL DIMENSIONS

CPC1972G

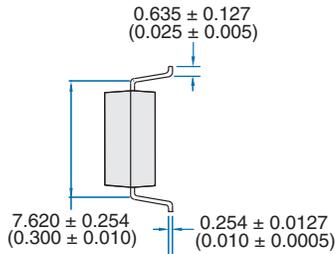
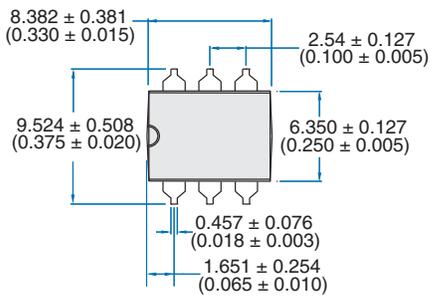


PCB Hole Pattern

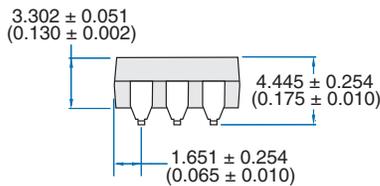
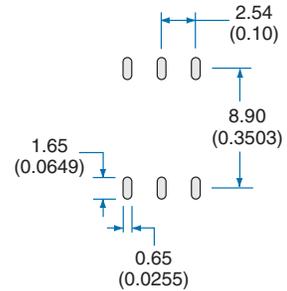


Dimensions
mm
(inches)

CPC1972GS

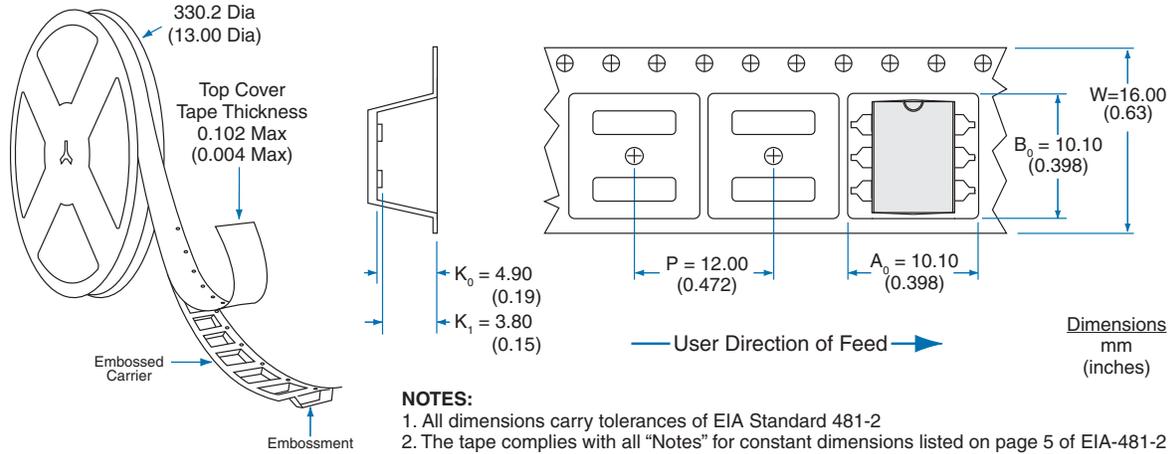


PCB Land Pattern



Dimensions
mm
(inches)

CPC1972GS Tape & Reel



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